

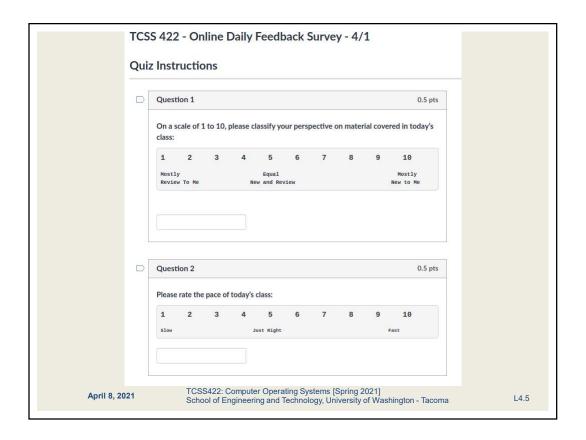
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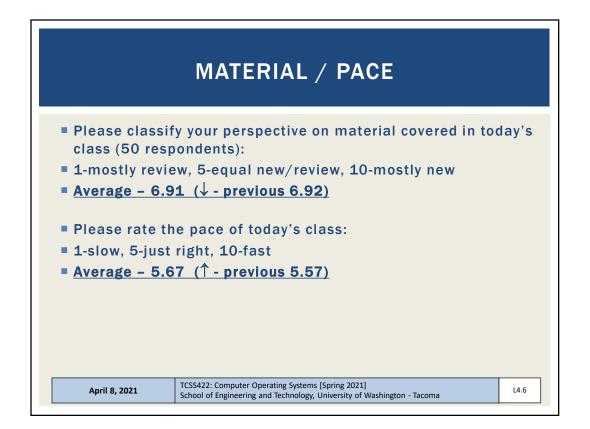
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L4.3

ONLINE DAILY FEEDBACK SURVEY Daily Feedback Quiz in Canvas - Available After Each Class Extra credit available for completing surveys ON TIME ■ Tuesday surveys: due by ~ Wed @ 11:59p ■ Thursday surveys: due ~ Mon @ 11:59p TCSS 422 A > Assignments Spring 2021 Search for Assignment Home Announcements Upcoming Assignments Zoom Syllabus TCSS 422 - Online Daily Feedback Survey - 4/1 Available until Apr 5 at 11:59pm | Due Apr 5 at 10pm | -/1 pts Assignments Ouiz O - C background curve TCSS422: Computer Operating Systems [Spring 2021] April 8, 2021 L4.4 School of Engineering and Technology, University of Washington - Tacoma

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FEEDBACK

- Can we say the number of tasks is the number of processes?

 - Processes are identified as "tasks" in top
- How are memory leaks taken care of when we close a program?
 - When a program is closed, all memory is freed
- Does the OS keep track of what parts of memory were being used by a program even if the program itself dereferences it?
 - Unlike Java, C does not have automatic garbage collection
 - A programmer releases malloc'd memory using the free() function
 - The OS tracks the location of the heap. The data may still reside on the heap but it is no longer referenced. Allocating new variables on the heap may result in finding the old data. The values can be seen if the new variables are not initialized.

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FEEDBACK - 2

- I'm confused about the differences between the READY and BLOCKED process states
- BLOCKED: can not run, waiting on I/O to finish
- READY: is able to run, but not yet scheduled on the CPU

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FEEDBACK - 3

- Can you fork a process multiple times?
 - Yes
- Can a parent have more than one child?
 - Vac
- Can a child have a child and become a parent?
 - Yes
- I saw in a diagram on pg 8 of the lecture 3 slides that you can keep forking but if the child process's PID is 0 then how can you make a new child with a process PID 0 and then differentiate which is the parent?
 - The child can call getPID() to discover its true PID
 - When the child calls fork, it will also receive back its PID

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L4.9

FEEDBACK - 4

- How is prioritization between the parent and child processes done after the call to fork()?
 - The operating system schedules which process goes next
 - If the computer has multiple cores, they may be scheduled to run at the same time
 - The programmer can enforce execution ordering by using the wait() API
- Is there a similar command to fork() that can create a child process without also copying memory, registers, and the program counter?
 - Yes, these are threads, and the API is pthread_create()

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FEEDBACK - 5

- What is overhead?
 - Question for the class...
- (Assignment questions with example) As I researched and understood, we have several command options to find the number of processes (question #1 ex: ""ps"" or ""top"").
- Is it okay to use any commands from these options, or do you expect specific commands which were mentioned from the lecture?
 - Any command can be used

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L4.11

OBJECTIVES - 4/8

- Questions from 4/6
- C Review Survey Closes Friday Apr 9
- Assignment 0
- Chapter 5: Process API
 - fork(), wait(), exec()
- Chapter 6: Limited Direct Execution
 - Direct execution
 - Limited direct execution
 - CPU modes
 - System calls and traps
 - Cooperative multi-tasking
 - Context switching and preemptive multi-tasking

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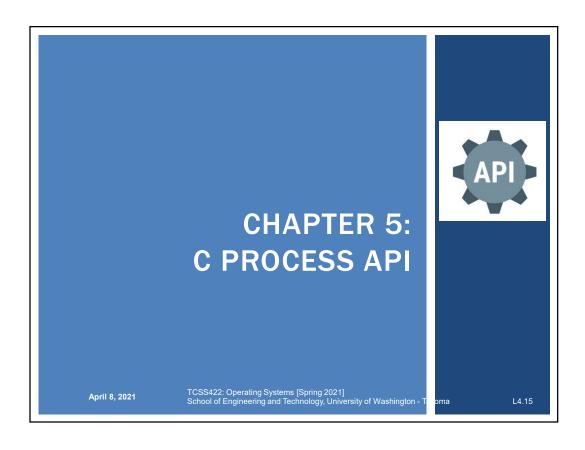
OBJECTIVES - 4/8

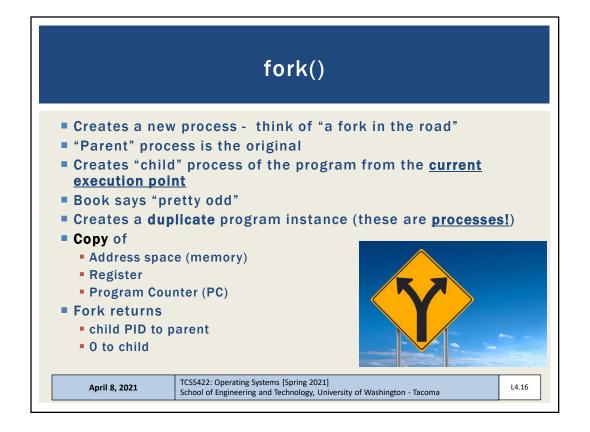
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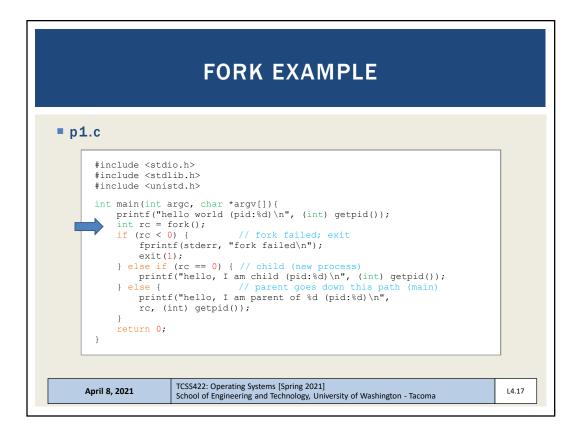
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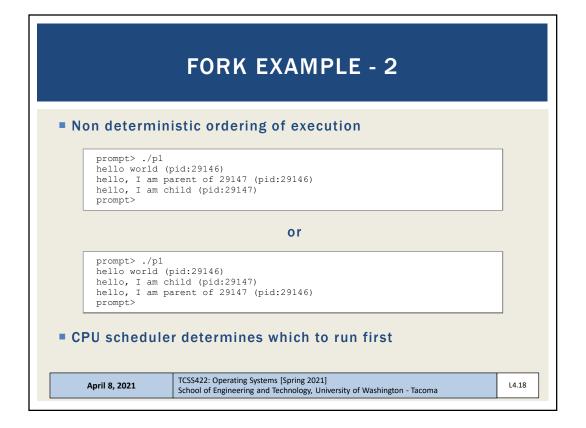
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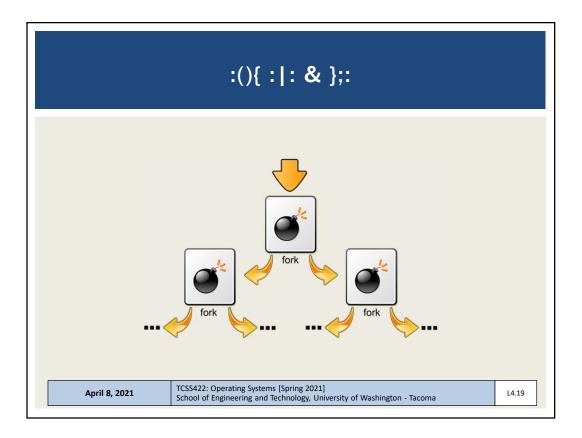
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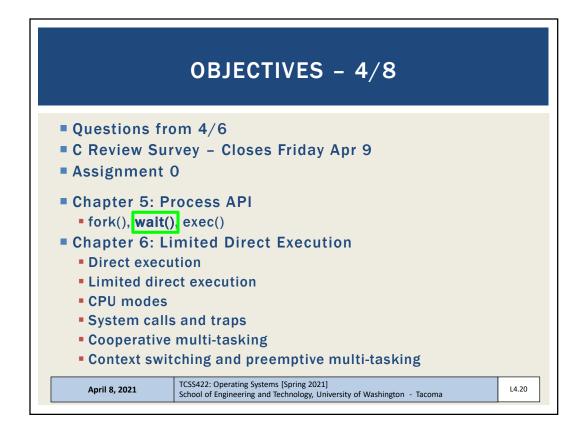












wait()

- wait(), waitpid()
- Called by parent process
- Waits for a child process to finish executing
- Not a sleep() function
- Provides some ordering to multi-process execution



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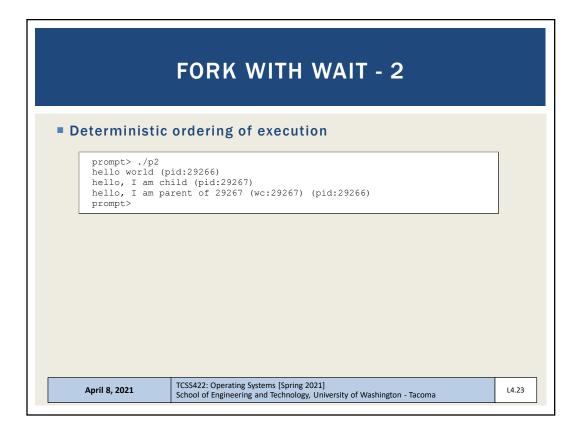
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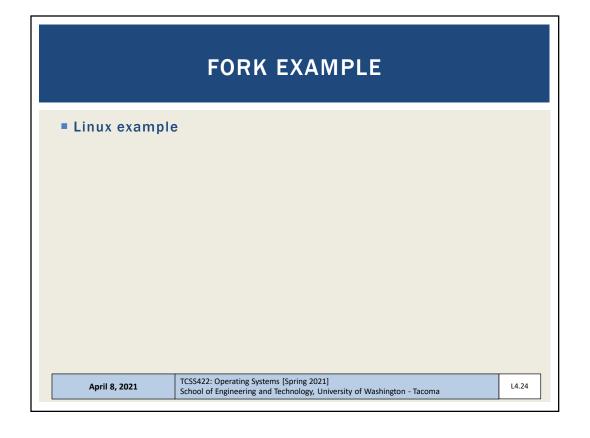
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FORK WITH WAIT

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L4.25

exec()

- Supports running an external program by "transferring control"
- 6 types: execl(), execlp(), execle(), execv(), execvp(), execvpe()
- execl(), execlp(), execle(): const char *arg (example: execl.c)

Provide cmd and args as individual params to the function Each arg is a pointer to a null-terminated string **ODD**: pass a variable number of args: (arg0, arg1, .. argn)

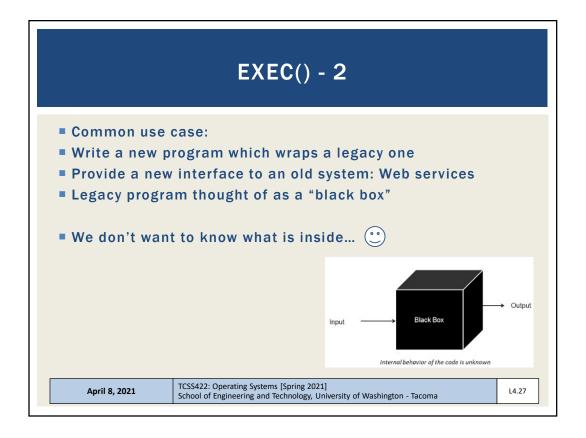
Execv(), execvp(), execvpe() (example: exec.c) Provide cmd and args as an Array of pointers to strings

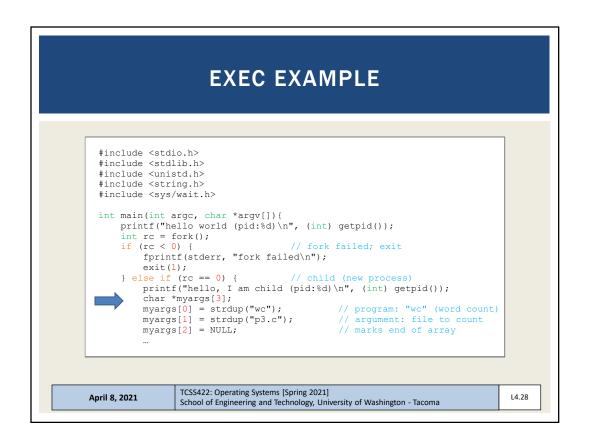
Strings are null-terminated First argument is name of command being executed Fixed number of args passed in

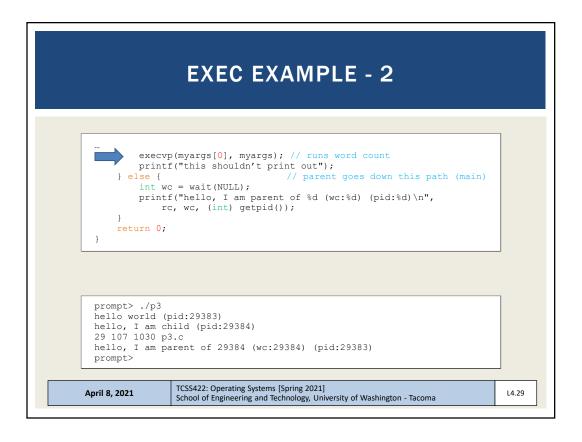
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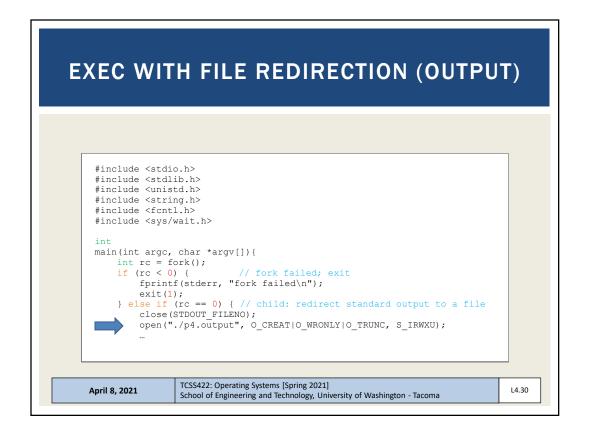
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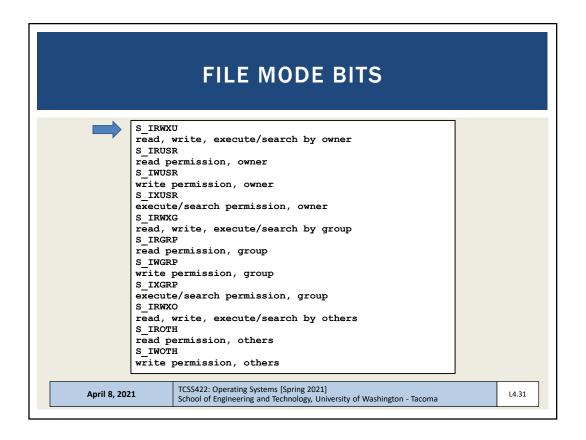
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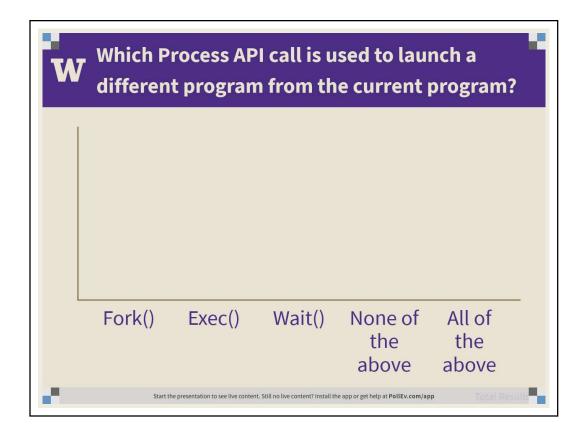


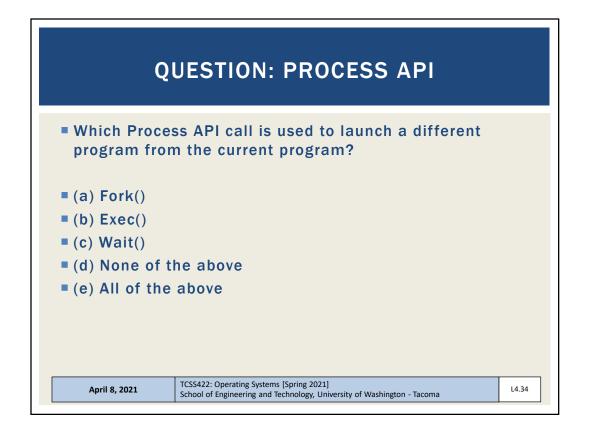






```
EXEC W/ FILE REDIRECTION (OUTPUT) - 2
           // now exec "wc"...
           char *myargs[3];
           execvp(myargs[0], myargs);
                                        // runs word count
                                // parent goes down this path (main)
        } else {
           int wc = wait(NULL);
        return 0;
     prompt> ./p4
     prompt> cat p4.output
     32 109 846 p4.c
     prompt>
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                                                                    L4.32
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```

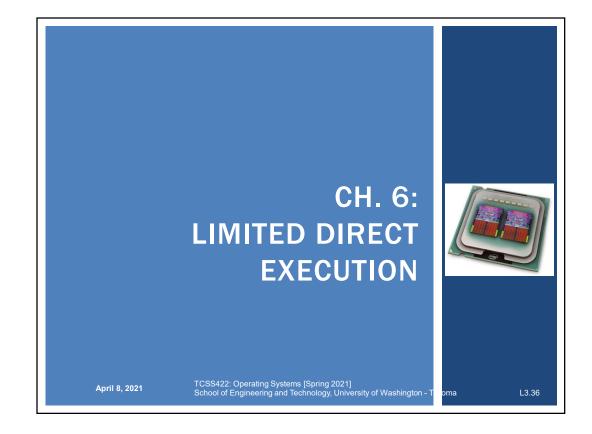




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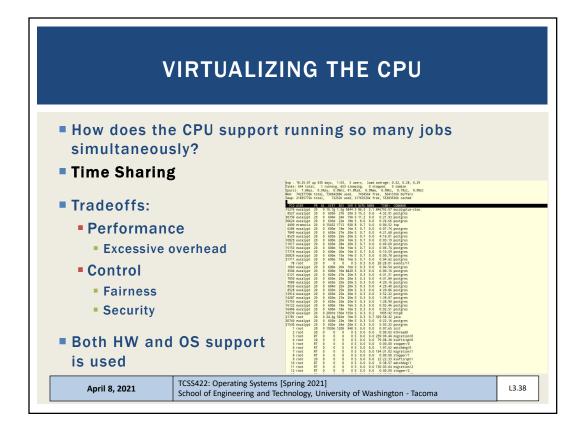
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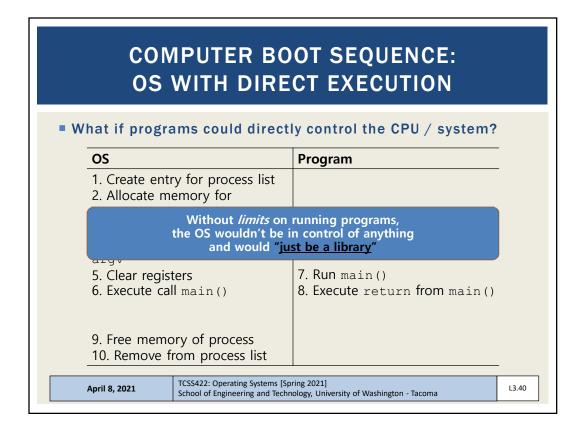
Cooperative multi-tasking

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Context switching and preemptive multi-tasking



COMPUTER BOOT SEQUENCE: OS WITH DIRECT EXECUTION What if programs could directly control the CPU / system? OS **Program** 1. Create entry for process list 2. Allocate memory for program 3. Load program into memory 4. Set up stack with argc / arqv 5. Clear registers 7. Run main() 6. Execute call main() 8. Execute return from main() 9. Free memory of process 10. Remove from process list TCSS422: Operating Systems [Spring 2021] School of Engineering and Technology, University of Washington - Tacoma April 8, 2021 L3.39



DIRECT EXECUTION - 2

■ With direct execution:

How does the OS stop a program from running, and switch to another to support time sharing?

How do programs share disks and perform I/O if they are given direct control? Do they know about each other?

With direct execution, how can dynamic memory structures such as linked lists grow over time?

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L3.41

L3.42

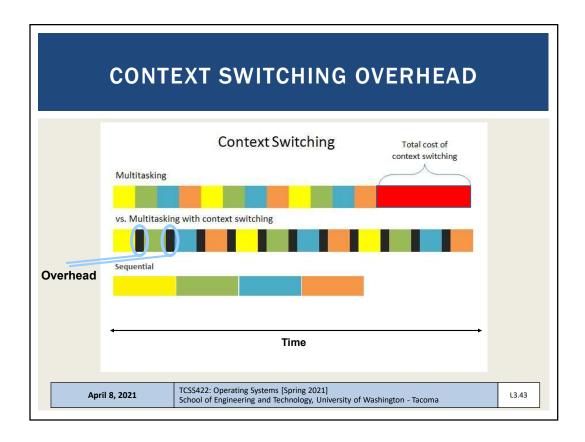
CONTROL TRADEOFF

- Too little control:
 - No security
 - No time sharing
- Too much control:
 - Too much OS overhead
 - Poor performance for compute & I/O
 - Complex APIs (system calls), difficult to use

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LIMITED DIRECT EXECUTION

- OS implements LDE to support time/resource sharing
- Limited direct execution means "only limited" processes can execute DIRECTLY on the CPU in trusted mode
- TRUSTED means the process is trusted, and it can do anything... (e.g. it is a system / kernel level process)
- Enabled by protected (safe) control transfer
- CPU supported context switch
- Provides data isolation

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L4.47

L4.48

CPU MODES

- Utilize CPU Privilege Rings (Intel x86)
 - rings 0 (kernel), 1 (VM kernel), 2 (unused), 3 (user)

access < no access

User mode:

Application is running, but w/o direct I/O access

Kernel mode:

OS kernel is running performing restricted operations

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CPU MODES

- <u>User mode: ring 3 untrusted</u>
 - Some instructions and registers are disabled by the CPU
 - Exception registers
 - HALT instruction
 - MMU instructions
 - OS memory access
 - I/O device access
- Kernel mode: ring 0 trusted
 - All instructions and registers enabled

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SYSTEM CALLS

- Implement restricted "OS" operations
- Kernel exposes key functions through an API:
 - Device I/O (e.g. file I/O)
 - Task swapping: context switching between processes
 - Memory management/allocation: malloc()
 - Creating/destroying processes

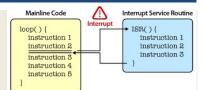
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TRAPS: SYSTEM CALLS, EXCEPTIONS, INTERRUPTS

Trap: any transfer to kernel mode



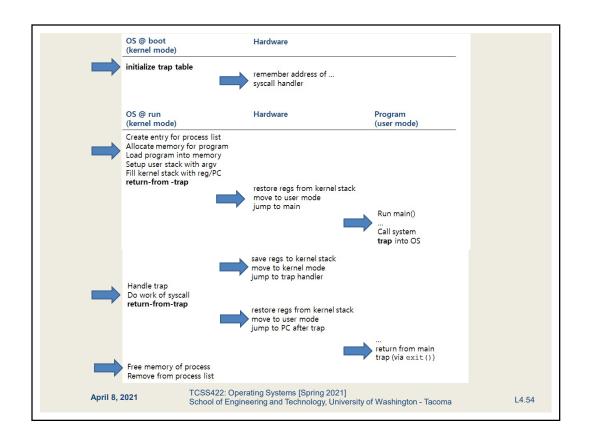
- Three kinds of traps
 - System call: (planned) user → kernel
 - SYSCALL for I/O, etc.
 - Exception: (error) user → kernel
 - Div by zero, page fault, page protection error
 - Interrupt: (event) user → kernel
 - Non-maskable vs. maskable
 - Keyboard event, network packet arrival, timer ticks
 - Memory parity error (ECC), hard drive failure

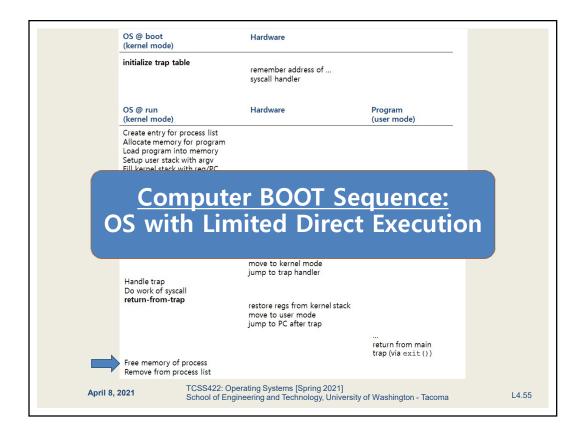
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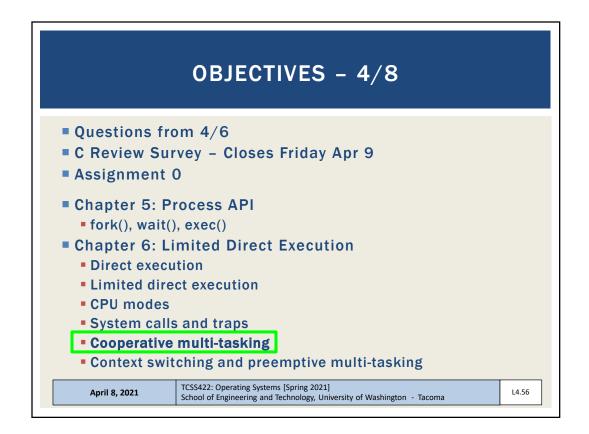
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EXCEPTION TYPES					
/O device request	Asynchronous	Coerced	Nonmaskable	Between	Resume
invoke operating system	Synchronous	User request	Nonmaskable	Between	Resume
Tracing instruction execution	Synchronous	User request	User maskable	Between	Resume
Breakpoint	Synchronous	User request	User maskable	Between	Resume
integer arithmetic overflow	Synchronous	Coerced	User maskable	Within	Resume
Floating-point arithmetic overflow or underflow	Synchronous	Coerced	User maskable	Within	Resume
Page fault	Synchronous	Coerced	Nonmaskable	Within	Resume
Misaligned memory accesses	Synchronous	Coerced	User maskable	Within	Resume
Memory protection violation	Synchronous	Coerced	Nonmaskable	Within	Resume
Using undefined instruction	Synchronous	Coerced	Nonmaskable	Within	Terminate
Hardware malfunction	Asynchronous	Coerced	Nonmaskable	Within	Terminate
Power fallure	Asynchronous	Coerced	Nonmaskable	Within	Terminate







MULTITASKING

- How/when should the OS regain control of the CPU to switch between processes?
- Cooperative multitasking (mostly pre 32-bit)
 - < Windows 95, Mac OSX</p>
 - Opportunistic: running programs must give up control
 - User programs must call a special yield system call
 - When performing I/O
 - Illegal operations
 - (POLLEV) What problems could you for see with this approach?

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MULTITASKING

- How/when should the OS regain control of the CPU to switch between processes?
- Cooperative multitacking (mostly pro 32 hit)
 - A process gets stuck in an infinite loop.
 - Op → Reboot the machine
 - Wnen performing 1/ 0
 - Illegal operations
 - POLLEV)

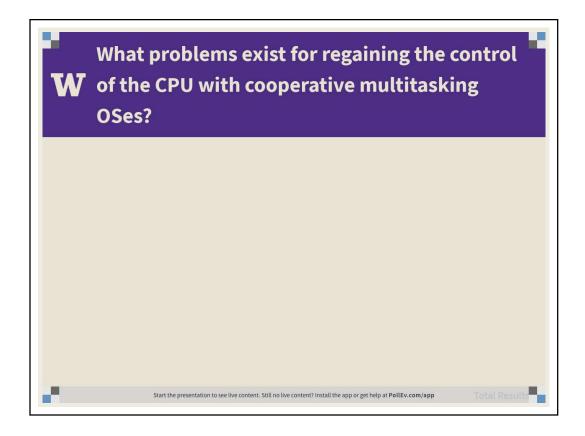
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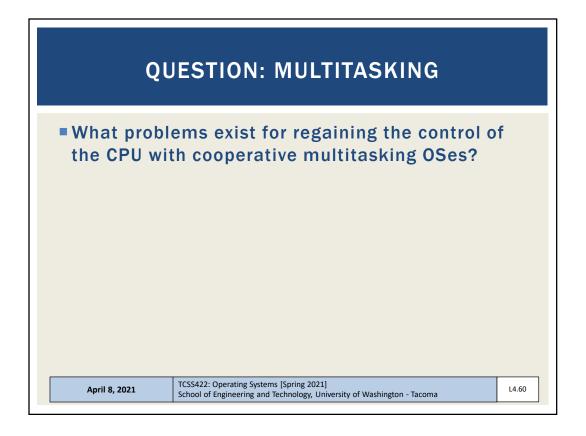
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MULTITASKING - 2

- Preemptive multitasking (32 & 64 bit OSes)
- >= Mac OSX, Windows 95+
- **■** Timer interrupt
 - Raised at some regular interval (in ms)
 - Interrupt handling
 - 1. Current program is halted
 - 2. Program states are saved
 - 3. OS Interrupt handler is run (kernel mode)
- (PollEV) What is a good interval for the timer interrupt?

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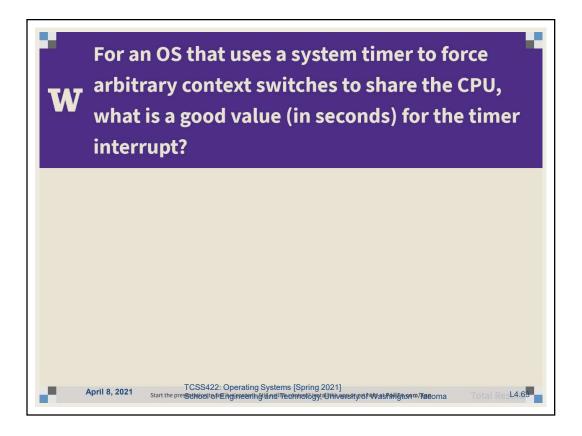
MULTITASKING - 2

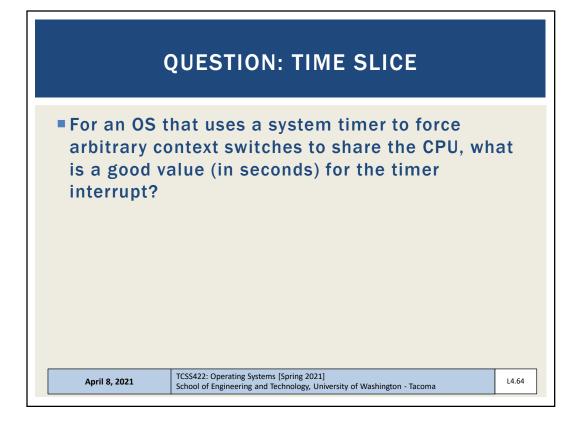
- Preemptive multitasking (32 & 64 bit OSes)
- ► >= Mac OSX, Windows 95+
- Timer
 - Rais A timer interrupt gives OS the ability to run again on a CPU.
 - Inter
 - 1. Current program is halted
 - 2. Program states are saved
 - 3. OS Interrupt handler is run (kernel mode)
- (PollEV) What is a good interval for the timer interrupt?

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CONTEXT SWITCH

- Preemptive multitasking initiates "trap" into the OS code to determine:
- Whether to continue running the current process, or switch to a different one.
- If the decision is made to switch, the OS performs a <u>context</u> <u>switch</u> swapping out the current process for a new one.

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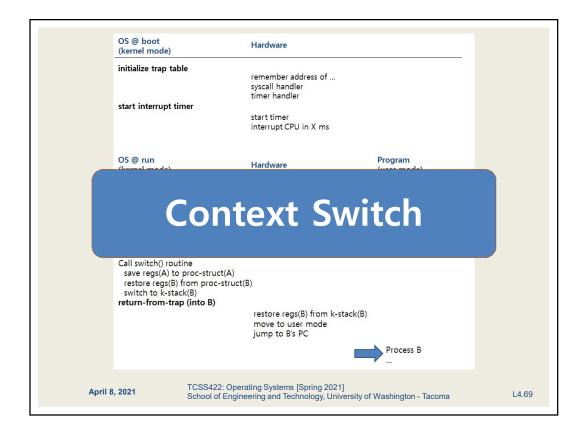
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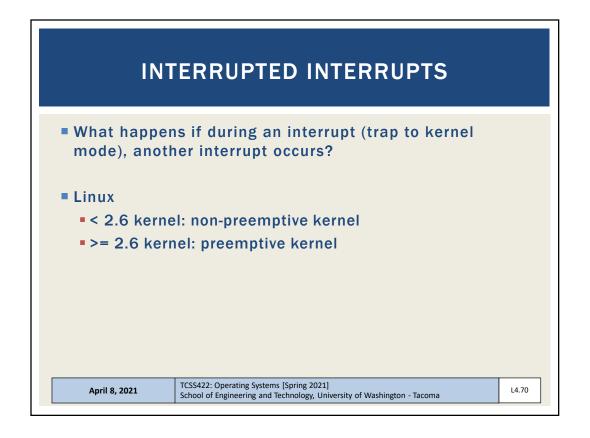
CONTEXT SWITCH - 2

- 1. Save register values of the current process to its kernel stack
 - General purpose registers
 - PC: program counter (instruction pointer)
 - kernel stack pointer
- 2. Restore soon-to-be-executing process from its kernel stack
- 3. Switch to the kernel stack for the soon-to-be-executing process

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OS @ boot Hardware (kernel mode) initialize trap table remember address of ... syscall handler timer handler start interrupt timer start timer interrupt CPU in X ms OS @ run Hardware (kernel mode) (user mode) Process A timer interrupt save regs(A) to k-stack(A) move to kernel mode jump to trap handler Handle the trap Call switch() routine save regs(A) to proc-struct(A) restore regs(B) from proc-struct(B) switch to k-stack(B) return-from-trap (into B) restore regs(B) from k-stack(B) move to user mode jump to B's PC Process B TCSS422: Operating Systems [Spring 2021] School of Engineering and Technology, University of Washington - Tacoma April 8, 2021 L4.68





PREEMPTIVE KERNEL

- Use "locks" as markers of regions of nonpreemptibility (non-maskable interrupt)
- Preemption counter (preempt_count)
 - begins at zero
 - increments for each lock acquired (not safe to preempt)
 - decrements when locks are released
- Interrupt can be interrupted when preempt_count=0
 - It is safe to preempt (maskable interrupt)
 - the interrupt is more important

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CHAPTER 7-SCHEDULING: **INTRODUCTION** TCSS422: Operating Systems [Spring 2021] School of Engineering and Technology, University of Washington -April 8, 2021

OBJECTIVES - 4/8

- Chapter 7: Scheduling Introduction
 - Scheduling metrics
 - Turnaround time, Jain's Fairness Index, Response time
 - FIFO, SJF, STCF, RR schedulers

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SCHEDULING METRICS

- Metrics: A standard measure to quantify to what degree a system possesses some property. Metrics provide <u>repeatable</u> techniques to quantify and compare systems.
- Measurements are the numbers derived from the application of metrics
- Scheduling Metric #1: Turnaround time
- The time at which the job completes minus the time at which the job arrived in the system

 $T_{turnaround} = T_{completion} - T_{arrival}$

How is turnaround time different than execution time?

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SCHEDULING METRICS - 2

- Scheduling Metric #2: Fairness
 - Jain's fairness index
 - Quantifies if jobs receive a fair share of system resources

$$\mathcal{J}(x_1,x_2,\ldots,x_n) = rac{(\sum_{i=1}^n x_i)^2}{n \cdot \sum_{i=1}^n x_i{}^2}$$

- n processes
- x_i is time share of each process
- worst case = 1/n
- best case = 1
- Consider n=3, worst case = .333, best case=1
- With n=3 and x_1 =.2, x_2 =.7, x_3 =.1, fairness=.62
- With n=3 and x_1 =.33, x_2 =.33, x_3 =.33, fairness=1

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OBJECTIVES - 4/8

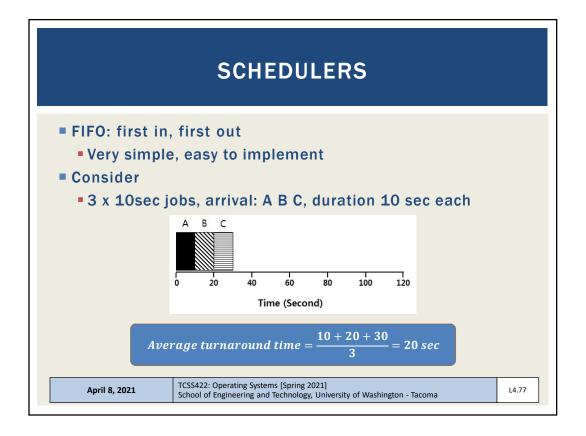
- Chapter 7: Scheduling Introduction
 - Scheduling metrics
 - Turnaround time, Jain's Fairness Index, Response time
 - FIFO SJF, STCF, RR schedulers

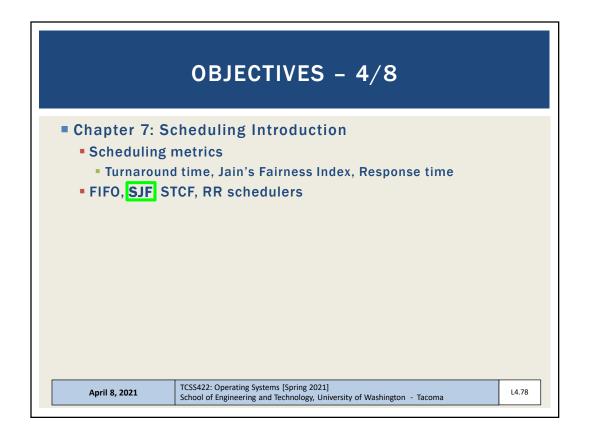
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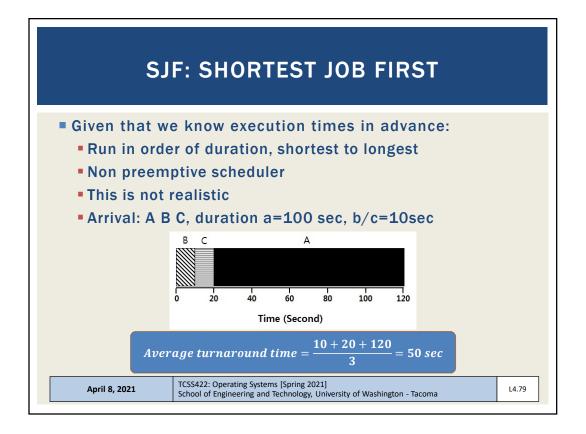
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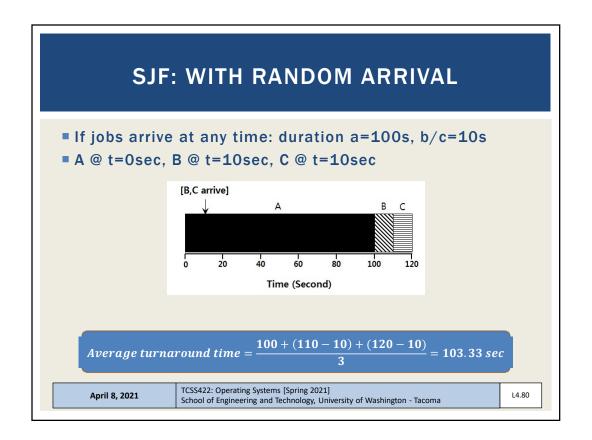
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L4.76

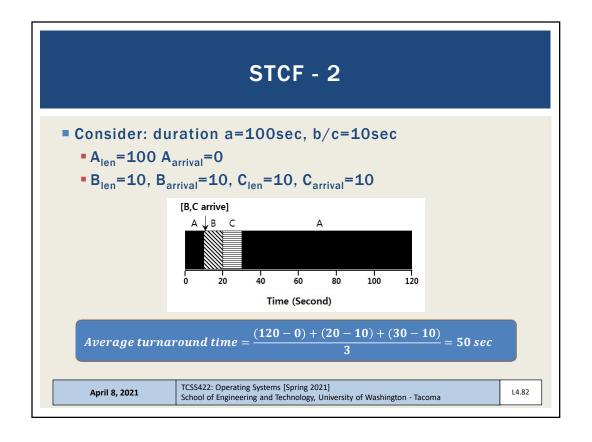








OBJECTIVES - 4/8 Chapter 7: Scheduling Introduction Scheduling metrics Turnaround time, Jain's Fairness Index, Response time FIFO, SJF, STCF RR schedulers April 8, 2021 TCSS422: Operating Systems [Spring 2021] School of Engineering and Technology, University of Washington - Tacoma



SCHEDULING METRICS - 3

- Scheduling Metric #3: Response Time
- Time from when job arrives until it starts execution

$$T_{response} = T_{firstrun} - T_{arrival}$$

- STCF, SJF, FIFO
 - can perform poorly with respect to response time

What scheduling algorithm(s) can help minimize response time?

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L4.83

OBJECTIVES - 4/8

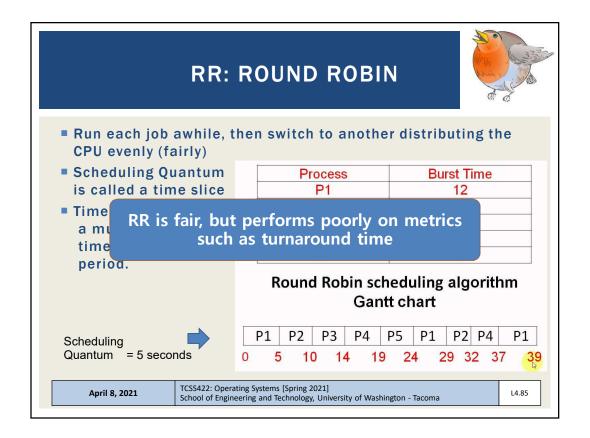
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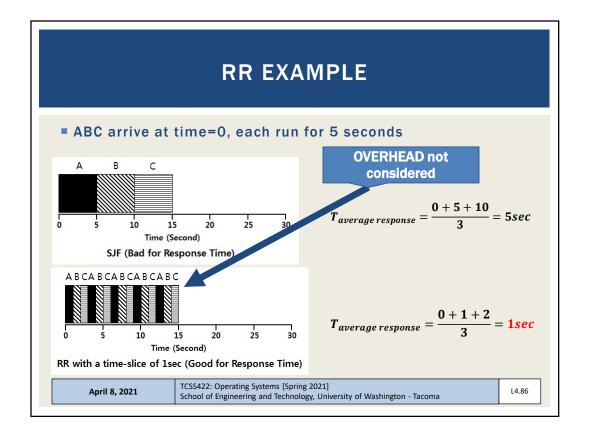
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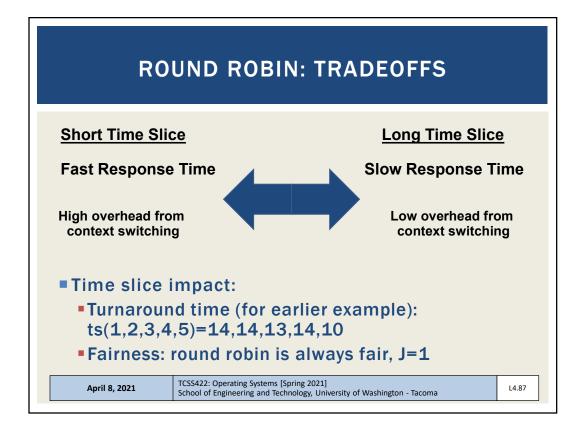
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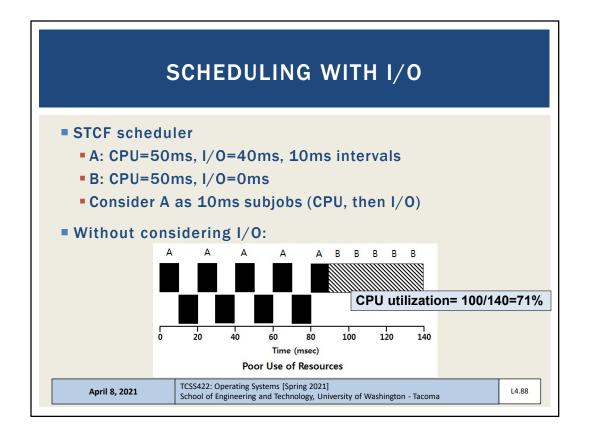
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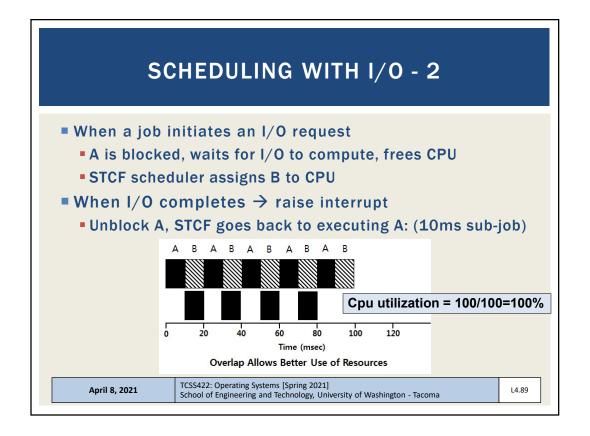
L4.84

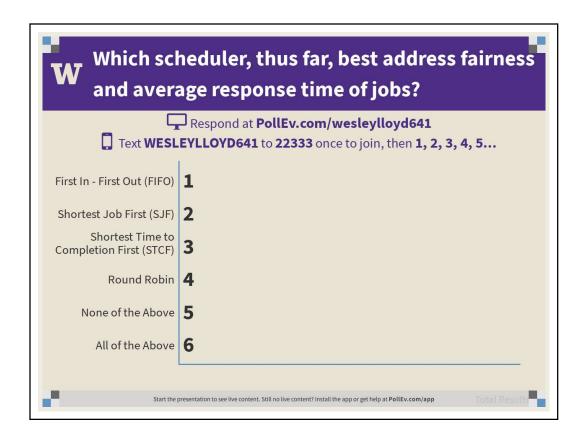












QUESTION: SCHEDULING FAIRNESS

- Which scheduler, this far, best addresses fairness and average response time of jobs?
- First In First Out (FIFO)
- Shortest Job First (SJF)
- Shortest Time to Completion First (STCF)
- Round Robin (RR)
- None of the Above
- All of the Above

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L4.91

L4.92

SCHEDULING METRICS

- Consider Three jobs (A, B, C) that require: $time_{\Delta}$ =400ms, $time_{R}$ =100ms, and $time_{C}$ =200ms
- All jobs arrive at time=0 in the sequence of A B C.
- Draw a scheduling graph to help compute the average response time (ART) and average turnaround time (ATT) scheduling metrics for the FIFO scheduler.

Example:



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