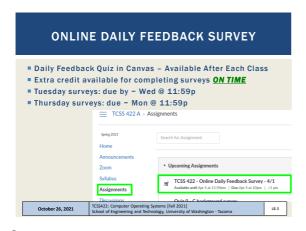


**OBJECTIVES - 10/26**  Questions from 10/21
 C Tutorial - Pointers, Strings, Exec in C - Due Fri Oct 29
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 Chapter 29: Lock Based Data Structures Concurrent Structures: Linked List, Queue, Hash Table

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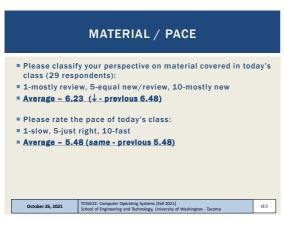


TCSS 422 - Online Daily Feedback Survey - 4/1 Quiz Instructions TCSS422: Computer Operating Systems [Fall 2021] School of Engineering and Technology, University of Washington - Tacoma

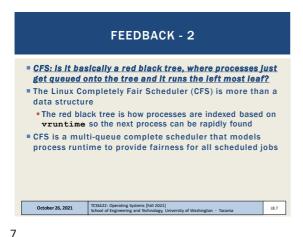
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3



**FEEDBACK** Why does the final (value of the) counter fluctuate so much? When two threads count up and each increments the same variable, if the count is low (e.g. < 5000) then each thread is so FAST that it often completes the full count before a <u>context switch</u> • For larger counts, the threads will have to context switch due to the OS timer interrupt that restricts jobs from running longer than their allowed time slice • A <u>race condition</u> occurs when two threads race to update a shared variable at roughly the same time (\* - introduced today) • The threads "race" to see which thread can write the value last to the shared variable - this is the winner For programs to be <u>synchronized</u>, all thread updates (to shared variables) must be SAVED October 26, 2021 L8.6



FEEDBACK - 3

We've seen Linux CFS, but what do other OSes use as their CPU scheduler? How are they better/worse than the CFS?

All distros of Linux now generally used CFS

Many other Oses may be closed source, so information regarding their process/thread scheduling may be limited

Windows 10

Some suggest MLFQ

Windows uses priority-based preemptive scheduling where the highest-priority thread runs next

https://www.andrew.cmu.edu/course/14-712-s20/applications/ln/14712-16.pdf (see slide 5.60)

8

10

FEEDBACK - 4

MAC OS X CPU Scheduler discussed in 2013 book:

http://newosxbook.com/MOXil.pdf (see Chapter 11)

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FEEDBACK - 5

Should we always avoid parallel programming? Or should we avoid parallel programming only in the context of concurrency?
You should never avoid parallel programming ... = )
But parallel programming that does not involve sharing memory can be far more painless

1.8.10

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9

BONUS SESSION EXAMPLE SCHEDULER PROBLEMS

Bonus session: Wednesday October 27 starting at 6:30pm
Approximately ~1 hour

Will solve a series of example scheduling problems
Focus on: FIFO, SJF, STCF, RR, MLFQ

Video will be live-streamed and recorded and posted

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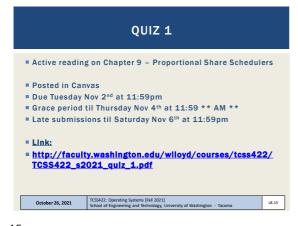
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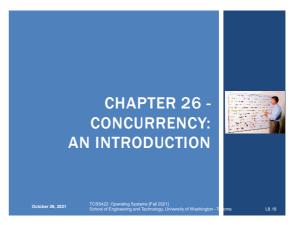
QUIZ 2 - CPU SCHEDULING ALGORITHMS

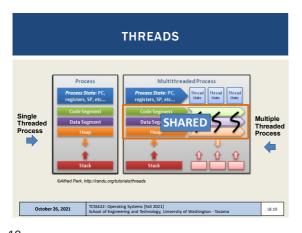
Quiz posted on Canvas
Due Thursday Nov 4 @ 11:59p
Provides CPU scheduling practice problems
FIFO, SJF, STCF, RR, MLFQ (Ch. 7 & 8)
Unlimited attempts allowed
Multiple choice and fill-in the blank
Quiz automatically scored by Canvas
Please report any grading problems

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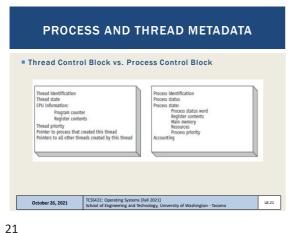






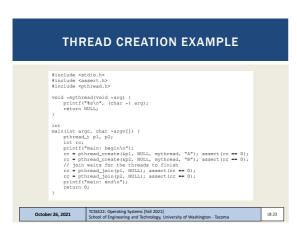
**THREADS - 2** ■ Enables a single process (program) to have multiple "workers" • This is parallel programming... Supports independent path(s) of execution within a program with shared memory ... ■ Each thread has its own Thread Control Block (TCB) PC, registers, SP, and stack ■ Threads share code segment, memory, and heap are shared What is an embarrassingly parallel program? October 26, 2021 L8.20

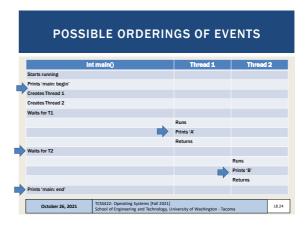
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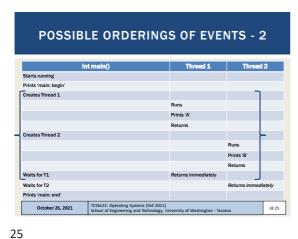


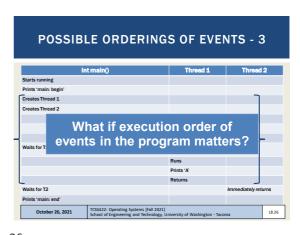
SHARED ADDRESS SPACE Every thread has it's own stack / PC OKB The code segment: where instructions live Program Code Program Code 1KB 1KB Heap Heap 2KB 2KB Stack (2) 15KB 15KB Stack (1) Stack (1) 16KB 16KB A Single-Threaded Address Space Two threaded October 26, 2021 L8.22

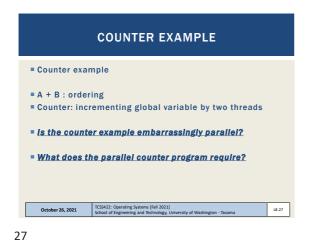
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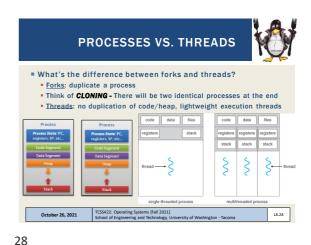










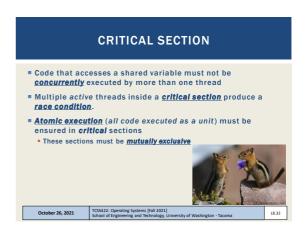


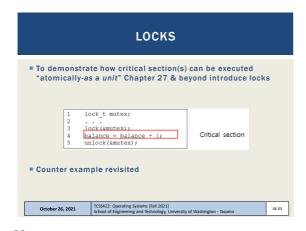


**RACE CONDITION** ■ What is happening with our counter? • When counter=50, consider code: counter = counter + 1 If synchronized, counter will = 52 (after instruction) PC %eax counter Thread1 before critical section mov 0x8049alc, %eax add \$0x1, %eax mov 0x8049alc, %eax add \$0x1, %eax mov %eax, 0x8049alc restore T1's state mov %eax, 0x8049alc 108 113

29 30

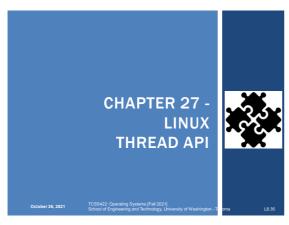




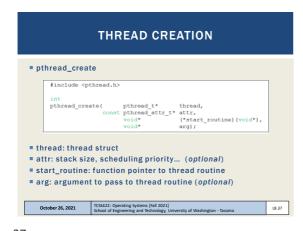




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37 38

```
Using this approach on your Ubuntu VM,
How large (in bytes) can the primitive data type be?

Printit (NATH , M);

How large (in bytes) can the primitive data type be on a 32-bit operating system?

In printing join (p, (void **) &m);
printit ("returned %d\n", m);
printit ("returned %d\n", m);
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```

waiting for threads to finish

int pthread\_join(pthread\_t thread, void \*\*value\_ptr);

= thread: which thread?

= value\_ptr: pointer to return value type is dynamic / agnostic

= Returned values \*must\* be on the heap

= Thread stacks destroyed upon thread termination (join)

= Pointers to thread stack memory addresses are invalid

- May appear as gibberish or lead to crash (seg fault)

= Not all threads join - What would be Examples ??

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39

```
struct myarg {
    int a;
    int b;
};

What will this code do?

};

void *worker(void *arg)
{
    struct myarg *input = (struct myarg *) arg;
    printf("a-%d b-%d\n".input->a, input->b);
    struct myarg output;
    output.a = 1;
    output.b = 2;
    return (void *) &output;
}

int main (int argc, char * argv[])
{
    pthread_t p1;
    struct myarg args;
    struct myarg args;
    args.a = 10;
    args.b = 20;
    pthread_pthread_printf("
    args.b = 20;
    pthread_printf("
    return 0)
}

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```

```
struct myarg {
    int a;
    int b;
};

void *worker(void *arg)
{
    struct myarg *input = (struct myarg *) arg;
    printf("a=%d b=%d\n", input->a, input->b);
    input->b = 2;
    return (void *) &input;
};

int main (int argc, char * argv[])
{
    pthread_t pl;
    struct myarg arge;
    struct myarg aret_args;
    args a n0;
    args b = 20;
    pthread_create(&pl, NULL, worker, &args);
    pthread_create(&pl, NULL, worker, &args);
    printf("returned %d %d\n", ret_args->a, ret_args->b);
    return 0;
}

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```

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43 44

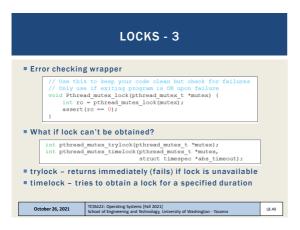
```
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Questions from 10/21
C Tutorial - Pointers, Strings, Exec in C - Due Fri Oct 29
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Race condition
Critical section
Chapter 27: Linux Thread API
pthread_create/_join
pthread_create/_join
pthread_cond_wait/_signal/_broadcast
Chapter 28: Locks
Introduction, Lock Granularity
Spin Locks, Test and Set, Compare and Swap
Chapter 29: Lock Based Data Structures
Sloppy Counter
Concurrent Structures: Linked List, Queue, Hash Table
```

45 46

| Assigning the constant | pthread\_mutex\_t lock = PTHREAD\_MUTEX\_INITIALIZER; |
| API call: | int rc = pthread\_mutex\_init(6lock, NULL); | assert(rc == 0); // always check success! |
| Initializes mutex with attributes specified by 2nd argument |
| If NULL, then default attributes are used |
| Upon initialization, the mutex is initialized and unlocked |
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 Race condition Critical section Chapter 27: Linux Thread API pthread\_create/\_joinpthread\_mutex\_lock/\_unlock/\_trylock/\_timelock pthread\_cond\_wait/\_signal/\_broadcastChapter 28: Locks Introduction, Lock Granularity Spin Locks, Test and Set, Compare and Swap
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Chapter 26: Concurrency: An Introduction

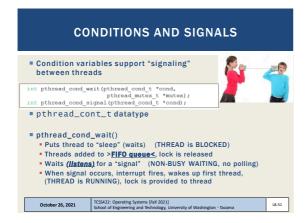
Questions from 10/21

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52

**OBJECTIVES - 10/26** 

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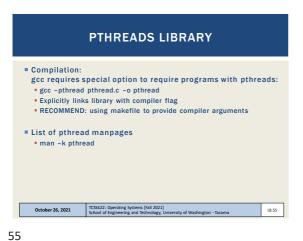


**CONDITIONS AND SIGNALS - 2** int pthread cond signal(pthread cond t \* cond): int pthread\_cond\_broadcast(pthread\_cond\_t \* cond); pthread\_cond\_signal() - Called to send a "signal" to wake-up first thread in FIFO "walt" queue The goal is to unblock a thread to respond to the signal pthread\_cond\_broadcast() Unblocks all threads in FIFO "walt" queue, currently blocked on the specified condition variable Broadcast is used when all threads should wake-up for the signal Which thread is unblocked first? Determined by OS scheduler (based on priority) Thread(s) awoken based on placement order in FIFO wait queue When awoken threads acquire lock as in pthread\_mutex\_lock() TCSS422: Operating Systems [Fall 2021] School of Engineering and Technology, University of Washington - Tacoma October 26, 2021 L8.52

51

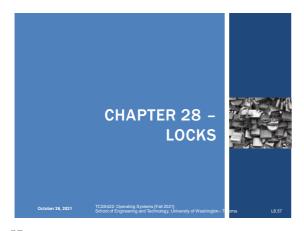
```
CONDITIONS AND SIGNALS - 3
■ Wait example:
         pthread_mutex_t lock = PTHREAD_MUTEX_INITIALIZER;
pthread_cond_t cond = PTHREAD_COND_INITIALIZER;
          pthread mutex lock(&lock);
         while (initialized == 0)
pthread_cond_wait(&cond, &lock);
// Perform work that requires lock
         a = a + b;
pthread mutex unlock(&lock);
wait puts thread to sleep, releases lock
when awoken, lock reacquired (but then released by this code)
                                                                   State variable set,
Enables other thread(s)
When initialized, another thread signals
           pthread_mutex_lock(&lock);
initialized = 1;
                                                                      to proceed above
      pthread_cond_signal(&init);
pthread_mutex_unlock(&lock)
   October 26, 2021 TCSS422: Operating Systems [Fall 2021]
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                                                                                              L8.53
```

**CONDITION AND SIGNALS - 4** pthread mutex t lock = PTHREAD MUTEX INITIALIZER; pthread\_cond\_t cond = PTHREAD\_COND\_INITIALIZER; pthread mutex lock(&lock);
while (initialized == 0) pthread cond wait(&cond, &lock);
// Perform work that requires lock = a + b; pthread\_mutex\_unlock(&lock); Why do we wait inside a while loop? The while ensures upon awakening the condition is rechecked A signal is raised, but the pre-conditions required to proceed may have not been met. \*\*MUST CHECK STATE VARIABLE\*\* Without checking the state variable the thread may proceed to execute when it should not. (e.g. too early) October 26, 2021 TCSS422: Operating Systems [Fall 2021]
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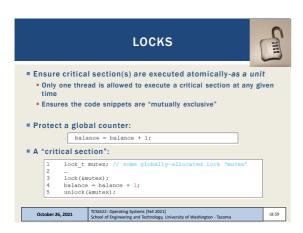
**SAMPLE MAKEFILE** CC=gcc CFLAGS=-pthread -I. -Wall binaries=pthread pthread\_int pthread\_lock\_cond pthread\_struct pthread\_mult: pthread.c pthread\_int.c
\$(CC) \$(CFLAGS) \$^ -o \$@ clean: \$(RM) -f \$(binaries) \*.o Example builds multiple single file programs pthread\_mult Example if multiple source files should produce a single executable clean target October 26, 2021

56



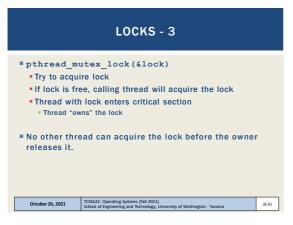
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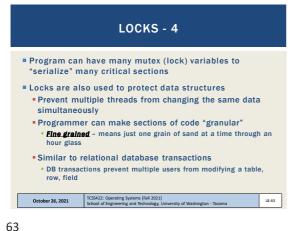
LOCKS - 2 ■ Lock variables are called "MUTEX" Short for mutual exclusion (that's what they guarantee) Lock variables store the state of the lock States Locked (acquired or held) Unlocked (available or free) Only 1 thread can hold a lock October 26, 2021 L8.60

59 60



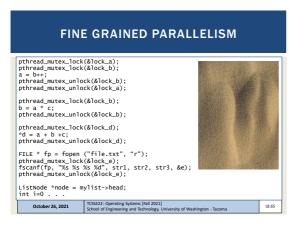
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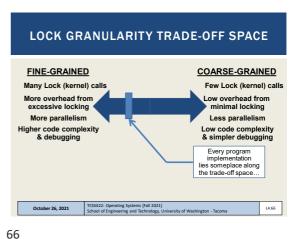
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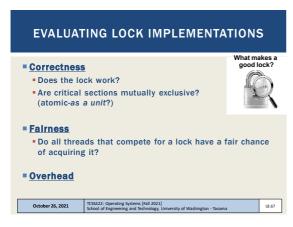


**FINE GRAINED?** Is this code a good example of "fine grained parallelism"? pthread mutex lock(&lock): ptnread\_muteX\_lock(&lock); a = b++; b = a \* c; \*d = a + b +c; FILE \* fp = fopen ("file.txt", "r"); fscanf(fp, "%s %s %s %d", str1, str2, str3, &e); ListNode \*node = mylist->head; Int i=0 Int i=0
while (node) {
 node->title = str1;
 node->subheading = str2;
 node->desc = str3;
 node->end = \*e;
 node = node->next;
 i++ e = e - i; pthread\_mutex\_unlock(&lock); October 26, 2021 L8.64

64







■ Locks require hardware support
■ To minimize overhead, ensure fairness and correctness
■ Special "atomic-as a unit" instructions to support lock implementation
■ Atomic-as a unit exchange instruction
■ XCHG
■ Compare and exchange instruction
■ CMPXCHG
■ CMPXCHGBB
■ CMPXCHG16B

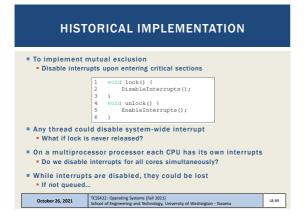
CMPXCHG16B

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Spin Locks Test and Set, Compare and Swap
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69

DIY: CORRECT?

# Correctness requires luck... (e.g. DIY lock is incorrect)

Thread1 Thread2

call lock()
while (flag == 1)
interrupt switch to Thread 2

call lock()
while (flag == 1)
flag = 1; // set flag to 1 (tool)

# Here both threads have "acquired" the lock simultaneously

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Assignment 1 - Due Fri Nov 12
Quiz 1 (Due Tue Nov 2) - Quiz 2 (Due Thur Nov 4)
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Critical section
Chapter 27: Linux Thread API
pthread\_create/\_join
pthread\_create/\_

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```
SPIN LOCK EVALUATION

Correctness:
Spin locks with atomic Test-and-Set:
Critical sections won't be executed simultaneously by (2) threads

Fairness:
No fairness guarantee. Once a thread has a lock, nothing forces it to relinquish it...

Performance:
Spin locks perform "busy waiting"
Spin locks are best for short periods of waiting (< 1 time quantum)
Performance is slow when multiple threads share a CPU
Especially if "spinning" for long periods

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Chapter 26: Concurrency: An Introduction

\*\*Race condition

Critical section

Chapter 27: Linux Thread API

\*\*phread\_create/\_join

pthread\_cnd\_wait/\_signal/\_broadcast

Chapter 28: Lock

Introduction, Lock Granularity

Spin Locks, Test and Set, Compare and Swap

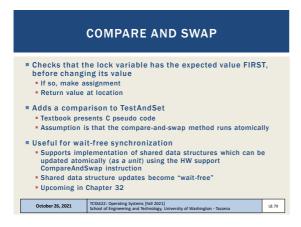
Chapter 29: Lock Based Data Structures

\*\*Sloppy Counter

Concurrent Structures: Linked List, Queue, Hash Table

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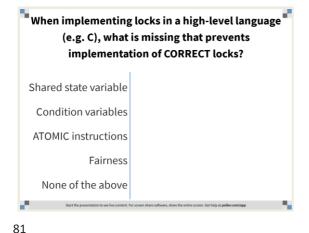


COMPARE AND SWAP

Compare and Swap

int CompareAndswap (int \*ptr, int expected, int new) {
 int actual = \*ptr /
 int (actual = \*ptr)
 if (actual = \*expected)
 if (actual =

79 80



TWO MORE "LOCK BUILDING"
CPU INSTRUCTIONS

CPU INSTRUCTIONS

COOperative instructions used together to support synchronization on RISC systems

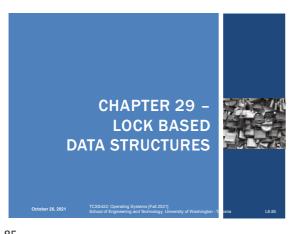
No support on x86 processors
Supported by RISC: Alpha, PowerPC, ARM

Load-linked (LL)
Loads value into register
Same as typical load
Used as a mechanism to track competition

Store-conditional (SC)
Performs "mutually exclusive" store
Allows only one thread to store value

82

83





## LOCK-BASED CONCURRENT DATA STRUCTURES Adding locks to data structures make them thread safe. Considerations: Correctness Performance Lock granularity TCSS422: Operating Systems [Fall 2021] School of Engineering and Technology, University of Washington - Tacoma

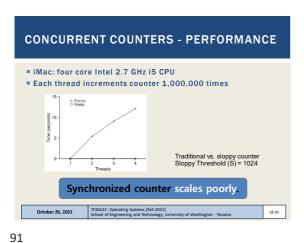
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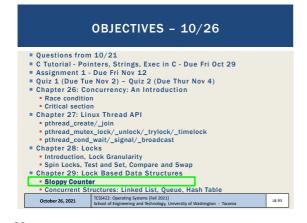
Slides by Wes J. Lloyd

L8.15



**PERFECT SCALING** Achieve (N) performance gain with (N) additional resources ■ Throughput: ■ Transactions per second (tps) ■ 1 core ■ N = 100 tps ■ 10 cores (x10)■ N = 1000 tps (x10)October 26, 2021 L8.92

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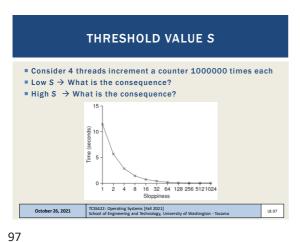
**SLOPPY COUNTER** Provides single logical shared counter Implemented using local counters for each ~CPU core 4 CPU cores = 4 local counters & 1 global counter Local counters are synchronized via local locks Global counter is updated periodically Global counter has lock to protect global counter value Sloppiness threshold (S): Update threshold of global counter with local values - Small (S): more updates, more overhead Large (S): fewer updates, more performant, less synchronized ■ Why this implementation? Why do we want counters local to each CPU Core? October 26, 2021 L8.94

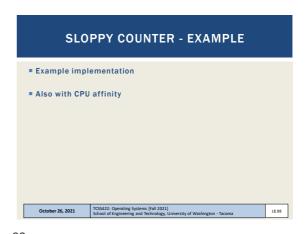
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SLOPPY COUNTER - MAIN POINTS		
<ul> <li>Idea of Sloppy Counter is to <u>RELAX</u> the synchronization requirement for counting</li> <li>Instead of synchronizing global count variable each time: counter=counter+1</li> </ul>		
<ul> <li>Synchronization occurs only every so often:</li> <li>e.g. every 1000 counts</li> </ul>		
Relaxing the synchronization requirement <u>drastically</u> reduces locking API overhead by trading-off split-second accuracy of the counter		
<ul> <li>Sloppy counter: trade-off accuracy for speed</li> <li>It's sloppy because it's not so accurate (until the end)</li> </ul>		
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**SLOPPY COUNTER - 2** Update threshold (S) = 5 Synchronized across four CPU cores ■ Threads update local CPU counters Time G 0 0 1 0 1 0 1 0 3 2 0 1 0 3 0 2 0 5 3 0  $5 \rightarrow 0$ 5 (from L<sub>1</sub>) 5 → 0 10 (from  $L_4$ ) 0 October 26, 2021 L8.96





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OBJECTIVES - 10/26
Questions from 10/21

    C Tutorial - Pointers, Strings, Exec in C - Due Fri Oct 29
    Assignment 1 - Due Fri Nov 12

Quiz 1 (Due Tue Nov 2) - Quiz 2 (Due Thur Nov 4)
■ Chapter 26: Concurrency: An Introduction
   Race condition

    Critical section

■ Chapter 27: Linux Thread API
   pthread_create/_join
    pthread_mutex_lock/_unlock/_trylock/_timelock
     pthread_cond_wait/_signal/_broadcast
Chapter 28: Locks

    Introduction, Lock Granularity
    Spin Locks, Test and Set, Compare and Swap
    Chapter 29: Lock Based Data Structures

     Sloppy Cour

    Concurrent Structures: Linked List, Queue, Hash Table

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CONCURRENT LINKED LIST - 1
Simplification - only basic list operations shown
Structs and initialization:
                     typedef struct __node_t {
   int key;
       struct __node_t *next;
} node_t;
                     // basic list structure (one u
typedef struct _ list_t {
    node_t *head;
    pthread_mutex_t lock;
} list_t;
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                     void List_Init(list_t *L) {
                                  L->head = NULL;
pthread_mutex_init(&L->lock, NULL);
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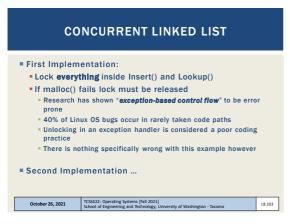
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CONCURRENT LINKED LIST - 2
■ Insert - adds item to list
 Everything is critical!
         There are two unlocks
                                     int List Insert(list t *I, int key) {
    pthread_nutex_lock(aL~>lock);
    node t *new = malloc(sizeo(node_t));
    if (new == NULL) {
        perror("malloc");
        perror("malloc");
        return -1; // fail;
        new->key = key;
        new-bad = new;
        pthread_nutex_unlock(sL~>lock);
        return 0; // success
}
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CONCURRENT LINKED LIST - 3
Lookup - checks list for existence of item with key
Once again everything is critical
  Note - there are also two unlocks
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                          curr = curr->next;
                   pthread_mutex_unlock(&L->lock);
return -1; // failure
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Users must "wait" in line for a single lock to access/modify any item

Hand-over-hand-locking (lock coupling)
Introduce a lock for each node of a list

Traversal involves handing over previous node's lock, acquiring the next node's lock...

Improves lock granularity
Degrades traversal performance

Consider hybrid approach
Fewer locks, but more than 1

Best lock-to-node distribution?

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**CONCURRENT LINKED LIST PERFORMANCE** 

105

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    Concurrent Structures: Linked List, Queue. Hash Table

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CONCURRENT HASH TABLE

Consider a simple hash table
Fixed (static) size
Hash maps to a bucket
Bucket is implemented using a concurrent linked list
One lock per hash (bucket)
Hash bucket is a linked lists

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INSERT PERFORMANCE -
CONCURRENT HASH TABLE

Four threads - 10,000 to 50,000 inserts

iMac with four-core Intel 2.7 GHz CPU

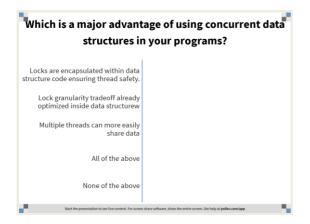
Simple Concurrent List
Concurrent Heath Table

The simple concurrent hash table scales
magnificently.

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LOCK-FREE DATA STRUCTURES

Lock-free data structures in Java
Java.util.concurrent.atomic package
Classes:
AtomicBoolean
AtomicInteger
AtomicIntegerArray
AtomicIntegerFieldUpdater
AtomicLong
AtomicLongFieldUpdater
AtomicLongFieldUpdater
See: https://docs.oracle.com/en/java/javase/11/docs/apl/java.base/java/util/concurrent/atomic/package-summary.html
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Lock-free DATA STRUCTURES

AtomicBoolean
AtomicBoolean
AtomicBoolean
AtomicLongFieldUpdater
AtomicCongFieldUpdater
AtomicCongFieldUpdater
AtomicReference

See: https://docs.oracle.com/en/java/javase/11/docs/apl/java.base/java/util/concurrent/atomic/package-summary.html

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