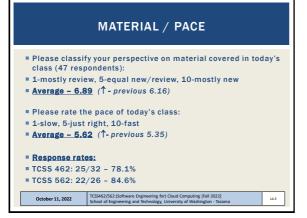


OBJECTIVES - 10/11

 Questions from 10/6
 Cloud Computing - How did we get here?
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FEEDBACK FROM 10/6

I'm not quite clear on how Bit-Level and Instruction-Level Parallelism, being Implicit, happens "automatically".

With bit-level parallelism, arithmetic operations that require multiple instructions to perform on CPUs having lower word size can be accomplished with a single instruction on today's 64-bit CPUs

Word "size" refers to the amount of data a CPU's internal data registers can hold and process at one time. Modern desktop computers have 64-bit words. Computers embedded in appliances and consumer products have word sizes of 8, 16 or 32 bits

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| Lunderstand multithreading to some degree, where multiple Instructions can happen on a single or multiple cores, but multithreading requires specific code to 'break down' the program. How does parallel computing 'break down' tasks implicitly?

| With instruction-level parallelism, CPU features like pipelining, speculative execution, and out-of-order execution help CPUs accomplish more than one operation per clock cycle, to appear to magically do things in parallel when developers write only sequential code to effectively gain a speed-up
| Out-of-order execution (OoOE) allows instructions for high-performance CPUs to begin execution as soon as their operands are ready. Although instructions are issued in-order, they can proceed out-of-order with respect to each other.

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FEEDBACK - 3 Speculative execution is an optimization technique where a CPU performs some task that may not be needed. Work is done before it is known if it is actually needed, to prevent a delay that would have been incurred by doing the work after it is known that it is needed. • This is one way for CPUs to be productive during otherwise "idle" Modern pipelined microprocessors use speculative execution to reduce cost of conditional branch instructions by predicting a program's execution path based on history of branch executions. To improve performance and CPU utilization, instructions can be scheduled at a time when it is not yet known whether the instructions will need to be executed, ahead of a branch... TCSS462/562:(Software Engineering for) Cloud Computing [Fall 2022] School of Engineering and Technology, University of Washington - Taco October 11, 2022 L4.6

6

## FEEDBACK - 4 Am seeking some clarification for what MAP-REDUCE is besides a framework that uses lots of data processed in parallel. Are cloud computing services built using this Infrastructure and then it decides how the work is broken up for servers with different system hardware (heterogeneous, homogeneous, etc.)? MapReduce is a programming model for writing applications to process vast amounts of data (multi-terabyte data-sets) in parallel on large clusters (thousands of nodes) of commodity hardware in a reliable, fault-tolerant manner We also consider for data parallelism, data processing tasks that can be sped up using a divide-and-conquer approach ■ MapReduce provides a programming model and architecture for repeatedly applying the divide-and-conquer pattern October 11, 2022

7

MAP-REDUCE

MapReduce consists of two sequential tasks: Map and Reduce. MAP filters and sorts data while converting it into key-value pairs. REDUCE takes this input and reduces its size by performing some kind of summary operation over the dataset

MapReduce drastically speeds up big data tasks by breaking down large datasets and processing them in parallel

MapReduce paradigm was first proposed in 2004 by Google and later incorporated into the open-source Apache Hadoop framework for distributed processing over large datasets using files

Apache Spark supports MapReduce over large datasets in RAM

Amazon Elastic Map Reduce (EMR) provides cloud provider managed services for Apache Hadoop and Spark services

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MAP-REDUCE - ADDITIONAL RESOURCES

Original Google paper on MapReduce:

https://static.googleusercontent.com/media/research.google.com/en//archive/mapreduce-osdi04.pdf

Apache Spark:

https://spark.apache.org/

Apache Hadoop:

https://hadoop.apache.org/

Amazon Elastic Map Reduce:

https://aws.amazon.com/emr/

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FEEDBACK - 3

When you speak through the mic in class there's a bit of a delay and it can be somewhat distracting at times. Would you be able to change anything about that to minimize the delay?

Is this happening on Zoom? Or in the classroom?

In the classroom I'm able to use the Zoom audio as output and am able to speak with less microphone feedback because of the delay (as long as the volume is not too high)

I can not use the Zoom audio, but it may be hard to hear questions asked verbally over Zoom

This is a work in progress...

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AWS CLOUD CREDITS SURVEY

If you did not provide your AWS account number on the AWS CLOUD CREDITS SURVEY to request AWS cloud credits and you would like credits this quarter, please contact the professor

56 of 58 survey completions logged as of early Oct 11<sup>th</sup>

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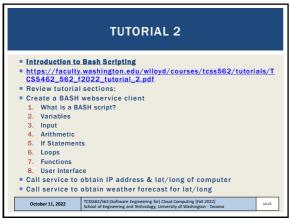
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**TUTORIAL 1** Introduction to Linux & the Command Line https://faculty.washington.edu/wlloyd/courses/tcss562/tutorials/TCSS462\_562\_f2022\_tutorial\_1.pdf Tutorial Sections: The Command Line Basic Navigation More About Files Manual Pages File Manipulation VI - Text Editor Wildcards Permissions Filters 10. Grep and regular expressions 11. Piping and Redirection 12. Process Management TCSS462/562:(Software Engineering for) Cloud Computing [Fall 2022] School of Engineering and Technology, University of Washington - Tac October 11, 2022

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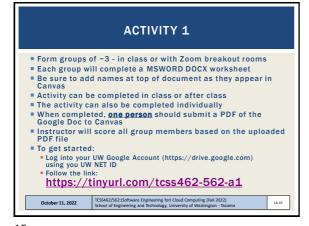
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OBJECTIVES - 10/11

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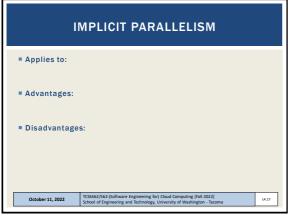


ACTIVITY 1

Solutions to be discussed..

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EXPLICIT PARALLELISM

Applies to:

Advantages:

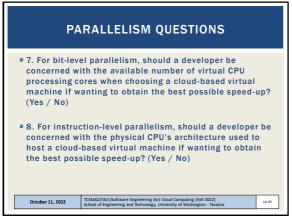
Disadvantages:

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PARALLELISM QUESTIONS - 2

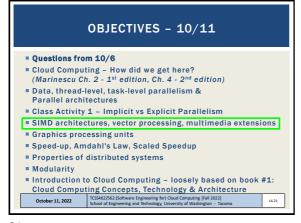
9. For thread level parallelism (TLP) where a programmer has spent considerable effort to parallelize their code and algorithms, what consequences result when this code is deployed on a virtual machine with too few virtual CPU processing cores?

What happens when this code is deployed on a virtual machine with too many virtual CPU processing cores?

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MICHAEL FLYNN'S COMPUTER
ARCHITECTURE TAXONOMY

Michael Flynn's proposed taxonomy of computer
architectures based on concurrent instructions and
number of data streams (1966)

SISD (Single Instruction Single Data)

SIMD (Single Instruction, Multiple Data)

MIMD (Multiple Instructions, Multiple Data)

LESS COMMON: MISD (Multiple Instructions, Single Data)

Pipeline architectures: functional units perform different
operations on the same data

For fault tolerance, may want to execute same instructions
redundantly to detect and mask errors – for task replication

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FLYNN'S TAXONOMY

SISD (Single Instruction Single Data)
Scalar architecture with one processor/core.
Individual cores of modern multicore processors are "SISD"

SIMD (Single Instruction, Multiple Data)
Supports vector processing
When SIMD instructions are issued, operations on individual vector components are carried out concurrently
Two 64-element vectors can be added in parallel
Vector processing instructions added to modern CPUs
Example: Intel MMX (multimedia) instructions

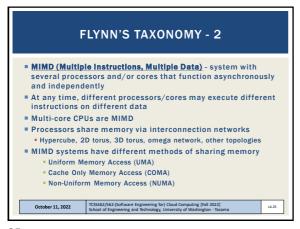
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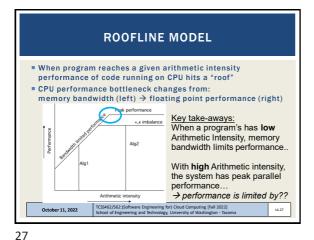
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OBJECTIVES - 10/11

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GRAPHICAL PROCESSING UNITS (GPUS)

GPU provides multiple SIMD processors
Typically 7 to 15 SIMD processors each
32,768 total registers, divided into 16 lanes
(2048 registers each)
GPU programming model:
single instruction, multiple thread
Programmed using CUDA- C like programming language by NVIDIA for GPUS
CUDA threads – single thread associated with each data element (e.g. vector or matrix)
Thousands of threads run concurrently

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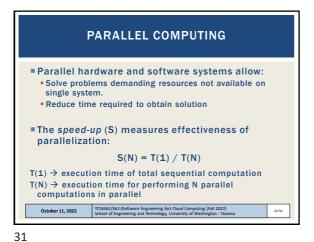
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SPEED-UP EXAMPLE

Consider embarrassingly parallel image processing
Eight images (multiple data)
Apply image transformation (greyscale) in parallel
S-core CPU, 16 hyperthreads
Sequential processing: perform transformations one at a time using a single program thread
Simages, 3 seconds each: T(1) = 24 seconds
Parallel processing
Signature of the seconds
Speedup: S(N) = 24 / 3 = 8x speedup
Called "perfect scalling"
Must consider data transfer and computation setup time

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AMDAHL'S LAW

Amdahl's law is used to estimate the speed-up of a job using parallel computing

Divide job into two parts
Part A that will still be sequential
Part B that will be sped-up with parallel computing

Portion of computation which cannot be parallelized will determine (i.e. limit) the overall speedup

Amdahl's law assumes jobs are of a fixed size

Also, Amdahl's assumes no overhead for distributing the work, and a perfectly even work distribution

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AMDAHL'S LAW EXAMPLE

Program with two independent parts:
Part A is 75% of the execution time
Part B is 25% of the execution time
Part B is made 5 times faster with parallel computing
Estimate the percent improvement of task execution
Original Part A is 3 seconds, Part B is 1 second

N=5 (speedup of part B)
f=.25 (only 25% of the whole job (A+B) will be sped-up)
S=1 / ((1-f) + f/S)
S=1 / ((.75) + .25/5)
S=1.25
% improvement = 100 \* (1 - 1/1.25) = 20%

TCSS462/562:(Software Engineering for) Cloud Computing [Fall 2022] School of Engineering and Technology, University of Washington - Tacoma GUSTAFSON'S LAW

Calculates the scaled speed-up using "N" processors S(N) = N + (1 - N) α

N: Number of processors α: fraction of program run time which can't be parallelized (e.g. must run sequentially)

Can be used to estimate runtime of parallel portion of program

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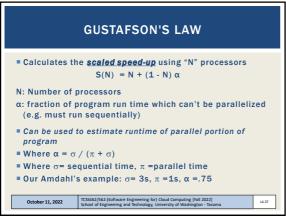
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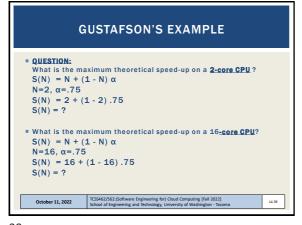
GUSTAFSON'S LAW

Calculates the scaled speed-up using "N" processors  $S(N) = N + (1 - N) \alpha$ N: Number of processors  $\alpha$ : fraction of program run time which can't be parallelized (e.g. must run sequentially)

Example: Consider a program that is embarrassingly parallel, but 75% cannot be parallelized.  $\alpha$ =.75 QUESTION: If deploying the job on a 2-core CPU, what scaled speedup is possible assuming the use of two processes that run in parallel?

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GUSTAFSON'S EXAMPLE

\*\*QUESTION:
What is the maximum theoretical speed-up on a  $\frac{2\text{-core CPU}}{5(N)} = N + (1 - N) \alpha$  N = 2,  $\alpha = 5(N) = 2$ For 2 CPUs, speed up is 1.25x S(N) = 2For 16 CPUs, speed up is 4.75x\*\*What is the maximum theoretical speed-up on a  $\frac{1}{2}$ -core CPU?  $S(N) = N + (1 - N) \alpha$  N = 16,  $\alpha = .75$  S(N) = 16 + (1 - 16) .75 S(N) = ?\*\*TCSS42/562/56f/ware Engineering for) Coud Computing [Fall 2022] School of Engineering and Technology, University of Washington - Tacoma

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MOORE'S LAW

■ Transistors on a chip doubles approximately every 1.5 years
■ CPUs now have billions of transistors
■ Power dissipation issues at faster clock rates leads to heat removal challenges
■ Transition from: increasing clock rates → to adding CPU cores
■ Symmetric core processor — multi-core CPU, all cores have the same computational resources and speed
■ Asymmetric core processor — on a multi-core CPU, some cores have more resources and speed
■ Dynamic core processor — processing resources and speed can be dynamically configured among cores
■ Observation: asymmetric processors offer a higher speedup

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OBJECTIVES - 10/11

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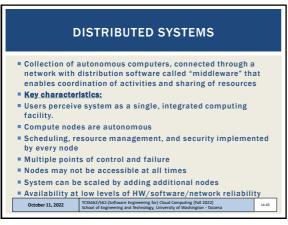
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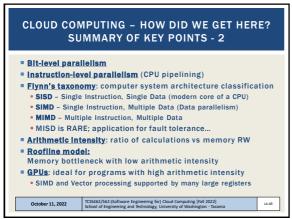
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## TYPES OF MODULARITY **Soft modularity:** TRADITIONAL Divide a program into modules (classes) that call each other and communicate with shared-memory A procedure calling convention is used (or method invocation) ■ Enforced modularity: CLOUD COMPUTING Program is divided into modules that communicate only through message passing ■ The ubiquitous client-server paradigm Clients and servers are independent decoupled modules System is more robust if servers are stateless May be scaled and deployed separately May also FAIL separately! TCSS462/562:(Software Engineering for) Cloud Computing [Fall 2022] School of Engineering and Technology, University of Washington - Tac October 11, 2022 L4.47

**CLOUD COMPUTING - HOW DID WE GET HERE? SUMMARY OF KEY POINTS** ■ Multi-core CPU technology and hyper-threading ■ What is a Heterogeneous system? Homogeneous system? • Autonomous or self-organizing system? Fine grained vs. coarse grained parallelism Parallel message passing code is easier to debug than shared memory (e.g. p-threads) Know your application's max/avg Thread Level Parallelism (TLP) Data-level parallelism: Map-Reduce, (SIMD) Single Instruction Multiple Data, Vector processing & GPUs TCSS462/562:(Software Engineering for) Cloud Computing [Fall 2022] School of Engineering and Technology, University of Washington - Tacoma October 11, 2022 L4.48

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CLOUD COMPUTING – HOW DID WE GET HERE?

SUMMARY OF KEY POINTS - 3

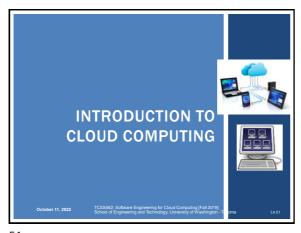
Speed-up (S)
S(N) = T(1) / T(N)

Amdahl's law:
S = 1/ α
α = percent of program that must be sequential

Scaled speedup with N processes:
S(N) = N - α(N-1)

Moore's Law
Symmetric core, Asymmetric core, Dynamic core CPU
Distributed Systems Non-function quality attributes
Distributed Systems – Types of Transparency
Types of modularity- Soft, Enforced

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OBJECTIVES - 10/11

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OBJECTIVES - 10/11

Introduction to Cloud Computing
Why study cloud computing?
History of cloud computing
Business drivers
Cloud enabling technologies
Terminology
Benefits of cloud adoption
Risks of cloud adoption
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WHY STUDY CLOUD COMPUTING?

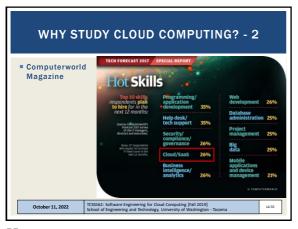
LINKEDIN - TOP IT Skills from job app data
#1 Cloud and Distributed Computing
https://learning.linkedin.com/week-of-learning/top-skills
#2 Statistical Analysis and Data Mining

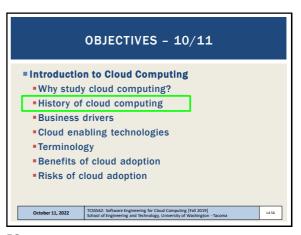
FORBES Survey - 6 Tech Skills That'll Help You Earn More
#1 Data Science
#2 Cloud and Distributed Computing
http://www.forbes.com/sites/laurencebradford/2016/12/19/6-tech-skills-thatll-help-you-earn-more-in-2017/

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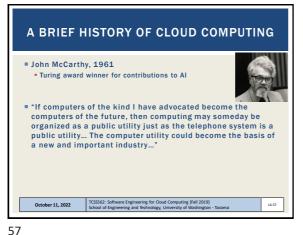
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CLOUD HISTORY - 2

Internet based computer utilities
Since the mid-1990s
Search engines: Yahoo!, Google, Bing
Email: Hotmail, Gmail

2000s
Social networking platforms: MySpace, Facebook, LinkedIn
Social media: Twitter, YouTube

Popularized core concepts
Formed basis of cloud computing

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CLOUD HISTORY: SERVICES - 2

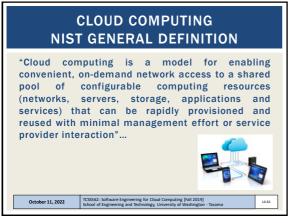
2006 - Software-as-a-Service (SaaS)
Google: Offers Google DOCS, "MS Office" like fully-web based application for online documentation creation and collaboration

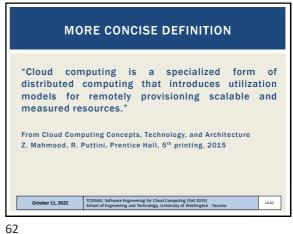
2009 - Platform-as-a-Service (PaaS)
Google: Offers Google App Engine, publicly hosted platform for hosting scalable web applications on google-hosted datacenters

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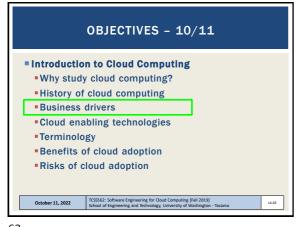
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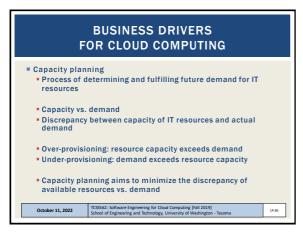


BUSINESS DRIVERS
FOR CLOUD COMPUTING

Capacity planning
Cost reduction
Operational overhead
Organizational agility

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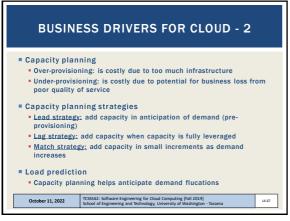
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CAPACITY PLANNING

Capacity vs. Usage (Traditional Data Center)

Planned Capacity

Wenter

Time

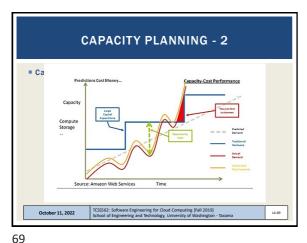
Time

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Cost reduction
IT Infrastructure acquisition
IT Infrastructure maintenance

Operational overhead
Technical personnel to maintain physical IT infrastructure
system upgrades, patches that add testing to deployment cycles
Utility bills, capital investments for power and cooling
Security and access control measures for server rooms
Admin and accounting staff to track licenses, support agreements, purchases

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BUSINESS DRIVERS FOR CLOUD - 4

 Organizational agility
 Ability to adapt and evolve infrastructure to face change from internal and external business factors
 Funding constraints can lead to insufficient on premise IT
 Cloud computing enables IT resources to scale with a lower financial commitment

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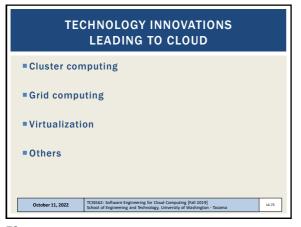
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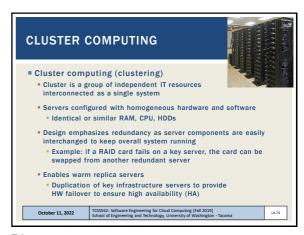
OBJECTIVES - 10/11

Introduction to Cloud Computing
Why study cloud computing?
History of cloud computing
Business drivers
Cloud enabling technologies
Terminology
Benefits of cloud adoption
Risks of cloud adoption
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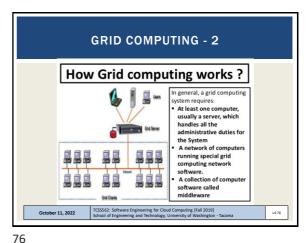
Slides by Wes J. Lloyd L4.12



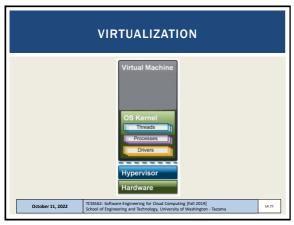


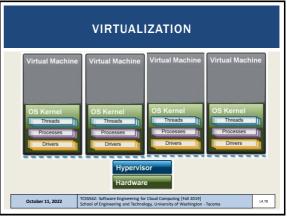
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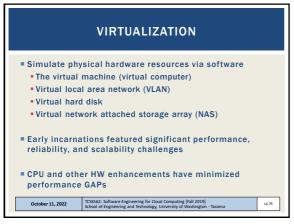


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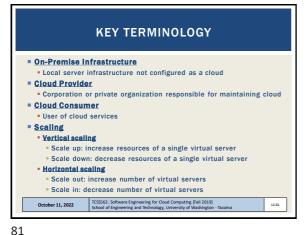


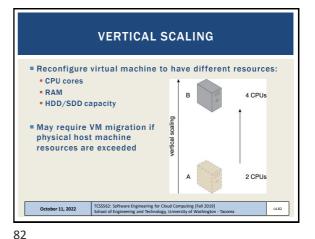
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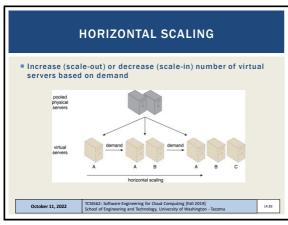


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HORIZONTAL VS VERTICAL SCALING

Horizontal Scaling

Vertical Scaling

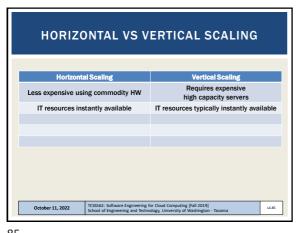
Requires expensive using commodity HW

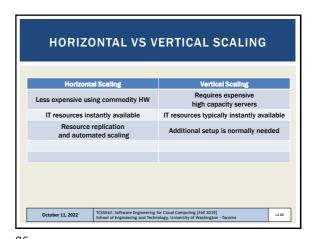
Requires expensive high capacity servers

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HORIZONTAL VS VERTICAL SCALING

Horizontal Scaling
Less expensive using commodity HW
Requires expensive high capacity servers
IT resources instantly available
Resource replication and automated scaling
Additional setup is normally needed
No additional servers required

No additional servers required

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HORIZONTAL VS VERTICAL SCALING **Horizontal Scaling** Vertical Scaling Requires expensive Less expensive using commodity HW high capacity servers IT resources instantly available IT resources typically instantly available Resource replication and automated scaling Additional setup is normally needed Additional servers required No additional servers required Not limited by individual server capacity Limited by individual server capacity October 11, 2022 L4.88

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OBJECTIVES - 10/11

Introduction to Cloud Computing
Why study cloud computing?
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Benefits of cloud adoption
Risks of cloud adoption

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CLOUD BENEFITS - 2

On demand access to pay-as-you-go resources on a short-term basis (less commitment)

Ability to acquire "unlimited" computing resources on demand when required for business needs

Ability to add/remove IT resources at a fine-grained level

Abstraction of server infrastructure so applications deployments are not dependent on specific locations, hardware, etc.

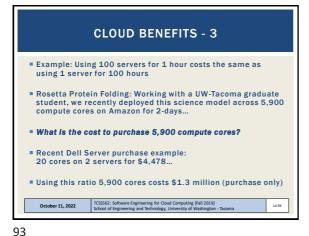
The cloud has made our software deployments more agile...

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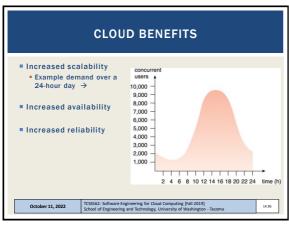
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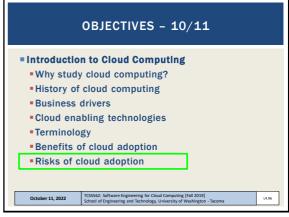
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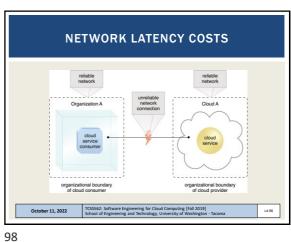
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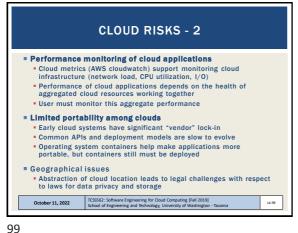


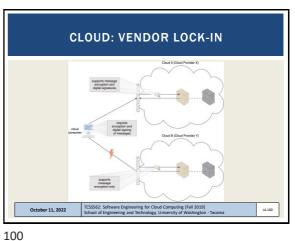
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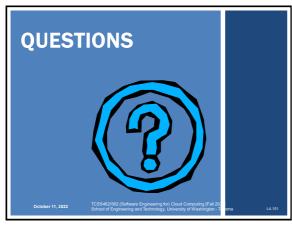




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