


TCSS 422: OPERATING SYSTEMS

Memory API,  
Address Translation,  
Memory Segmentation,  
Free Space Management



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OBJECTIVES

- Optional Ungraded Quiz – Synchronized Array
- Homework 2 Questions
- Homework 3
- Ch. 14
  - Memory API
- Ch. 15
  - Address Translation
- Ch. 16
  - (Memory) Segmentation
- Ch. 17
  - Free Space Management

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FEEDBACK FROM 2/14

- What is a bounded buffer, and when is it used?
- What is piping in operating systems?
- What is a condition variable?

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FEEDBACK - 2

- Homework #2: I'm using "for\_each\_process(task)" but Ubuntu can't find this function... HELP!?
- In your kernel module make file, note the required files:  
All target:  

```
make -C /lib/modules/$(shell uname -r)/build M=$(PWD) modules
```
- Check to be sure you have this kernel sources:  

```
sudo apt-get install build-essential linux-headers-`uname -r`
```
- Helpful command - list kernel modules:  

```
$lsmod
```

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FEEDBACK - 3


- "TCSS 422 has overlap with TCSS 333 for memory maps, memory/address translation"...
- Initial chapters of memory virtualization may be review
- However, memory virtualization spans chapters 13, 14, 15, 16, 17, 18, 19, 20, 21, 22. . . (10 chapters)
  - Suspect this is not all review...

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CHAPTER 14: THE  
MEMORY API



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## MALLOC

```
#include <stdlib.h>
void* malloc(size_t size)
```

- Allocates memory on the heap
- `size_t` unsigned integer (must be +)
- `size` size of memory allocation in bytes
- Returns
  - SUCCESS: A void \* to a memory address
  - FAIL: NULL
- `sizeof()` often used to ask the system how large a given datatype or struct is

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## sizeof()

- Not safe to assume data type sizes using different compilers, systems

```
int *x = malloc(10 * sizeof(int));
printf("%d\n", sizeof(x));
```

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- Dynamic array of 10 ints
- Static array of 10 ints

```
int x[10];
printf("%d\n", sizeof(x));
```

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## FREE()

```
#include <stdlib.h>
void free(void* ptr)
```

- Free memory allocated with `malloc()`
- Provide: (void \*) ptr to malloc'd memory
- Returns: nothing

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```
#include<stdio.h>
```

What will this code do?

```
int * set_magic_number_a()
{
    int a = 53247;
    return &a;
}

void set_magic_number_b()
{
    int b = 11111;
}

int main()
{
    int * x = NULL;
    x = set_magic_number_a();
    printf("The magic number is=%d\n",*x);
    set_magic_number_b();
    printf("The magic number is=%d\n",*x);
    return 0;
}
```

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```
#include<stdio.h>
```

What will this code do?

```
int * set_magic_number_a()
{
    int a = 53247;
    return &a;
}

void set_magic_number_b()
{
    int b = 11111;
}

int main()
{
    int * x = NULL;
    x = set_magic_number_a();
    printf("The magic number is=%d\n",*x);
    set_magic_number_b();
    printf("The magic number is=%d\n",*x);
    return 0;
}
```

Output:

```
$ ./pointer_error
The magic number is=53247
The magic number is=11111
```

We have not changed \*x but  
the value has changed!!

Why?

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## DANGLING POINTER (1/2)

- Dangling pointers arise when a variable referred (a) goes "out of scope", and it's memory is destroyed/overwritten (by b) without modifying the value of the pointer (\*x).
- The pointer still points to the original memory location of the deallocated memory (a), which has now been reclaimed for (b).

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## DANGLING POINTER (2/2)

- Fortunately in the case, a compiler warning is generated:

```
$ g++ -o pointer_error -std=c++0x pointer_error.cpp
```

```
pointer_error.cpp: In function 'int*
set_magic_number_a()':
pointer_error.cpp:6:7: warning: address of local
variable 'a' returned [enabled by default]
```

- This is a common mistake - - - accidentally referring to addresses that have gone "out of scope"

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## CALLOC()

```
#include <stdlib.h>
void *calloc(size_t num, size_t size)
```

- Allocate "C"lear memory on the heap
- Calloc wipes memory in advance of use...
- size\_t num : number of blocks to allocate
- size\_t size : size of each block(in bytes)
- Calloc() prevents...

```
char *dest = malloc(20);
printf("dest string=%s\n", dest);

dest string=◆◆◆F
```

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## REALLOC()

```
#include <stdlib.h>
void *realloc(void *ptr, size_t size)
```

- Resize an existing memory allocation
- Returned pointer may be same address, or a new address
  - New if memory allocation must move
- void \*ptr: Pointer to memory block allocated with malloc, calloc, or realloc
- size\_t size: New size for the memory block(in bytes)
- EXAMPLE: realloc.c
- EXAMPLE: nom.c

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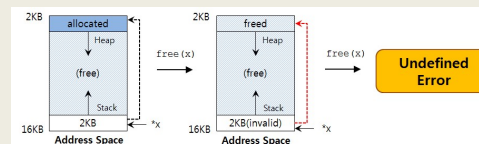
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## DOUBLE FREE

```
int *x = (int *)malloc(sizeof(int)); // allocated
free(x); // free memory
free(x); // free repeatedly
```

- Can't deallocate twice
- Second call core dumps



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## SYSTEM CALLS

- brk(), sbrk()
  - Used to change data segment size (the end of the heap)
  - Don't use these
- Mmap(), munmap()
  - Can be used to create an extra independent "heap" of memory for a user program
- See man page

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## CHAPTER 15: ADDRESS TRANSLATION

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OBJECTIVES

- Address translation
- Base and bounds
- HW and OS Support
- Memory segments
- Memory fragmentation

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ADDRESS TRANSLATION

- 64KB Address space example
- Translation: mapping virtual to physical

Virtual mapping

Address Space

Physical Memory

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BASE AND BOUNDS

- Dynamic relocation
- Two registers base & bounds: **on the CPU**
- OS places program in memory
- Sets base register

$$\text{physical address} = \text{virtual address} + \text{base}$$

- Bounds register
  - Stores size of program address space (16KB)
- OS verifies that every address:

$$0 \leq \text{virtual address} < \text{bounds}$$

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INSTRUCTION EXAMPLE

```
128 : movl 0x0(%ebx), %eax
```

- Base = 32768
- Bounds = 16384
- Fetch instruction at 128 (virt addr) ↑
  - $\text{Phy addr} = \text{virt addr} + \text{base reg}$
  - $32896 = 128 + 32768 \text{ (base)}$
- Execute instruction
  - Load from address (var x is @ 15kb=15360)
  - $48128 = 15360 + 32768 \text{ (base)}$  -- found x...
- Bounds register: terminate process if
  - **ACCESS VIOLATION:** Virtual address > bounds reg

$$\text{physical address} = \text{virtual address} + \text{base}$$

Program Code

Heap

Stack

Int x

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MEMORY MANAGEMENT UNIT

- MMU
  - Portion of the CPU dedicated to address translation
  - Contains base & bounds registers
- Base & Bounds Example:
  - Consider address translation
  - 4 KB (4096 bytes) address space, loaded at 16 KB physical location

Virtual Address	Physical Address
0	16384
1024	17408
3000	19384
4400	20784 (out of bounds)

**FAULT**

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DYNAMIC RELOCATION OF PROGRAMS

- Hardware requirements:

Requirements	HW support
Privileged mode	CPU modes: kernel, user
Base / bounds registers	Registers to support address translation
Translate virtual addr; check if in bounds	Translation circuitry, check limits
Privileged instruction(s) to update base / bounds regs	Instructions for modifying base/bound registers
Privileged instruction(s) to register exception handlers	Set code pointers to OS code to handle faults
Ability to raise exceptions	For out-of-bounds memory access, or attempts to access privileged instr.

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OS SUPPORT FOR MEMORY VIRTUALIZATION

- For base and bounds OS support required
  - When process starts running
    - Allocate address space in physical memory
  - When a process is terminated
    - Reclaiming memory for use
  - When context switch occurs
    - Saving and storing the base-bounds pair
  - Exception handlers
    - Function pointers set at OS boot time

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OS: WHEN PROCESS STARTS RUNNING

- OS searches for free space for new process
  - Free list: data structure that tracks available memory slots

The OS lookup the free list

Free list

16KB

48KB

Physical Memory

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OS: WHEN PROCESS IS TERMINATED

- OS places memory back on the free list

Physical Memory

Free list

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OS: WHEN CONTEXT SWITCH OCCURS

- OS must save base and bounds registers
  - Saved to the Process Control Block PCB (task\_struct in Linux)

Context Switching

Process A PCB

base : 32KB  
bounds : 48KB  
...

Physical Memory

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DYNAMIC RELOCATION

- OS can move process data when not running

- OS deschedules process from scheduler
- OS copies address space from current to new location
- OS updates PCB (base and bounds registers)
- OS reschedules process

- When process runs new base register is restored to CPU
- Process doesn't know it was even moved!

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CHAPTER 16:  
SEGMENTATION

MEMORY

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BASE AND BOUNDS INEFFICIENCIES

■ Address space

- Contains significant unused memory
- Is relatively large
- Preallocates space to handle stack/heap growth

■ Large address spaces

- Hard to fit in memory

■ How can these issues be addressed?

0KB  
1KB  
2KB  
3KB  
4KB  
5KB  
6KB

Program Code

Heap

(free)

Stack

14KB  
15KB  
16KB

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MULTIPLE SEGMENTS

■ Memory segmentation

■ Address space has (3) segments

- Contiguous portions of address space
- Logically separate segments for: code, stack, heap

■ Each segment can placed separately

■ Track base and bounds for each segment (registers)

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SEGMENTS IN MEMORY

■ Consider 3 segments:

0KB  
16KB  
32KB  
48KB  
64KB

Operating System

(not in use)

Stack

(not in use)

Code

Heap

(not in use)

Physical Memory

Segment

Base

Size

Code

32K

2K

Heap

34K

2K

Stack

28K

2K

Much smaller

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ADDRESS TRANSLATION: CODE SEGMENT

$physical\ address = offset + base$

■ Code segment - physically starts at 32KB (base)

■ Starts at "0" in virtual address space

Segment

Base

Size

Code

32K

2K

0KB  
16KB  
32KB  
48KB

Program

(not in use)

Virtual Address Space

Physical Address Space

Bounds check:  
Is virtual address within 2KB address space?

or 32068 desired address

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ADDRESS TRANSLATION: HEAP

Virtual address + base is not the correct physical address.

■ Heap starts at virtual address 4096

■ The data is at 4200

■ Offset= 4200 - 4096 = 104 (virt addr - virt heap start)

■ Physical address = 104 + 34816 (offset + heap base)

Segment

Base

Size

Heap

34K

2K

4KB  
6KB

Heap

Address Space

Physical Memory

104 + 34K or 34920 is the desired physical address

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SEGMENTATION FAULT

■ Access beyond the address space

■ Heap starts at virtual address: 4096

■ Data pointer is to 7KB (7168)

■ Is data pointer valid?

■ Heap starts at 4096 + 2 KB seg size = 6144

■ Offset= 7168 > 4096 + 2048 (6144)

4KB  
6KB  
7KB  
8KB

Heap

(not in use)

Address Space

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L12.6

SEGMENT REGISTERS

- Used to dereference memory during translation

13 12 11 10 9 8 7 6 5 4 3 2 1 0

SegmentOffset

- First two bits identify segment type
- Remaining bits identify memory offset
- Example: virtual heap address 4200 (01000001101000)

13 12 11 10 9 8 7 6 5 4 3 2 1 0

0 1 0 0 0 0 0 0 1 1 0 1 0 0 0

SegmentOffset

Segment	bits
Code	00
Heap	01
Stack	10
-	11

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SEGMENTATION DEREFERENCE

```
1 // get top 2 bits of 14-bit VA
2 Segment = (VirtualAddress & SEG_MASK) >> SEG_SHIFT
3 // now get offset
4 Offset = VirtualAddress & OFFSET_MASK
5 if (Offset >= Bounds[Segment])
6     RaiseException( PROTECTION_FAULT )
7 else
8     PhysAddr = Base [Segment] + Offset
9     Register = AccessMemory(PhysAddr)
```

- VIRTUAL ADDRESS = 01000001101000 (on heap)
- SEG\_MASK = 0x3000 (11000000000000)
- SEG\_SHIFT = 01 → heap (mask gives us segment code)
- OFFSET\_MASK = 0xFFF (00111111111111)
- OFFSET = 000001101000 = 104 (isolates segment offset)
- OFFSET < BOUNDS : 104 < 2048

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STACK SEGMENT

- Stack grows backwards (FILO)
- Requires hardware support:
- Direction bit: tracks direction segment grows

26KB

(not in use)

↑

Stack

(not in use)

28KB

Physical Memory

Segment Register (with Negative-Growth Support)

Segment	Base	Size	Grows	Positive?
Code	32K	2K	1	
Heap	34K	2K	1	
Stack	28K	2K	0	

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SHARED CODE SEGMENTS

- Code sharing: enabled with HW support
- Supports storing shared libraries in memory only once
- DLL: dynamic linked library
- .so (linux): shraed object in Linux (under /usr/lib)
- Many programs can access them
- Protection bits: track permissions to segment

Segment Register Values (with Protection)

Segment	Base	Size	Grows	Positive?	Protection
Code	32K	2K	1		Read-Execute
Heap	34K	2K	1		Read-Write
Stack	28K	2K	0		Read-Write


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SEGMENTATION GRANULARITY

- Coarse-grained
- Manage memory as large purpose based segments:
  - Code segment
  - Heap segment
  - Stack segment




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SEGMENTATION GRANULARITY - 2

- Fine-grained
- Manage memory as list of segments
- Code, heap, stack segments composed of multiple smaller segments
- Segment table
  - On early systems
  - Stored in memory
  - Tracked large number of segments



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MEMORY FRAGMENTATION

- Consider how much free space?
- We'll say about 24 KB
- Request arrives to allocate a 20 KB heap segment
- Can we fulfil the request for 20 KB of contiguous memory?

Not compacted

0KB	
8KB	Operating System
16KB	
24KB	(not in use)
32KB	Allocated
40KB	(not in use)
48KB	Allocated
56KB	(not in use)
64KB	Allocated

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COMPACTION

- Supports rearranging memory
- Can we fulfil the request for 20 KB of contiguous memory?
- Drawback:** Compaction is slow
  - Rearranging memory is time consuming
  - 64KB is fast
  - 4GB+ ... slow
- Algorithms:
  - Best fit: keep list of free spaces, allocate the most snug segment for the request
  - Others: worst fit, first fit... (in future chapters)

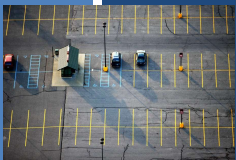
Compacted

0KB	
8KB	Operating System
16KB	
24KB	Allocated
32KB	
40KB	
48KB	(not in use)
56KB	
64KB	

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CHAPTER 17: FREE SPACE MANAGEMENT

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FREE SPACE MANAGEMENT

- Management of memory using
  - Only fixed-sized units
    - Easy: keep a list
    - Memory request → return first free entry
      - Simple search
  - With variable sized units
    - More challenging
    - Results from variable sized malloc requests
    - Leads to fragmentation

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QUESTIONS

