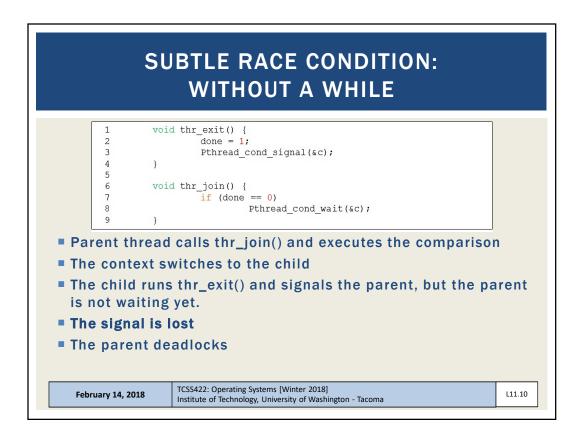
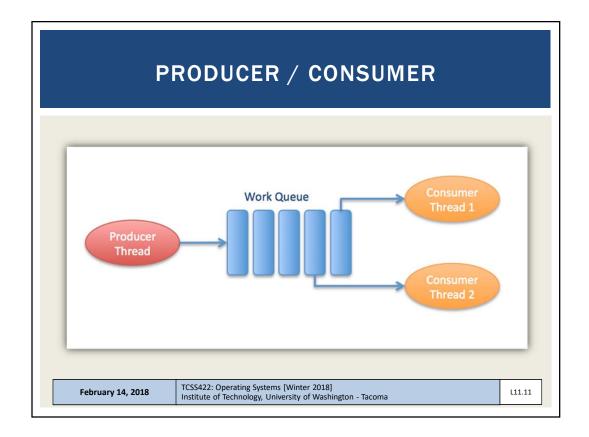
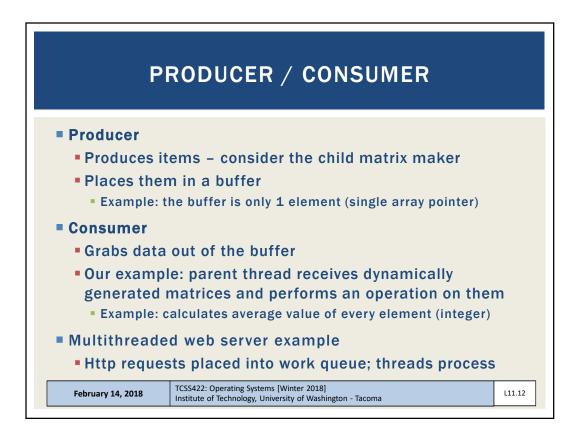


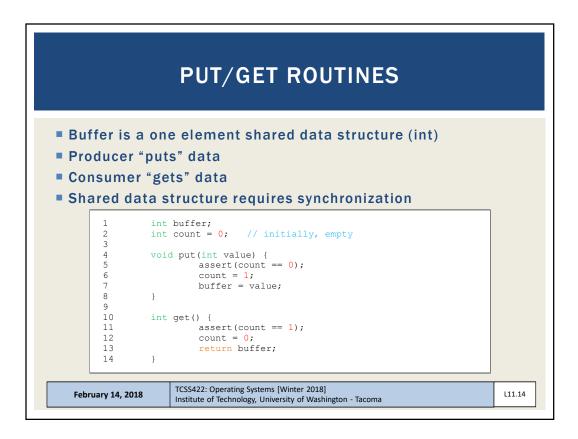
	MATRIX GENERATOR	
	ead, and worker thread (generates matrices) e matrix pointer.	
	appen if we don't use a condition variable to change of the lock?	
Let's try "nosi	ignal.c"	
February 14, 2018	TCSS422: Operating Systems [Winter 2018] Institute of Technology, University of Washington - Tacoma	L11.9

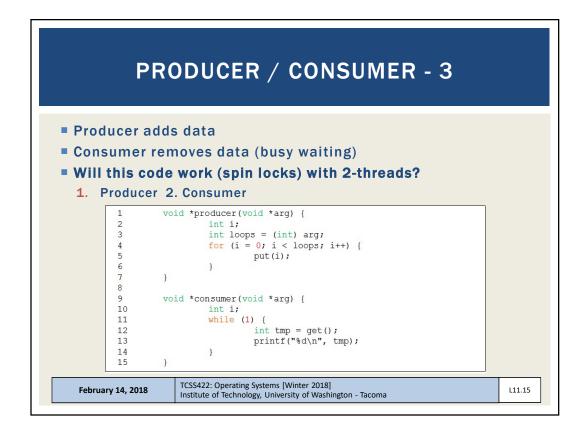


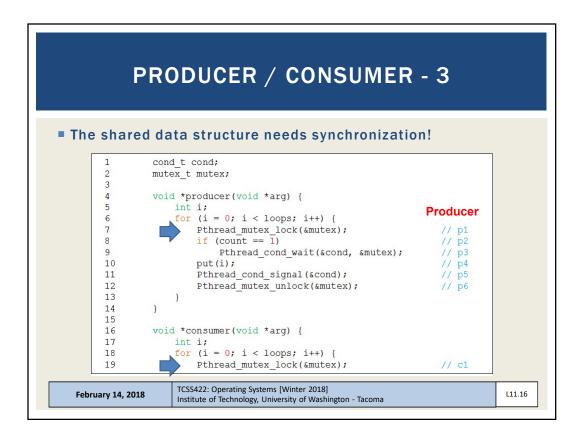




PRO	DDUCER / CONSUMER - 2
 Bounded buf Similar to p grep pthrea Synchronized 	biping output from one Linux process to another ad signal.c wc -l ed access: ut from grep \rightarrow wc as it is produced
February 14, 2018	TCSS422: Operating Systems [Winter 2018] L11.13 Institute of Technology, University of Washington - Tacoma L11.13





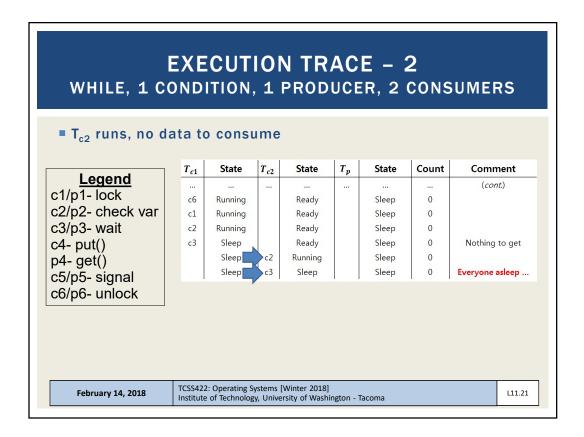


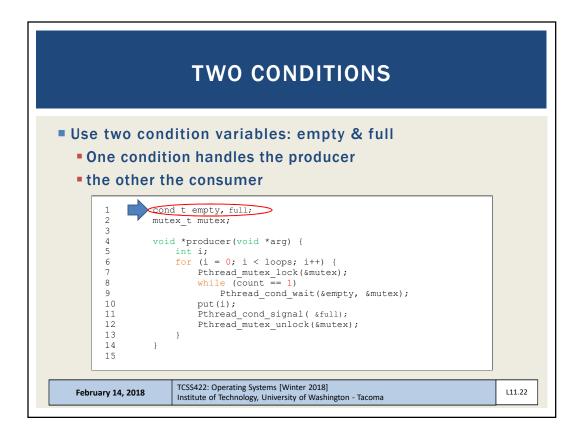
	PRO	DUCER/CONSUMER	- 4	
20		if (count == 0)	// c2	
21 22 23 24 25 26 27	}	<pre>Pthread_cond_wait(&cond, &mutex); int tmp = get(); Pthread_cond_signal(&cond); Pthread_mutex_unlock(&mutex); printf("%d\n", tmp);</pre>	// c3 // c4 // c5 // c6 Consumer	
This co	de as-is v	works with just:		
	(1)	Producer		
	(1)	Consumer		
	ale to (2	+) consumer's it fails		
		+) consumer's it fails		

NO WHI		ECU ⁻ 1 PRC					UMEF	RS
	,	State	$ T_{c2} $	State	T_p	State	Count	Comment
Two threads	c1	Running		Ready	-	Ready	0	
	, c2	Running		Ready		Ready	0	
	c3	Sleep		Ready		Ready	0	Nothing to get
Legend		Sleep		Ready	p1	Running	0	
c1/p1-lock		Sleep		Ready	p2	Running	0	
c2/p2- check var		Sleep		Ready	p 4	Running	1	Buffer now full
c3/p3- wait		Ready		Ready	p5	Running	1	T _{c1} awoken
c4- put()		Ready		Ready	p6	Running	1	
		Ready		Ready	p1	Running	1	
p4- get()		Ready		Ready	p2	Running	1	
c5/p5- signal		Ready		Ready	p 3	Sleep	1	Buffer full; sleep
c6/p6- unlock		Ready	c1	Running		Sleep	1	T _{c2} sneaks in
		Ready	c2	Running		Sleep	1	
		Ready	c4	Running		Sleep	0	and grabs data
		Ready	c5	Running		Ready	0	T _p awoken
		Ready	c6	Running		Ready	0	
	c4	Running	1	Ready		Ready	0	Oh oh! No data

Ρ	RODUCER/CONSUMER SYNCHRONIZATION
When produce any data in th	er threads awake, they do not check if there is e buffer
Need while,	not if
 Then T_p has a There is nothi T_{c1}, T_{c2}, and T_p 	s a value, wakes T_{c1} whom consumes the value value to put, but T_{c1} 's signal on &cond wakes T_{c2} ng for T_{c2} consume, so T_{c2} sleeps , all sleep forever
T _{c1} needs to w	ake T_p to T_{c2}
February 14, 2018	TCSS422: Operating Systems [Winter 2018] L11.19 Institute of Technology, University of Washington - Tacoma L11.19

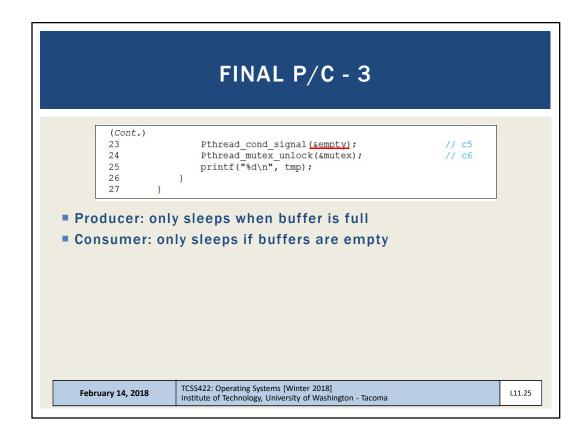
EXECUTION TRACE: WHILE, 1 CONDITION, 1 PRODUCER, 2 CONSUMERS								
WHILE, I CC	1		1 1		1 1		1 1	
	<i>T</i> _{c1}	State	T _{c2}	State	T _p	State	Count	Comment
	c1	Running		Ready		Ready	0	
	c2	Running		Ready		Ready	0	
Levend	c3	Sleep		Ready		Ready	0	Nothing to get
Legend		Sleep	c1	Running		Ready	0	
c1/p1- lock		Sleep	c2	Running		Ready	0	
c2/p2- check var		Sleep	c3	Sleep		Ready	0	Nothing to get
c3/p3- wait		Sleep		Sleep	p1	Running	0	
c4- put()		Sleep		Sleep	p2	Running	0	
p4- get()		Sleep		Sleep	p4	Running	1	Buffer now full
c5/p5- signal		Ready		Sleep	p5	Running	1	T _{c1} awoken
c6/p6- unlock		Ready		Sleep	p6	Running	1	
co/po- uniock		Ready		Sleep	p1	Running	1	
		Ready		Sleep	p2	Running	1	
_		Ready		Sleep	p3	Sleep	1	Must sleep (full)
	c2	Running		Sleep		Sleep	1	Recheck condition
	c4	Running		Sleep		Sleep	0	T _{c1} grabs data
	c5	Running		Ready		Sleep	0	Oops! Woke T _{c2}

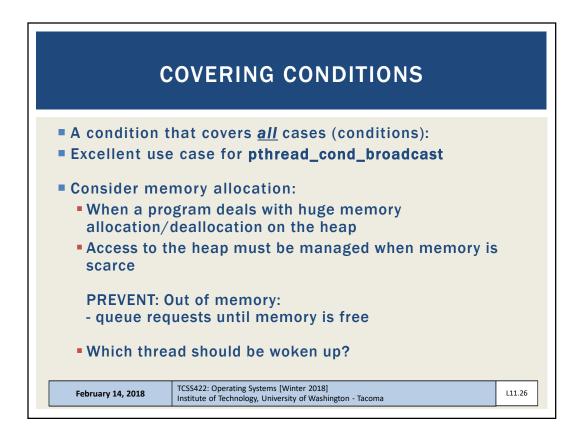


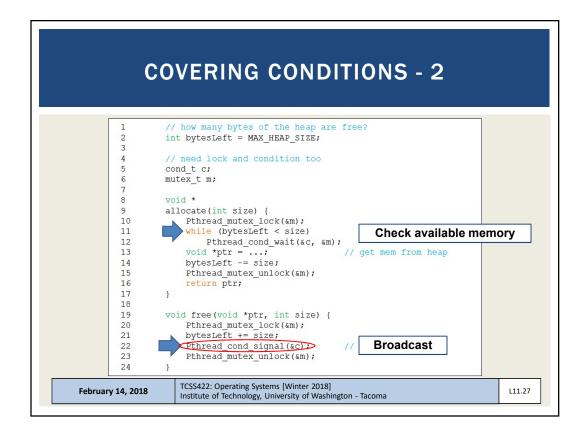


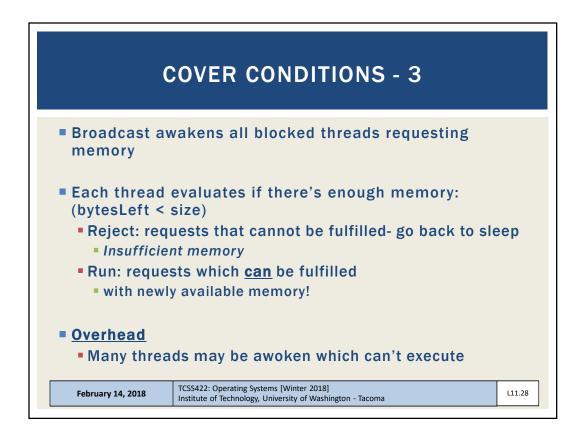
FINA	FINAL PRODUCER/CONSUMER				
 Change buffe Add indexing 	er from int, to int buffer[MAX] ; variables				
2 i 3 i	<pre>int buffer[MAX]; int fill = 0; int use = 0; int count = 0;</pre>				
6 V 7 8 9 10 }	<pre>roid put(int value) { buffer[fill] = value; fill = (fill + 1) % MAX; count++;</pre>				
11 12 13 14	<pre>int get() { int tmp = buffer[use]; use = (use + 1) % MAX; count; return tmp;</pre>				
February 14, 2018	TCSS422: Operating Systems [Winter 2018] Institute of Technology, University of Washington - Tacoma	L11.23			

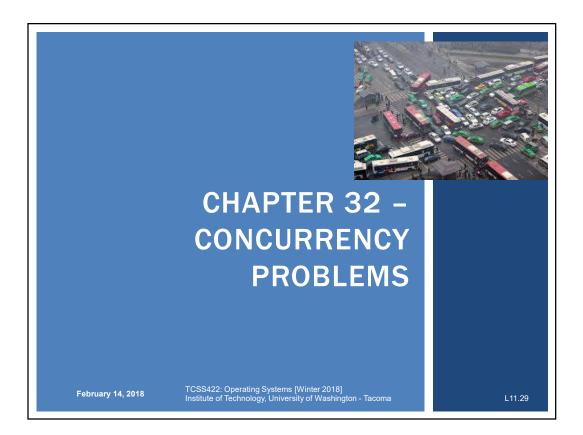
	FINAL P/C - 2	
1	cond_t empty, full	
2	<pre>mutex_t mutex;</pre>	
3	<pre>void *producer(void *arg) {</pre>	
5	int i;	
6	for $(i = 0; i < loops; i++)$ {	
7	Pthread mutex lock(&mutex);	// p1
8	while (count == MAX)	// p2
9	Pthread cond wait(∅, &mutex);	
10	put(i);	// p4
11	Pthread_cond_signal (&full);	// p5
12	<pre>Pthread_mutex_unlock(&mutex);</pre>	// p6
13	}	8.52
14	}	
15		
16	<pre>void *consumer(void *arg) {</pre>	
17	int i;	
18	<pre>for (i = 0; i < loops; i++) {</pre>	
19	Pthread_mutex_lock(&mutex);	// c1
20	while (count == 0)	// c2
21	Pthread_cond_wait(&full, &mutex);	
22	<pre>int tmp = get();</pre>	// c4

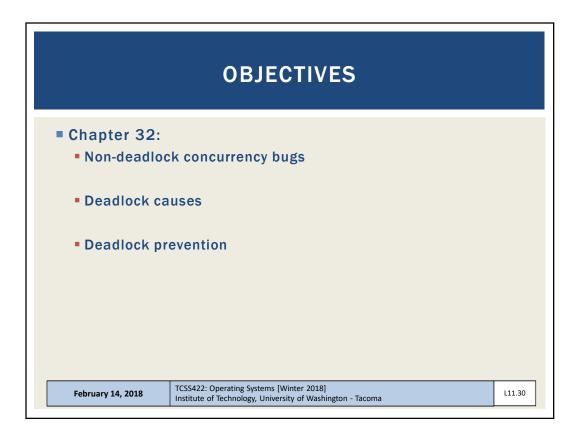


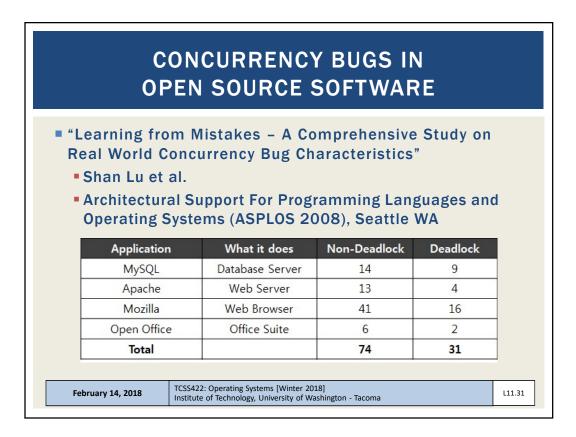


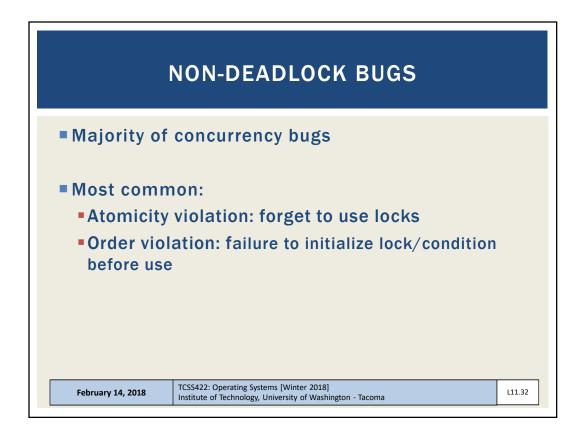


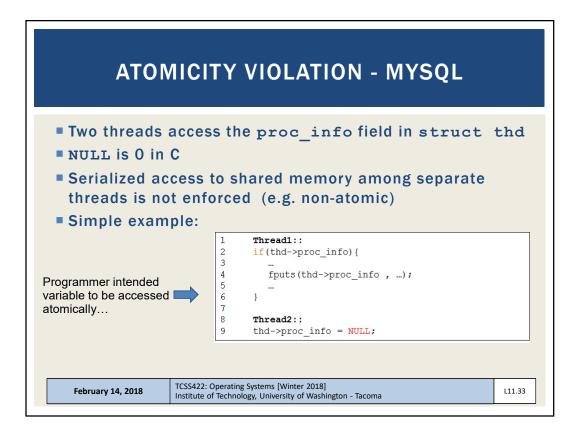


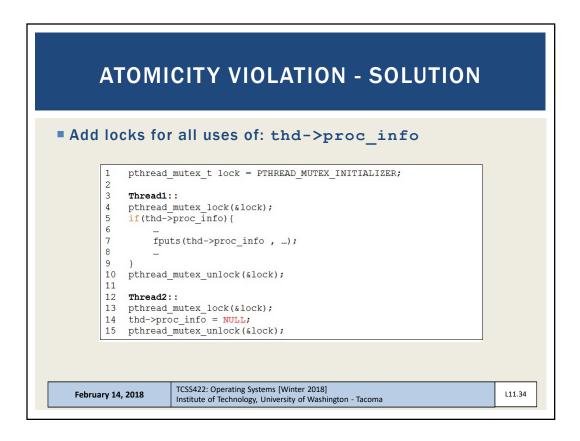


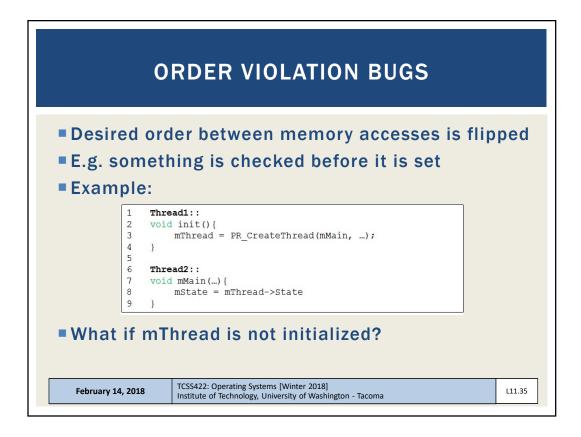


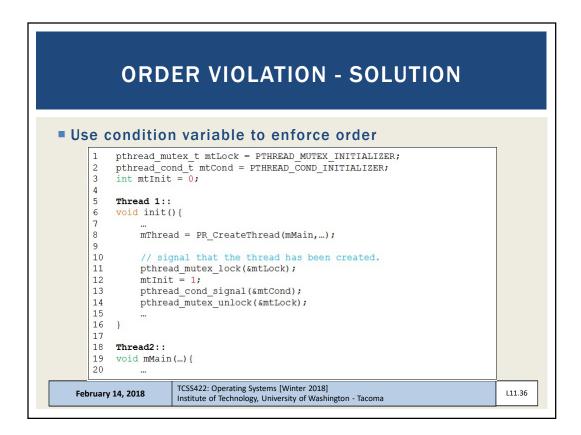




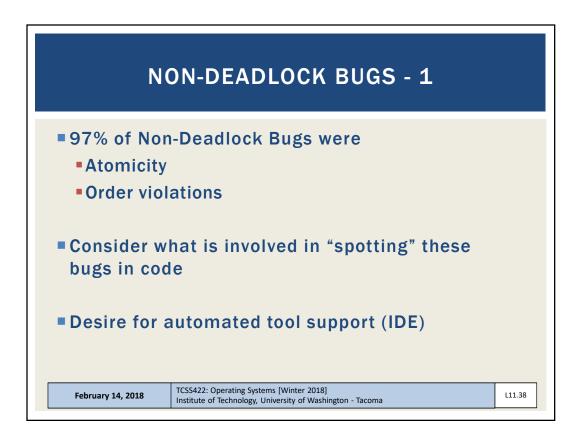


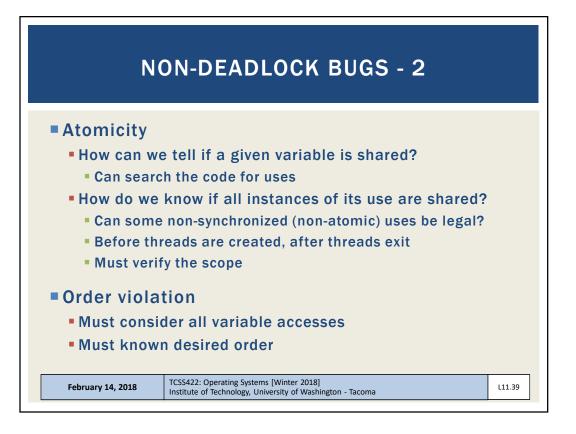


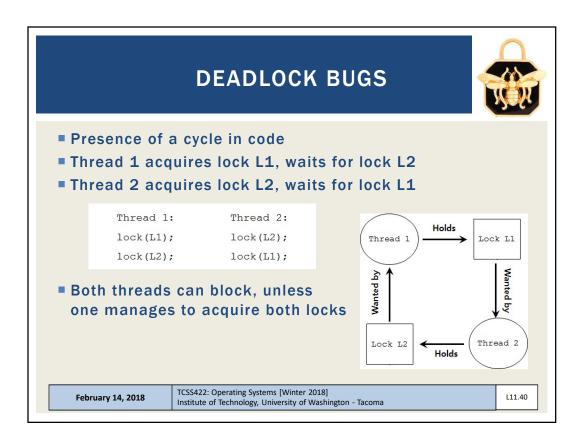


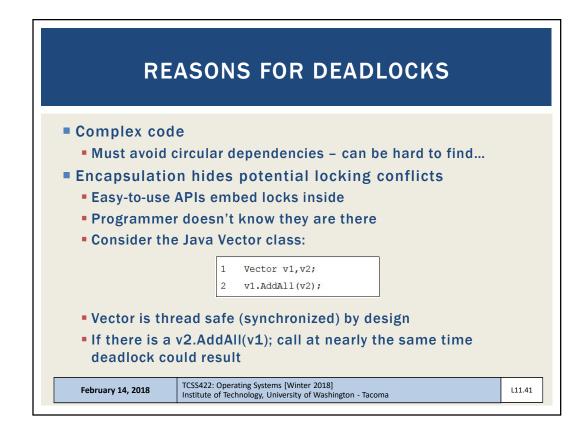


<pre>21 // wait for the thread to be initialized 22 pthread_mutex_lock(&mtLock); 23 while(mtInit == 0) 24 pthread_cond_wait(&mtCond, &mtLock); 25 pthread_mutex_unlock(&mtLock); 26 27 mstate = mThread->state; 28 29 }</pre>	<pre>22 pthread_mutex_lock(&mtLock); 23 while(mtInit == 0) 24 pthread_cond_wait(&mtCond, &mtLock); 25 pthread_mutex_unlock(&mtLock); 26 27 mstate = mThread->state; 28</pre>
<pre>22 pthread_mutex_lock(&mtLock); 23 while(mtInit == 0) 24 pthread_cond_wait(&mtCond, &mtLock); 25 pthread_mutex_unlock(&mtLock); 26 27 mstate = mThread->state; 28</pre>	<pre>22 pthread_mutex_lock(&mtLock); 23 while(mtInit == 0) 24 pthread_cond_wait(&mtCond, &mtLock); 25 pthread_mutex_unlock(&mtLock); 26 27 mstate = mThread->state; 28</pre>
<pre>23 while (mtInit == 0) 24</pre>	<pre>23 while(mtInit == 0) 24</pre>
<pre>25 pthread_mutex_unlock(&mtLock); 26 27 mState = mThread->State; 28</pre>	<pre>25 pthread_mutex_unlock(&mtLock); 26 27 mState = mThread->State; 28</pre>
26 27 mstate = mThread->State; 28	26 27 mState = mThread->State; 28
27 mstate = mThread->state; 28	27 mState = mThread->State; 28
28	28
	25 1

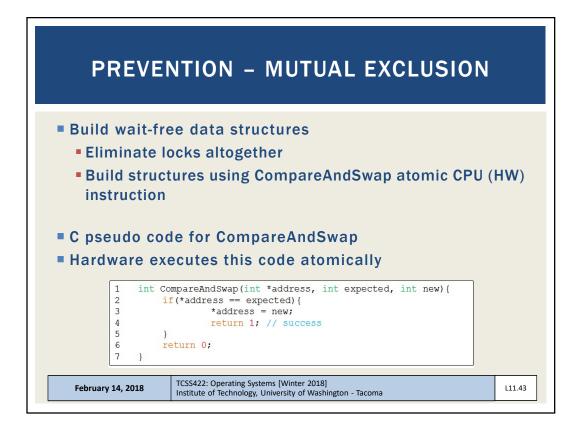


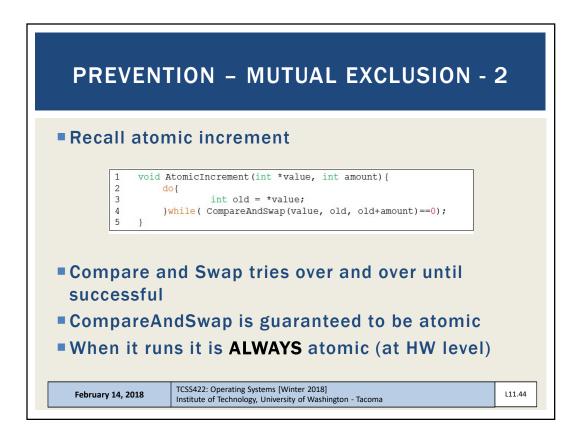




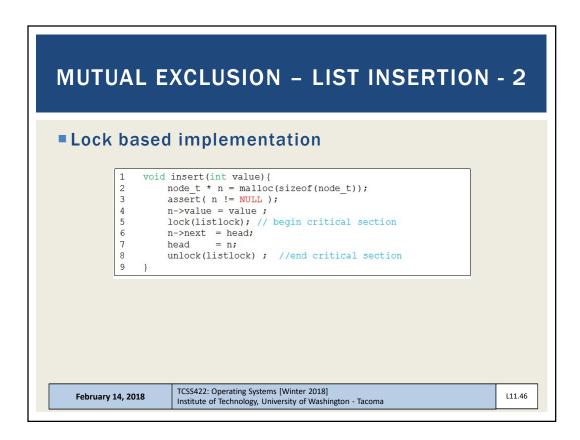


■ <u>Four co</u>	nditions are required for dead lock to occur				
Condition	Description				
Mutual Exclusion	Threads claim exclusive control of resources that they require.				
Hold-and-wait	Threads hold resources allocated to them while waiting for additional resources				
No preemption	Resources cannot be forcibly removed from threads that are holding them.				
Circular wait	There exists a circular chain of threads such that each thread holds one more resources that are being requested by the next thread in the chain				





MUTUAL	MUTUAL EXCLUSION: LIST INSERTION				
Consider list	tinsertion				
2 nod 3 ass 4 n-> 5 n->	<pre>sert(int value){ de_t * n = malloc(sizeof(node_t)); sert(n != NULL); >value = value ; >next = head; ad = n;</pre>				
	TCSS422: Operating Systems [Winter 2018] nstitute of Technology, University of Washington - Tacoma	L11.45			

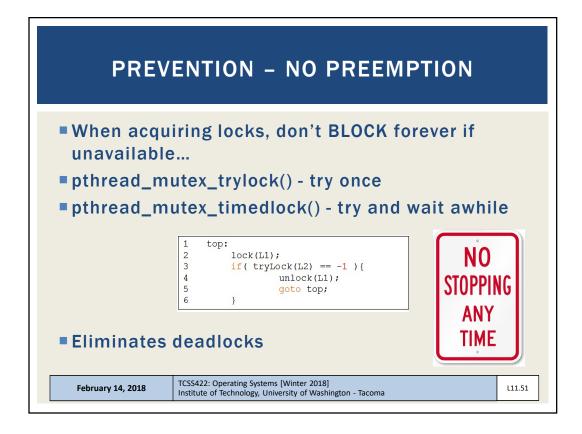


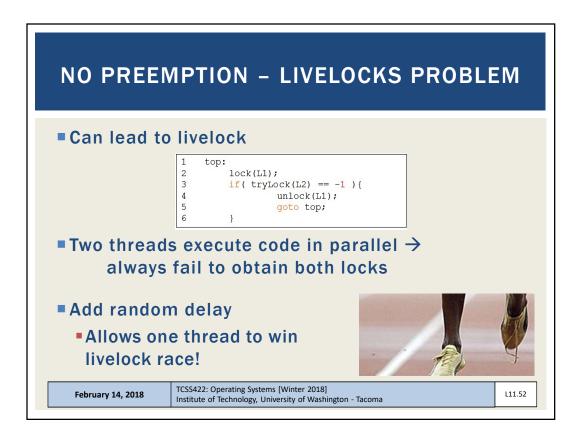
	XCLUSION – LIST INSERTIO	N - 3
2 1 3 4 5 6	<pre>insert(int value) { node_t *n = malloc(sizeof(node_t)); assert(n != NULL); n->value = value; do {</pre>	
-	ead to n (new node ptr) head = n->next	
February 14, 2018	TCSS422: Operating Systems [Winter 2018] Institute of Technology, University of Washington - Tacoma	L11.47

Four conditions are required for dead lock to occur				
Condition	Description			
Mutual Exclusion	Threads claim exclusive control of resources that they require.			
Hold-and-wait	Threads hold resources allocated to them while waiting for additional resources			
No preemption	Resources cannot be forcibly removed from threads that are holding them.			
Circular wait	There exists a circular chain of threads such that each thread holds one more resources that are being requested by the next thread in the chain			

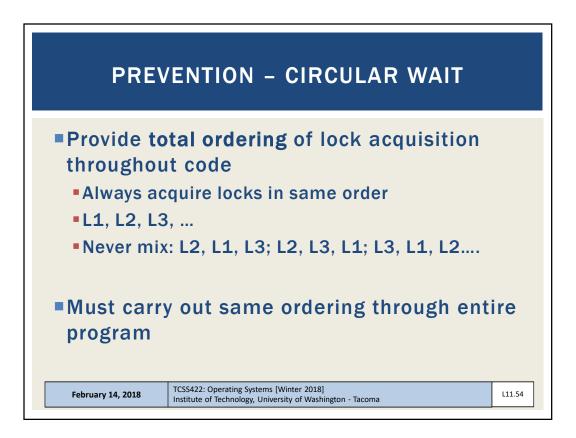
PREVENTION – HOLD AND WAIT					
 Problem: acquire all locks atomically Solution: use a "lock" "lock" (<i>like a guard lock</i>) 					
	<pre>1 lock(prevention); 2 lock(L1); 3 lock(L2); 4 5 unlock(prevention);</pre>				
 Effective solution – guarantees no race conditions while acquiring L1, L2, etc. Order decen't matter for L1, L2 					
 Order doesn't matter for L1, L2 Prevention (GLOBAL) lock decreases concurrency of code Acts Lowers lock granularity Encapsulation: consider the Java Vector class 					
February 14, 2018	TCSS422: Operating Systems [Winter 2018] Institute of Technology, University of Washington - Tacoma	1.49			

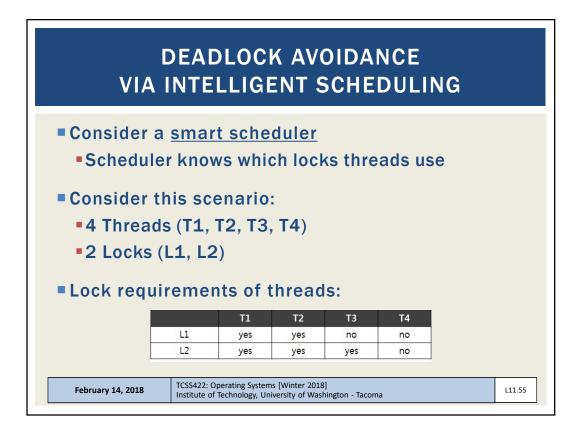
CONDITIONS FOR DEADLOCK				
Condition	nditions are required for dead lock to occur Description			
Mutual Exclusion	Threads claim exclusive control of resources that they require.			
Hold-and-wait	Threads hold resources allocated to them while waiting for additional resources			
No preemption	Resources cannot be forcibly removed from threads that are holding them.			
Circular wait	There exists a circular chain of threads such that each thread holds one more resources that are being requested by the next thread in the chain			
	·]			
February 14, 20	I8 TCSS422: Operating Systems [Winter 2018] Institute of Technology, University of Washington - Tacoma L11.50			

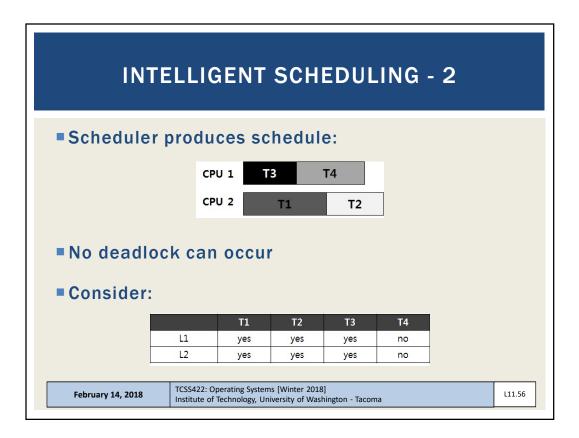


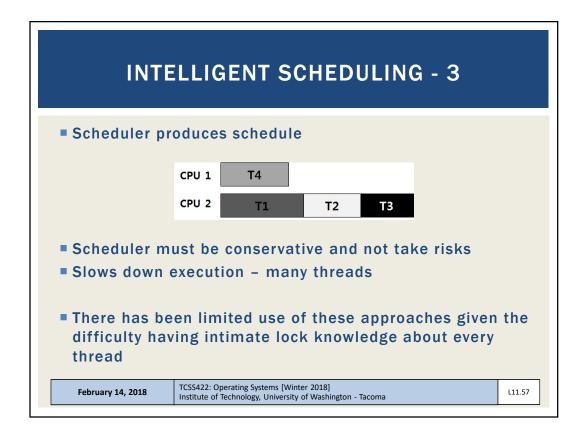


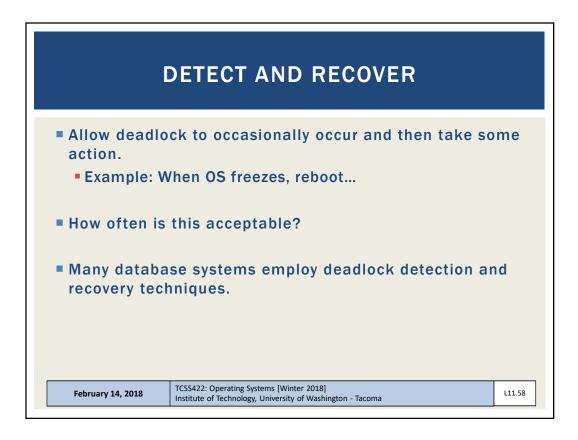
■ <u>Four co</u>	nditions are required for dead lock to occur	
Condition	Description	
Mutual Exclusion	Threads claim exclusive control of resources that they require.	
Hold-and-wait	Threads hold resources allocated to them while waiting for additional resources	
No preemption	Resources cannot be forcibly removed from threads that are holding them.	
·Circular wait	There exists a circular chain of threads such that each thread holds one more resources that are being requested by the next thread in the chain	

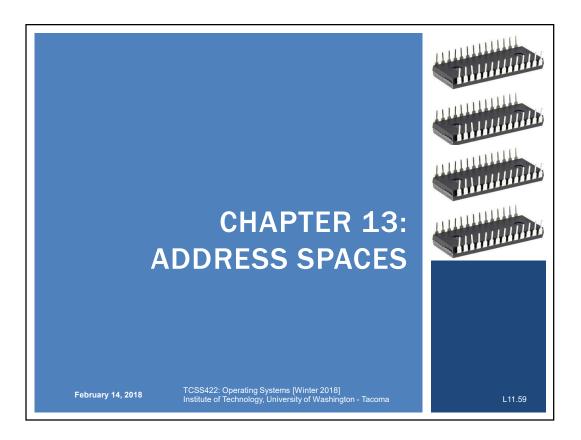


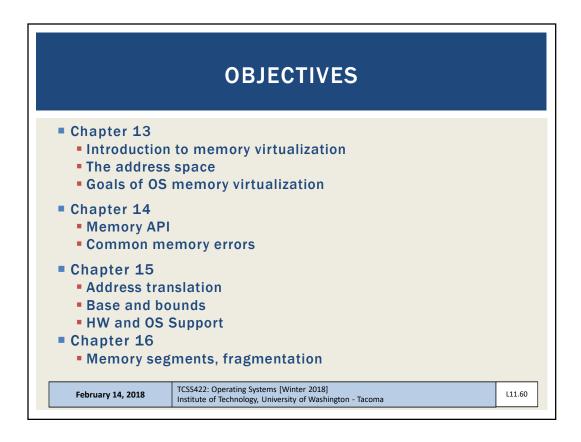




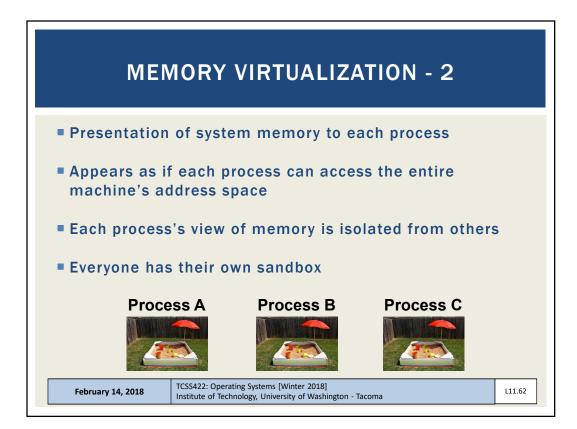


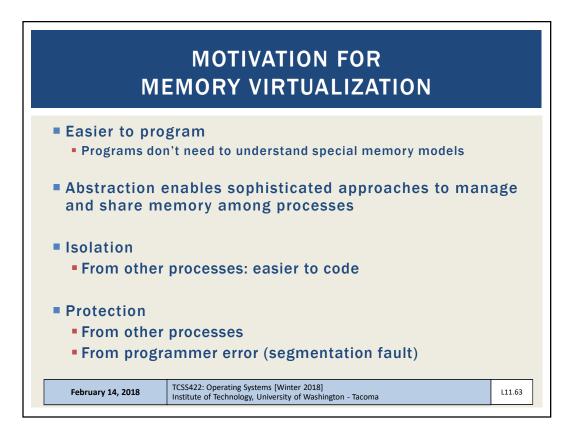


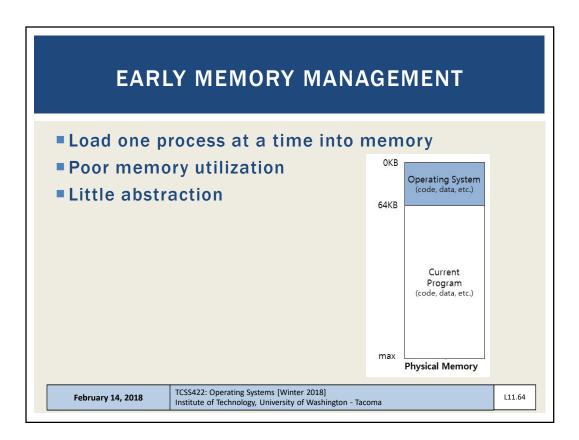


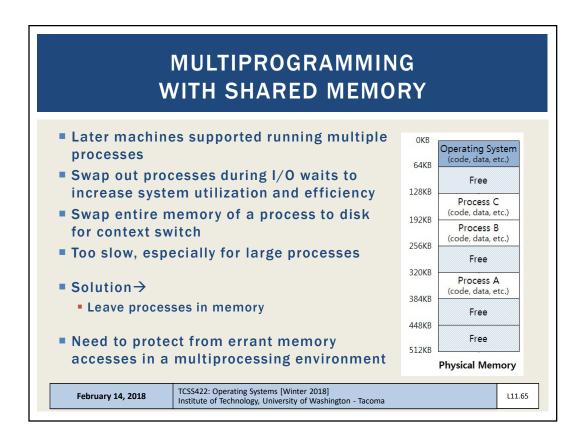


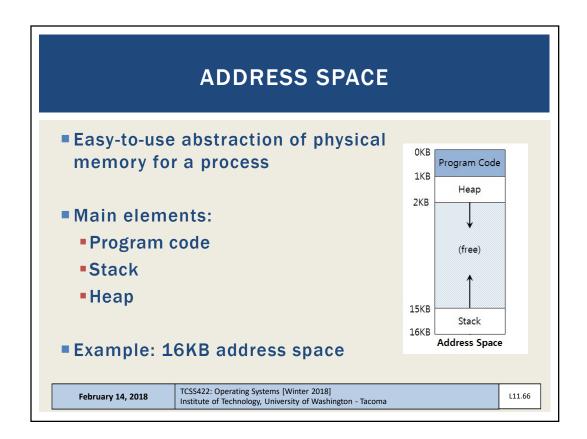
MEMORY VIRTUALIZATION		
What is mem	ory virtualization?	
Classic use	irtual" memory, of disk space as additional RAM	
 When availate Less comm 	able RAM was low	
- 2035 001111	Unrecently	
February 14, 2018	TCSS422: Operating Systems [Winter 2018] Institute of Technology, University of Washington - Tacoma	

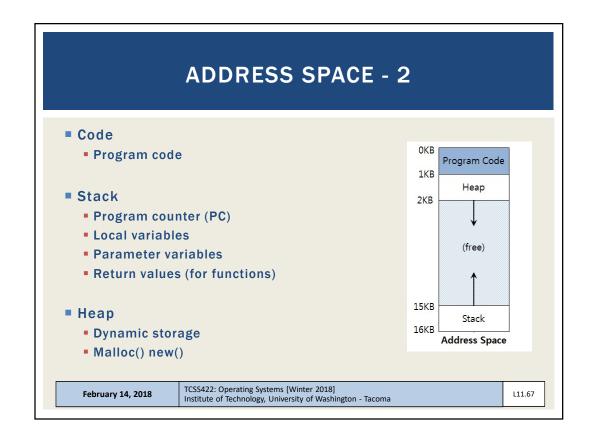


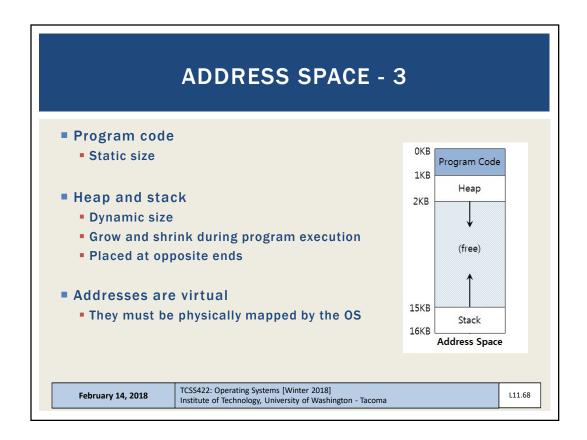




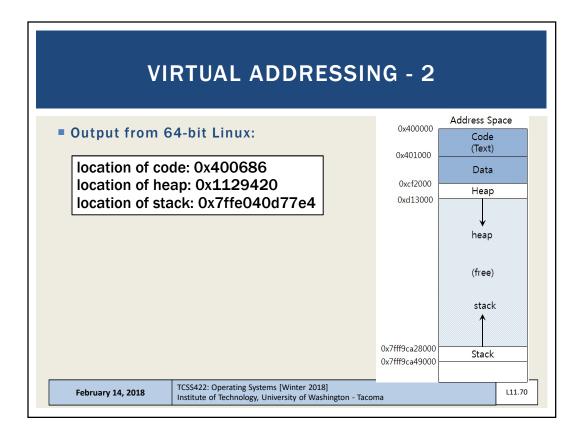


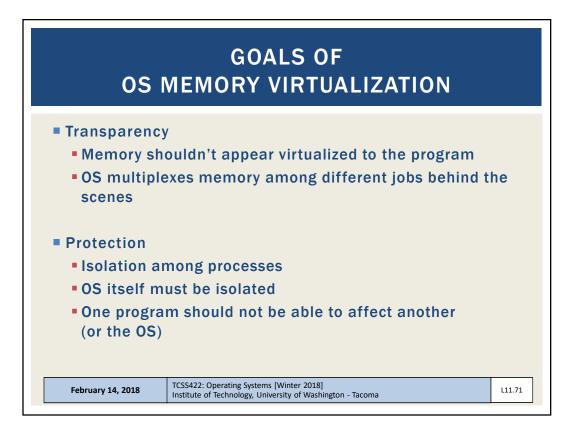


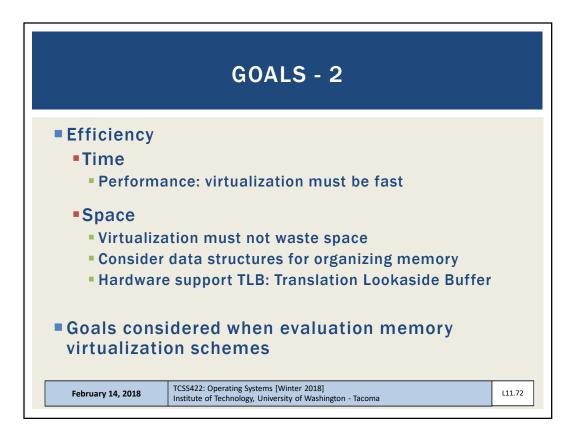


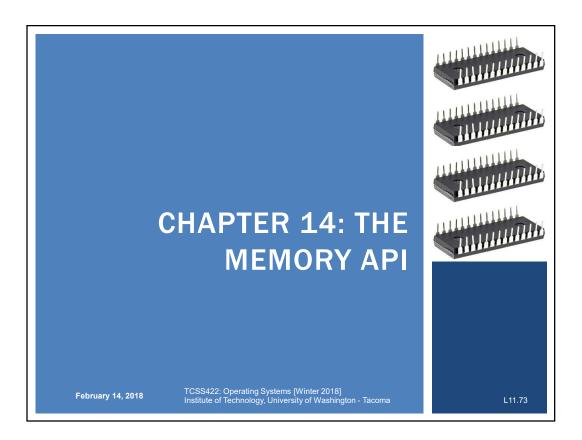


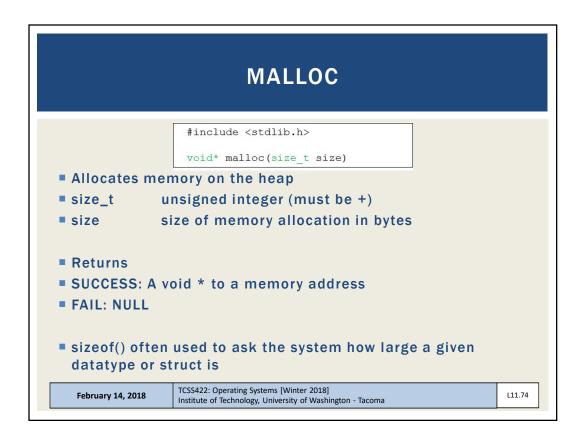
VIRTUAL ADDRESSING					
 Every addre OS transla 	ess is virtual ates virtual to physical addresses				
<pre>#include <st #include="" <st<="" pre=""></st></pre>					
int main(int	<pre>int main(int argc, char *argv[]){</pre>				
printf(" int x = printf("	<pre>printf("location of code : %p\n", (void *) main); printf("location of heap : %p\n", (void *) malloc(1)); int x = 3; printf("location of stack : %p\n", (void *) &x);</pre>				
return x					
= EXAMPLE:	virtual.c				
February 14, 2018	TCSS422: Operating Systems [Winter 2018] Institute of Technology, University of Washington - Tacoma	L11.69			

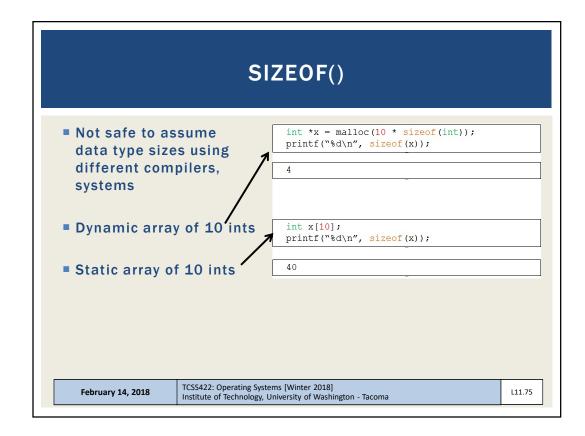




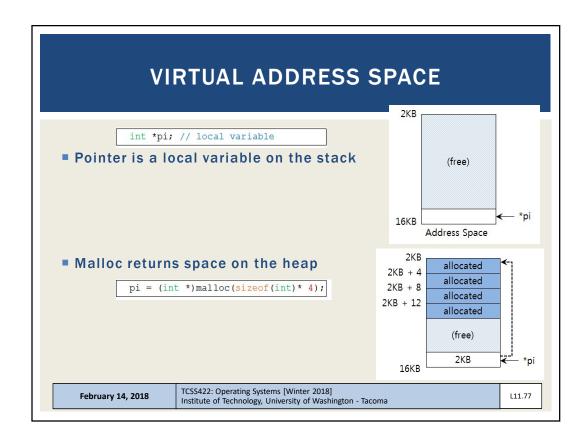


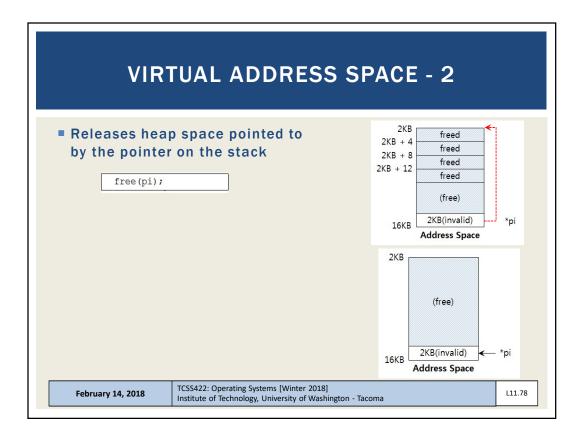


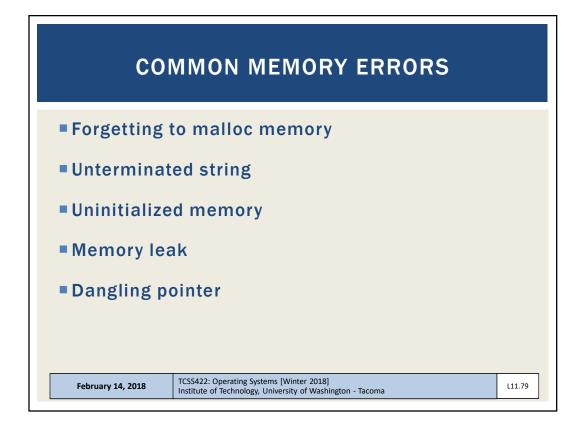


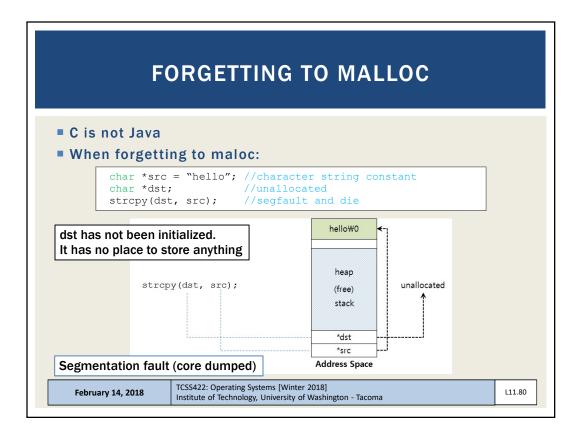


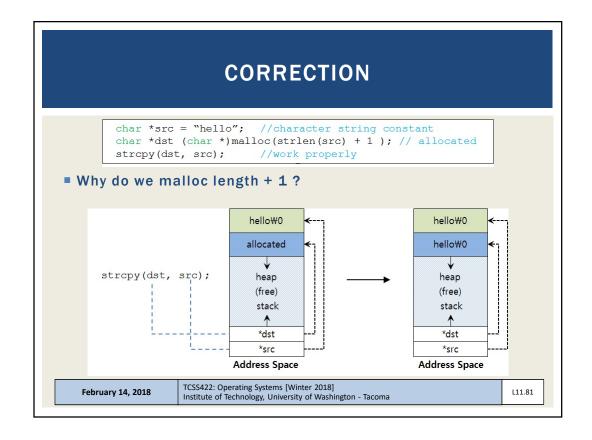


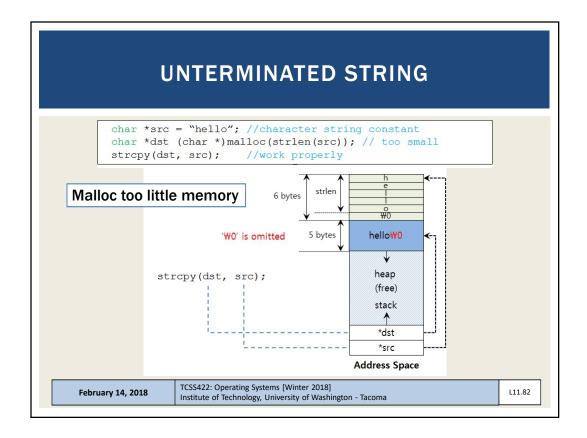


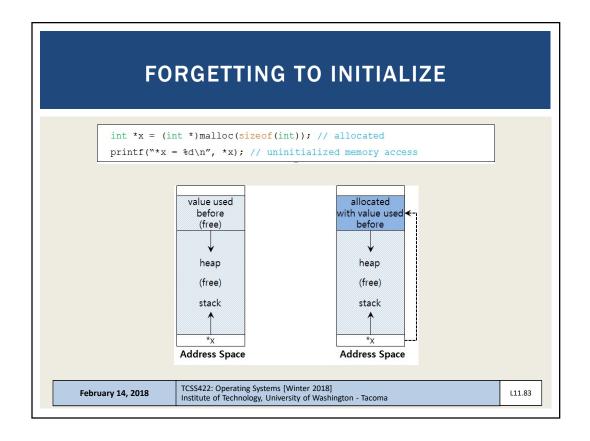


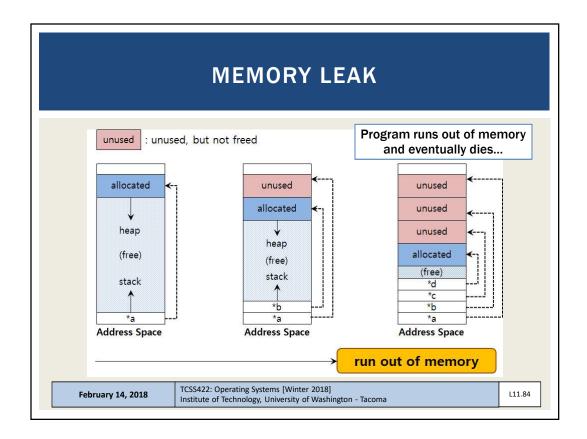












```
#include<stdio.h>
                                What will this code do?
int * set_magic_number_a()
Ł
  int a = 53247;
  return &a;
}
void set_magic_number_b()
£
  int b = 11111;
}
int main()
Ł
  int * x = NULL;
 x = set_magic_number_a();
  printf("The magic number is=%d\n",*x);
  set_magic_number_b();
  printf("The magic number is=%d\n",*x);
  return 0;
                                                       85
}
```

```
#include<stdio.h>
                               What will this code do?
int * set_magic_number_a()
Ł
  int a = 53247;
  return &a;
                                      Output:
}
                            $ ./pointer error
                            The magic number is=53247
void set_magic_number_b()
                            The magic number is=11111
{
 int b = 11111;
}
                            We have not changed *x but
int main()
                              the value has changed!!
Ł
 int * x = NULL;
                                       Why?
 x = set_magic_number_a();
  printf("The magic number is=%d\n",*x);
  set_magic_number_b();
  printf("The magic number is=%d\n",*x);
  return 0;
}
                                                     86
```

