













	TLB BASIC ALGO	RITHM				
= F = H	or: array based page table ardware managed TLB					
*	1: VPN = (VirtualAddress & VPN_MASK) >> SHIF	T				
-						
4: if (CanAccess (TlbEntry.ProtectBits) == True) {						
	5: Offset = VirtualAddress & OFFSET_MASK					
	6: PhysAddr (TlbEntry.PFN << SHIFT)	Offset				
	7: AccessMemory(PhysAddr)					
	8: }else RaiseException(PROTECTION_ERROR)					
	Generate the physical address to a	ccess memory				
F	ebruary 27, 2017 TCSS422: Operating Systems [Winter 2017] Institute of Technology, University of Washington	n - Tacoma				











0: int sum = 0; orisin 1: for(i=0; i<10; i++){ vvv.=0 2: sum+=a[i]; vvv.=0 3:) vvv.=0 vvv.=0 Page size vvv.=0 vvv.=0 uvv.=0 * Page size vvv.=0 uvv.=0 uvv.=0	0 ; i<10; i++) { um+=a(i); VPN = 05 VPN = 05 VPN = 05	SET 12 1
Temporal locality WN - 09 WN - 10 WN - 12 WN - 12 WN - 13 WN - 13	VN - 65 VN - 65 VN - 07 ultil 401 VN - 07 ultil 401 VN - 08 ultil 401 VN - 10 ultil 401 VN - 13 ultil 401 VN - 15 ultil 401	a[1] a[2 a[5] a[6 a[9]























SI	IARE	D M	EM	ORY	′ SP	ACE	
	VDN	DEN	wallid		ACID		
	10	101	valla 1	prot	1		
	- 10		-	-	-		
	50	101	1	rwx	2		
	-	-	-	-	-		
When proce Shared lib	esses s raries d	hare ok	a co	ode (bage		
 Code page not RWX 	es typic	ally	are F	₹X,	u	Sharing of pages is seful as it reduces the number of physical pages in use.	
February 27, 2017	TCSS422: Operating Systems [Winter 2017] Institute of Technology, University of Washington - Tacoma						







