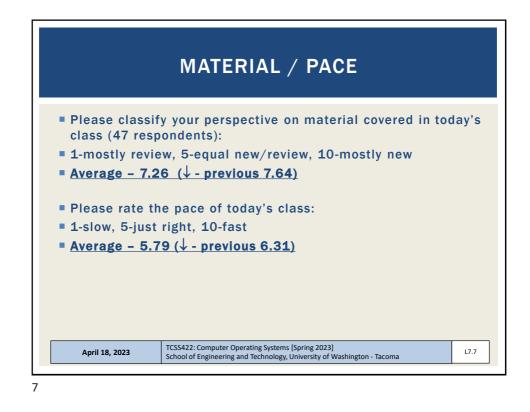
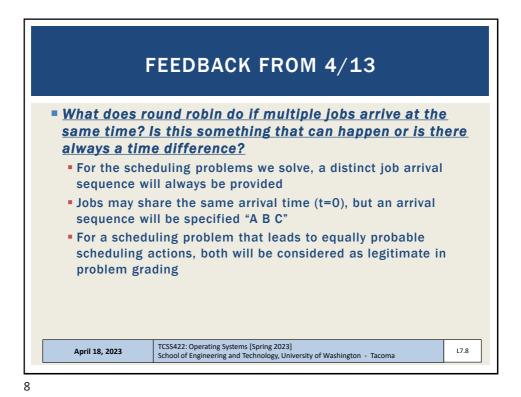


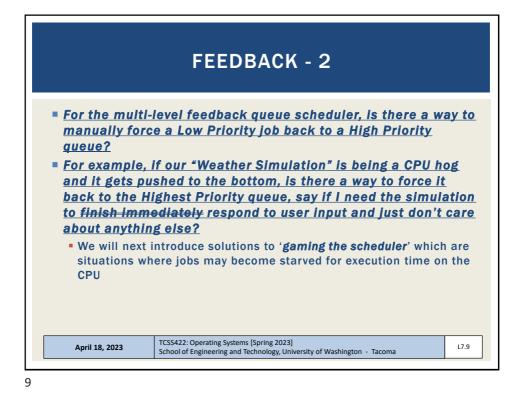


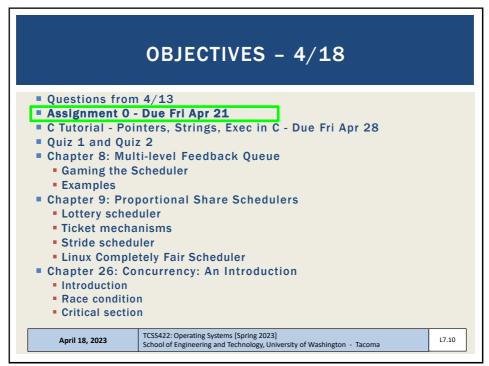
ONLII	NE DAILY I	FEEDBACK SURVEY
 Extra credit a Tuesday surv 		
- mursuay sur		> Assignments
	Spring 2021 Home	Search for Assignment
	Announcements Zoom	 Upcoming Assignments
	Syllabus Assignments	TCSS 422 - Online Daily Feedback Survey - 4/1 Available until Apr 5 at 11:59pm Due Apr 5 at 10pm -/1 pts
April 18, 2023	Discussions TCSS422: Computer Opera	ting Systems [Spring 2023] Technology, University of Washington - Tacoma

(Quiz	Instru	ctio	ns									
-												_	
		Questio	n 1								0.5 p	ts	
		On a sca class:	le of 1	to 10, p	lease cla	assify yo	ur persp	ective o	n materi	al cove	ered in today's	5	
		1	2	3	4	5	6	7	8	9	10		
		Mostly Review	to Me		Nex	Equal and Rev	tiew				Mostly New to Me		
							200				new come		
	L												
		Questio	n 2								0.5 pt	its	
		Please ra	te the r	nace of t	today's o	lass:							
		1	2	3	4	5	6	7	8	9	10		
		Slow	-	-		st Right	-		-		Fast		
						_							
April 18, 202	3		TCSS	422: Co	ompute aineerir	r Opera	ting Sys	stems [S pay, Uni	Spring 2 versity o	023] of Was	hington - Tac	coma	

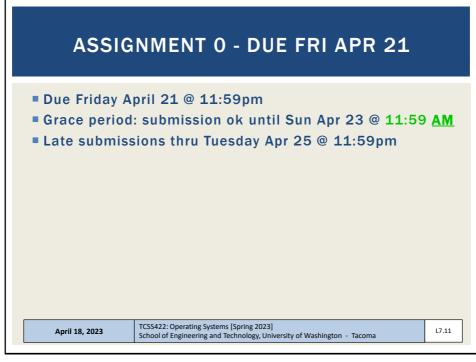


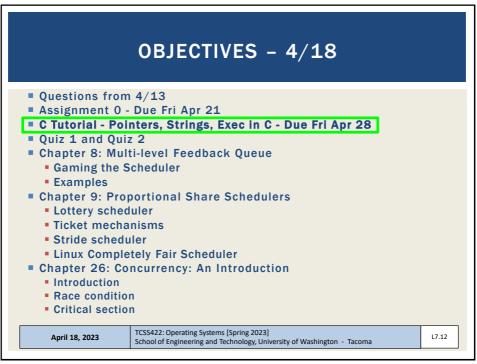






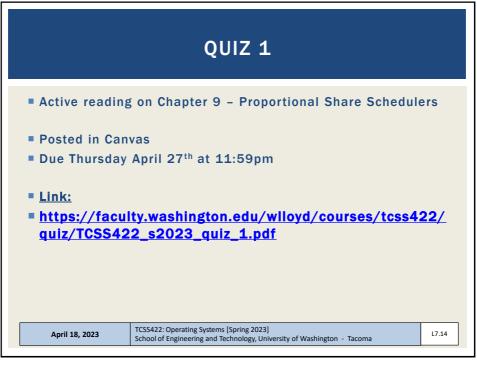




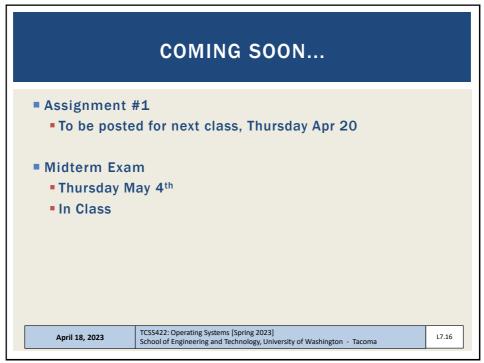




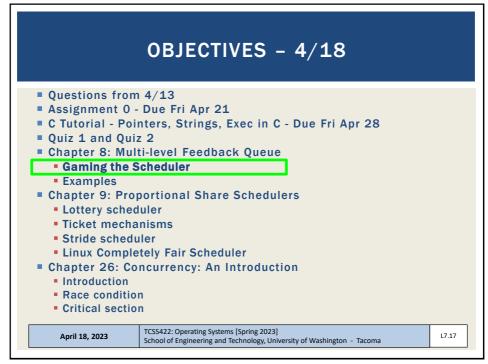


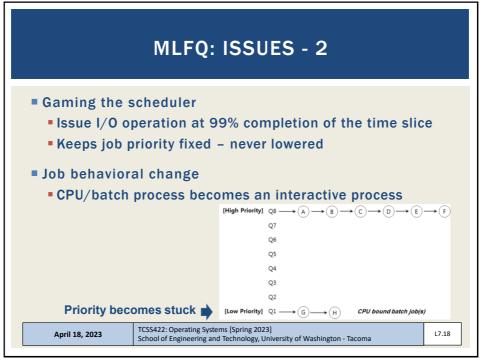


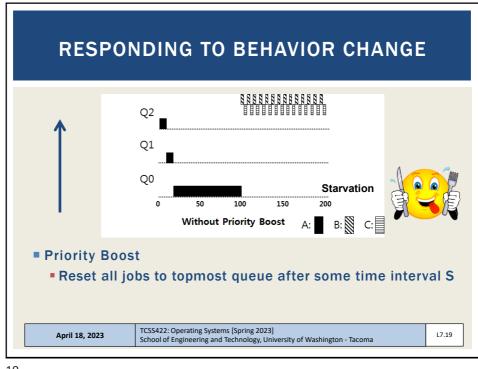
 Canvas Quiz - Practice CPU Scheduling Problems Posted in Canvas Unlimited attempts permitted Due Tuesday May 2nd at 11:59pm Link: https://canvas.uw.edu/courses/1642522/assignments/8316759 		QUIZ 2	
 Unlimited attempts permitted Due Tuesday May 2nd at 11:59pm Link: https://canvas.uw.edu/courses/1642522/assignments/8316759 	Canvas Quiz	- Practice CPU Scheduling Problems	
https://canvas.uw.edu/courses/1642522/assignments/8316759 T(5542: Operating Systems [Spring 2023]	 Unlimited att 	empts permitted	
TCSS422: Operating Systems [Spring 2023]		s.uw.edu/courses/1642522/assignments/8316	<u>759</u>
April 18, 2023 Cobact of Consistence of Machine 2023 Long and Constant of Machineten Teacone L7.15			

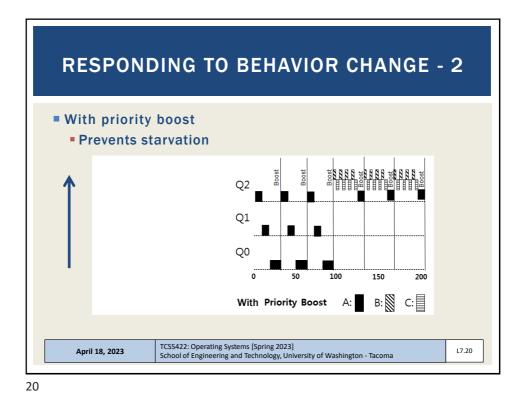




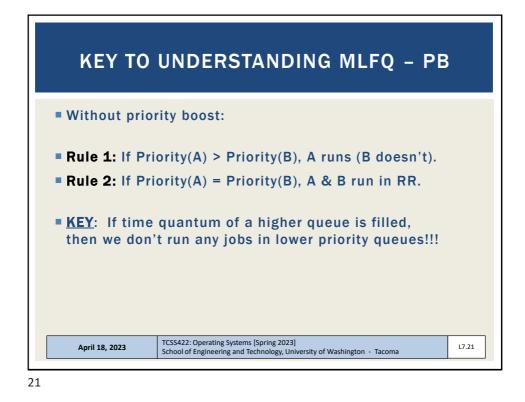


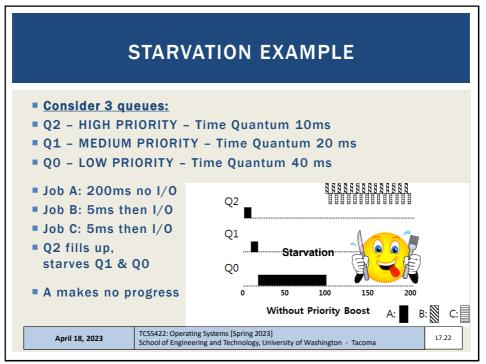




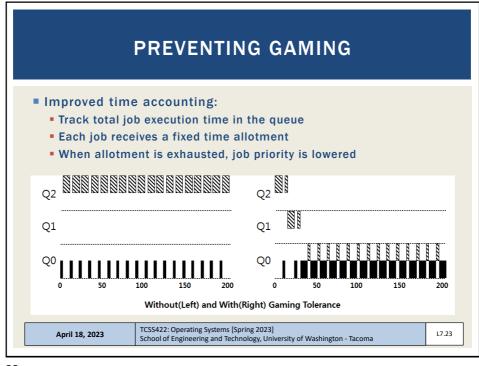


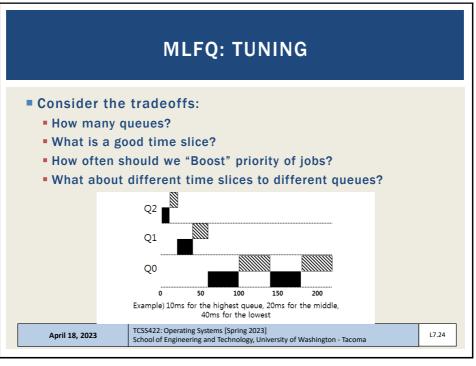
Slides by Wes J. Lloyd

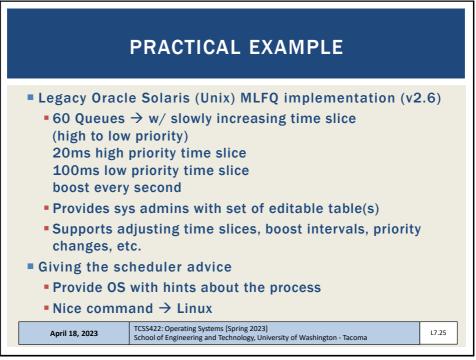


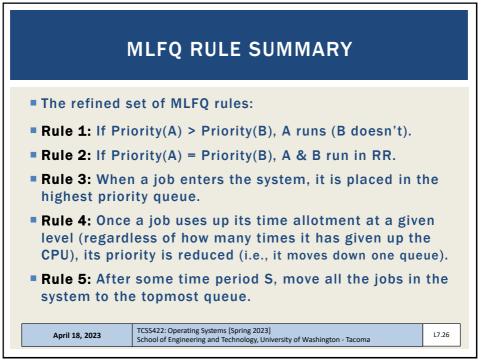


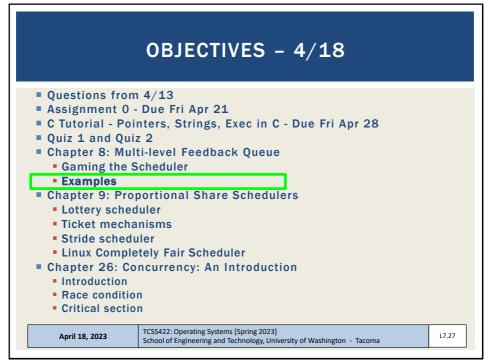


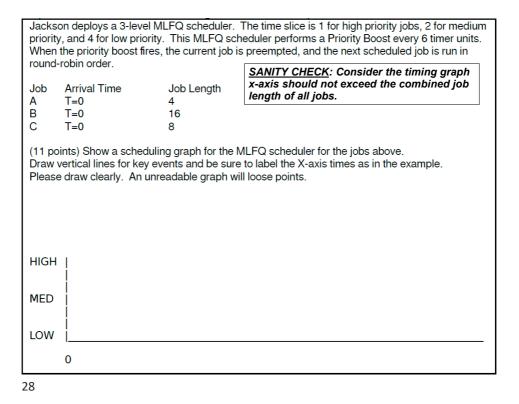


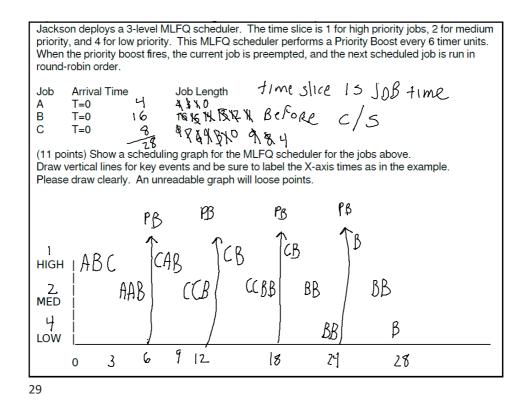


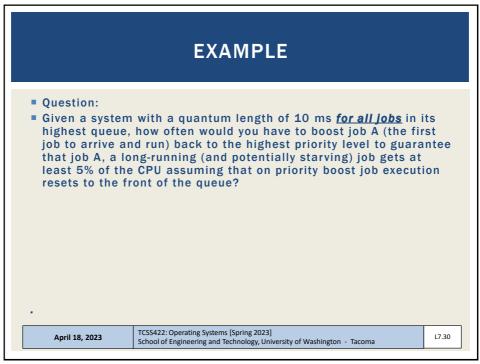


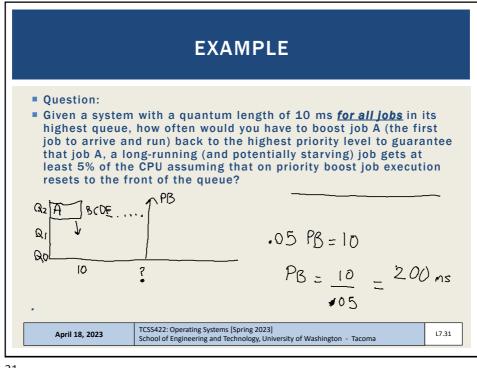


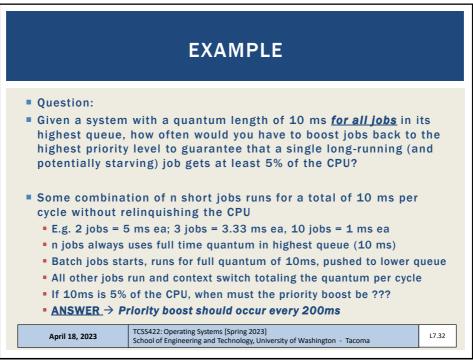




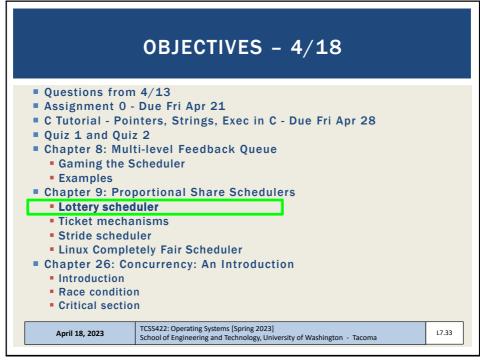


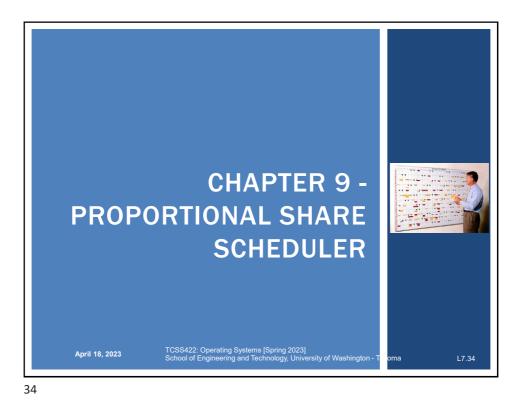




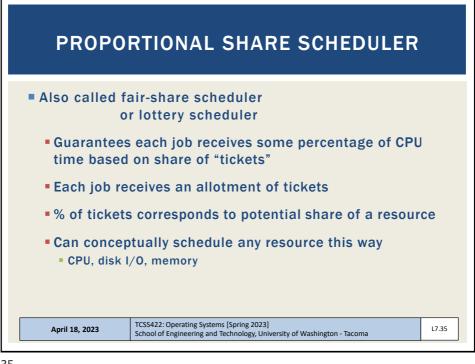


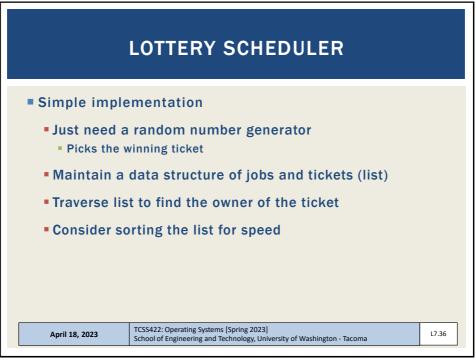




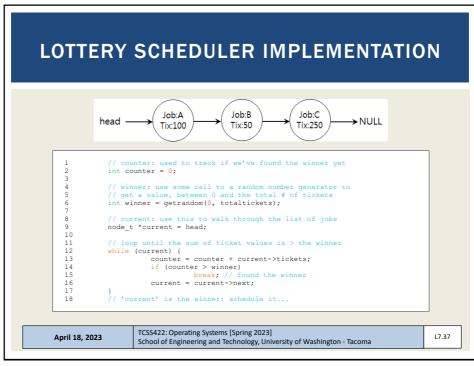


Slides by Wes J. Lloyd

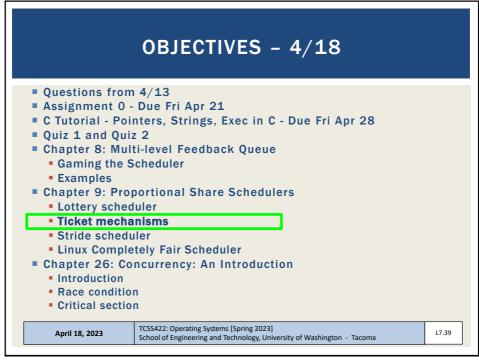


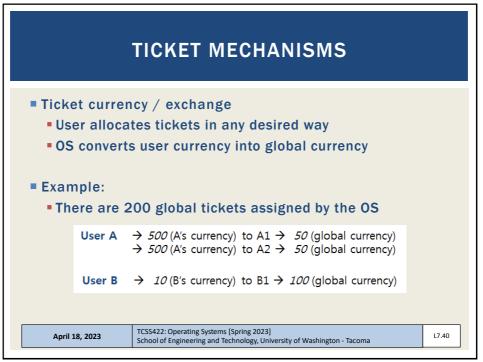


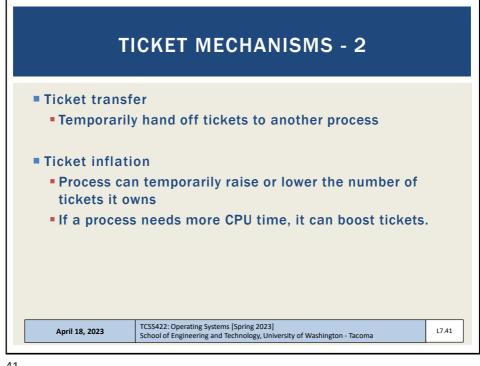


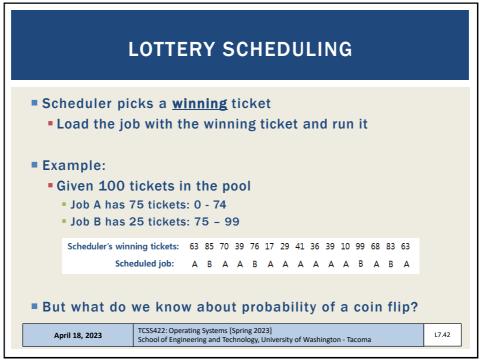


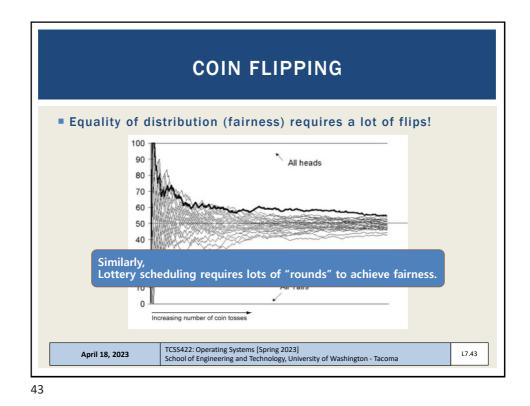


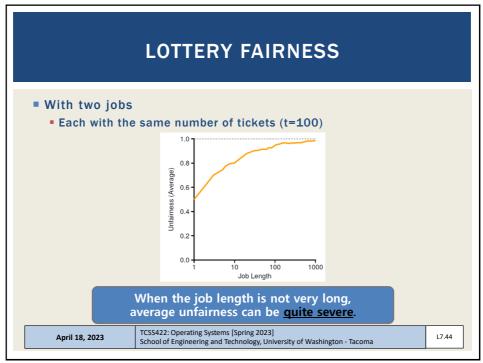


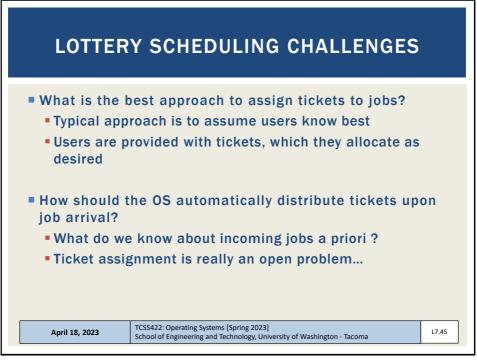


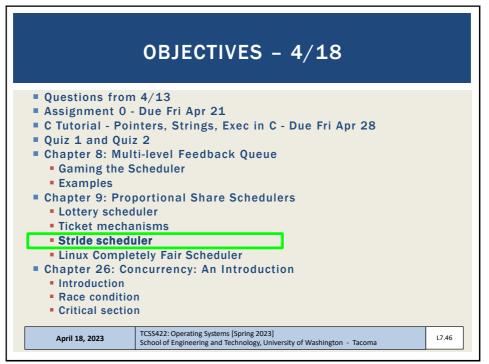




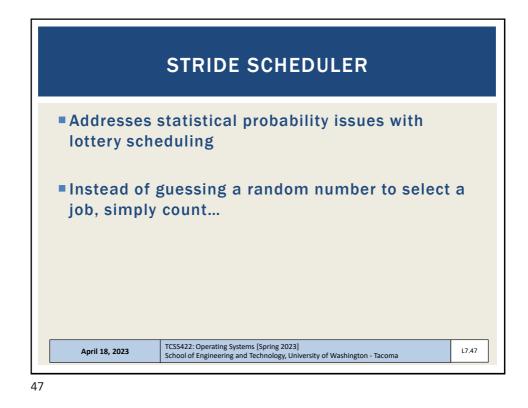


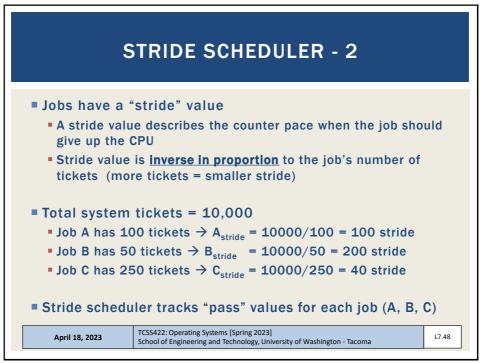


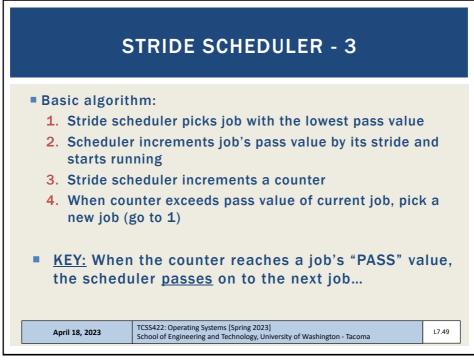


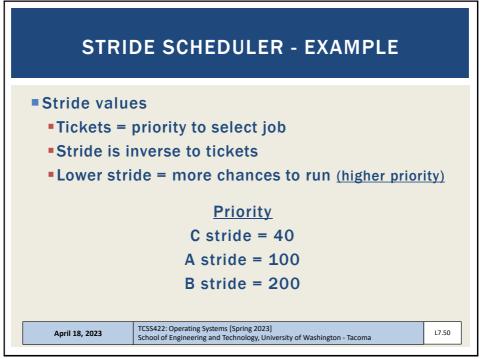


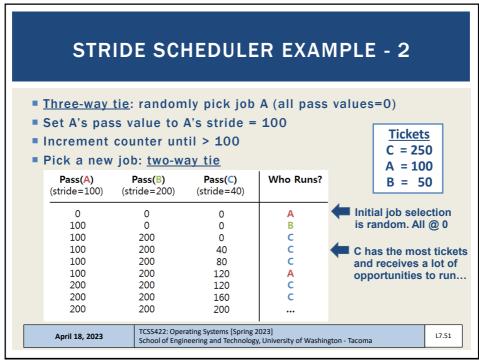


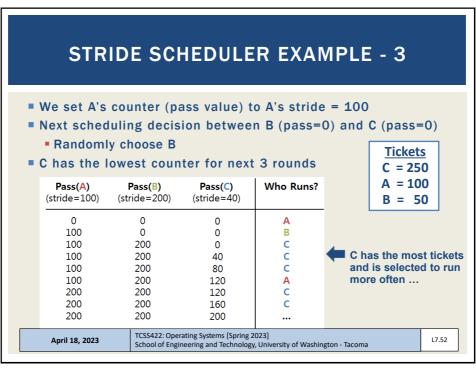


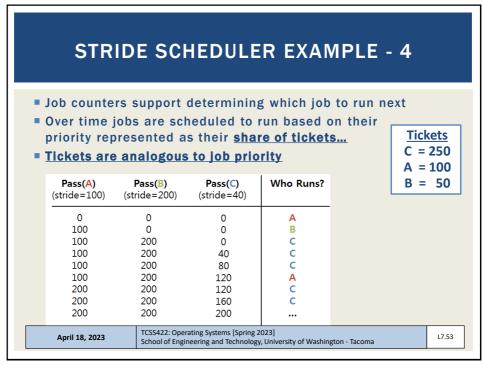


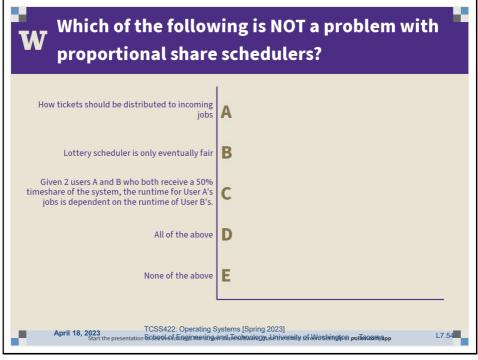


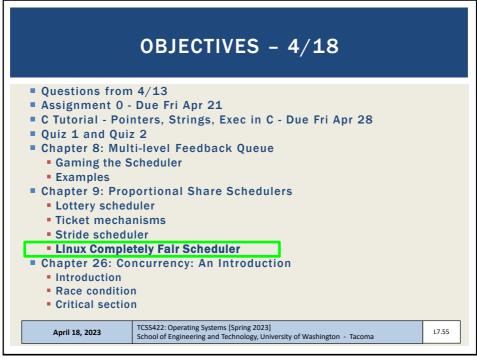


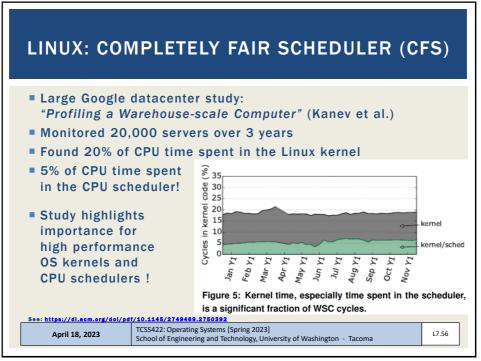


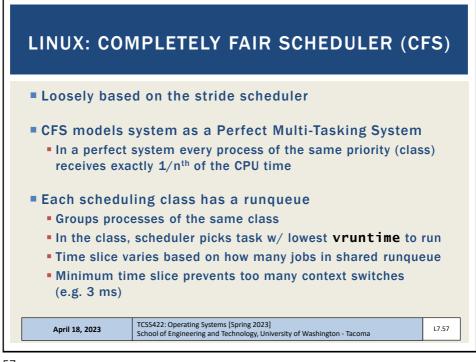


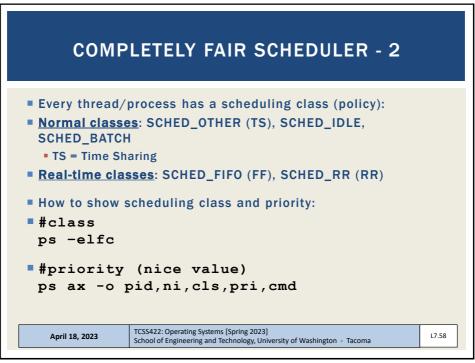


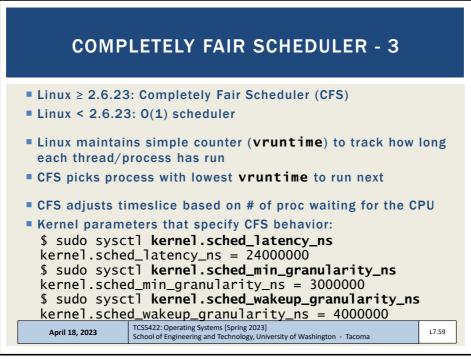


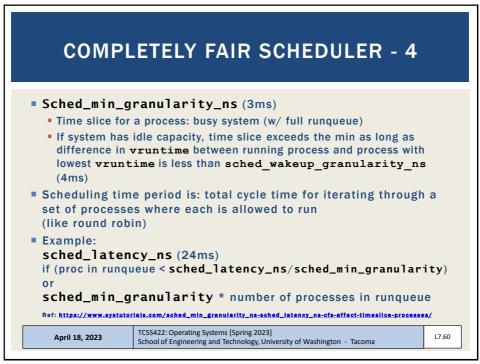




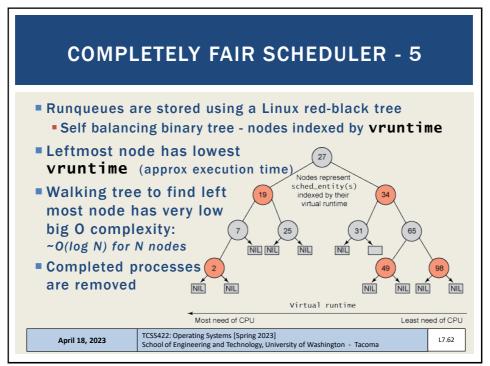


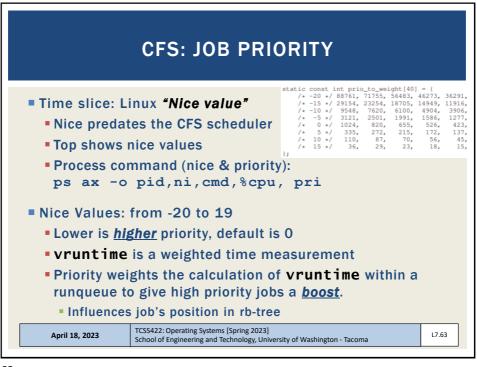


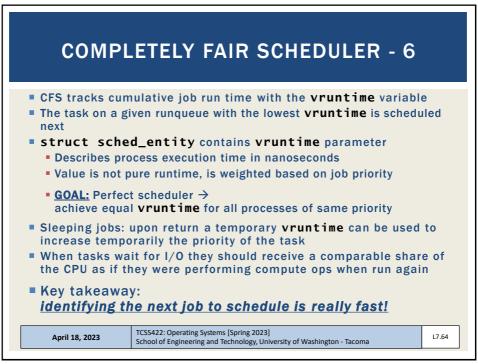




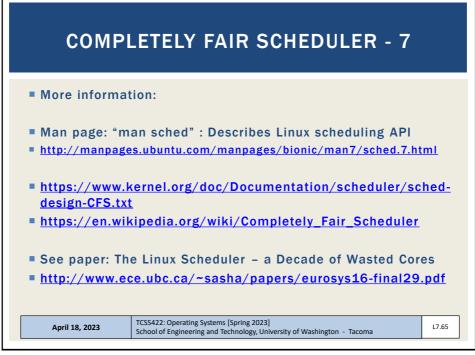
	CFS TRADEOFF
■ <u>HIGH</u>	sched_min_granularity_ns(timeslice) sched_latency_ns sched_wakeup_granularity_ns
	ares reduced context switching $ ightarrow$ less overhead r-term fairness
LOW	sched_min_granularity_ns (timeslice) sched_latency_ns
	sched_wakreup_granularity_ns
	•

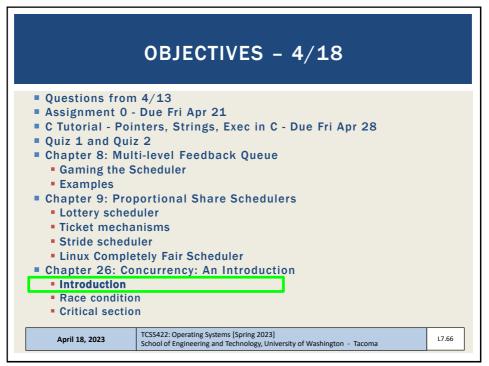




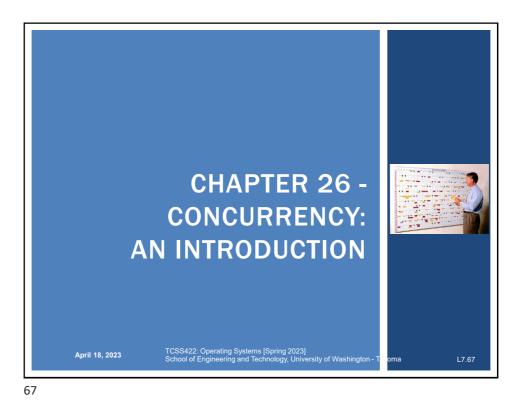




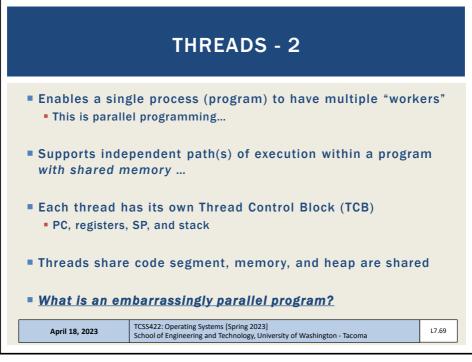


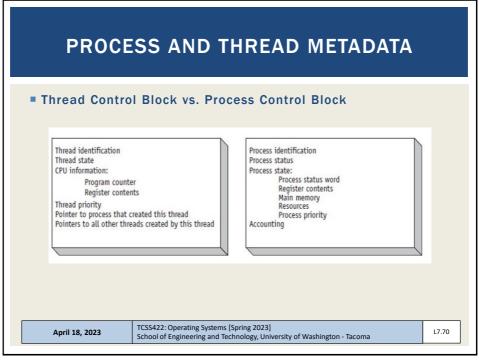




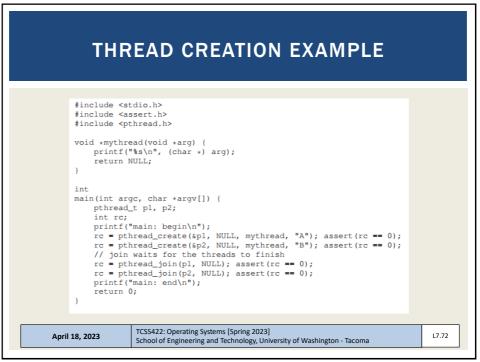


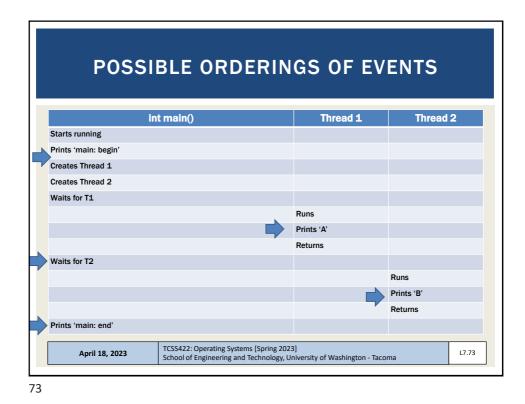
THREADS Multithreaded Process Process Thread State Thread State Thread State Process State: PC, Process State: PC, registers, SP, etc.. registers, SP, etc. Code Segment Single Multiple Data Seg SHA Threaded Data Segment Threaded Process Process Stack Stack ©Alfred Park, http://randu.org/tutorials/threads TCSS422: Operating Systems [Spring 2023] School of Engineering and Technology, University of Washington - Tacoma April 18, 2023 L7.68





	3 ⊓ <i>F</i>	ARED ADDR	(ESS SP/	ACE
Every	thread has	s it's own stack /	PC	
окв	Program Code	The code segment: where instructions live	ОКВ	Program Code
1KB -	Heap	The heap segment: contains malloc'd data	1KB - 2KB -	Неар
210	(free)	dynamic data structures (it grows downward)	210	(free)
				Stack (2)
15KB		(it grows upward) The stack segment: contains local variables	15KB	(free)
15KB	Stack (1)	arguments to routines, return values, etc.	16KB	Stack (1)
Δ	Single-Threade Address Space	d		Two threaded Address Space

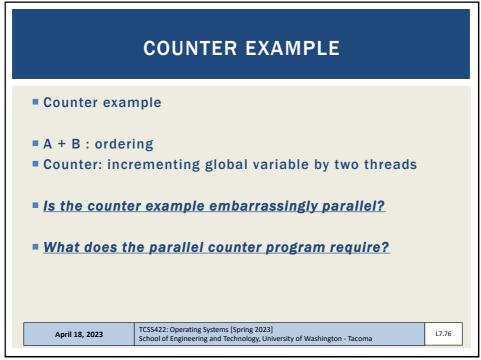




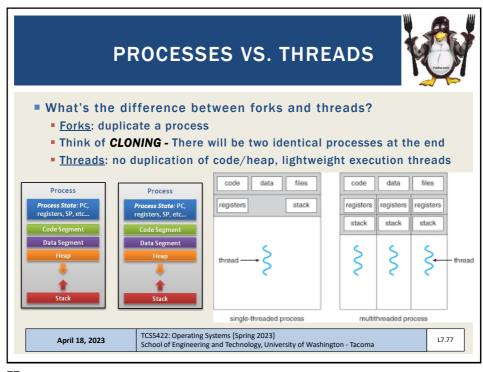
POSSIBLE ORDERINGS OF EVENTS - 2

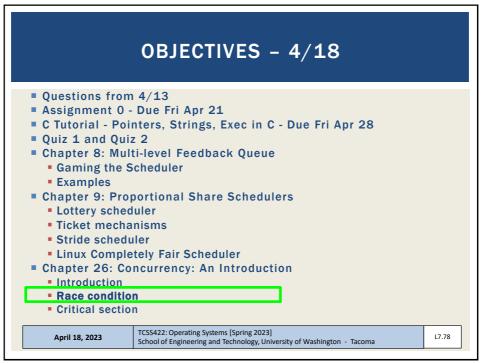
	int main()	Thread 1	Threa	d 2
Starts running				
Prints 'main: begin'				
Creates Thread 1				
		Runs		
		Prints 'A'		
		Returns		
Creates Thread 2				-
			Runs	
			Prints 'B'	
			Returns	
Waits for T1		Returns immediately	_	
Waits for T2			Returns imme	diately
Prints 'main: end'				
April 18, 2023	TCSS422: Operating Systems [Spring 202 School of Engineering and Technology, U		ma	L7.74

POSSIB	LE ORDERING	S OF EVEN	NTS - 3
ir	nt main()	Thread 1	Thread 2
Starts running			
Prints 'main: begin'			
Creates Thread 1			7
Creates Thread 2			
w	hat if executio	on order o	of
	hat if executions in the prog	ram matte	
even			
even		ram matte	
even		ram matte Runs Prints 'A'	
Waits for T: EVEN		ram matte Runs Prints 'A'	ers?











		RAU	E CONDITIO	N			
What	ic honn	oning with	our countor?				
		-	our counter?				
• wh	en counte	er=50, consid	er code: counter =	coun	iter -	- 1	
If s	ynchroniz	zed, counter v	vill = 52				
r	OS	Thread1	Thread2			ruction) counter	
		before criti	cal section	100	0	50	-
1		mov 0x8049a1	c, %eax	105	50	50	
L		add \$0x1, %e	ax	108	51	50	
¢	interrupt save T1's						
Г	restore T			100	0	50	٦
	1000010 1	00000	mov 0x8049a1c, %eax	105	-	50	
1			add \$0x1, %eax	108		50	
L			mov %eax, 0x8049a1c			51	
	interrupt save T2's	state					-
	restore T			108	51	50	
		mov %eax, 0x	9049510	113	51	51	

