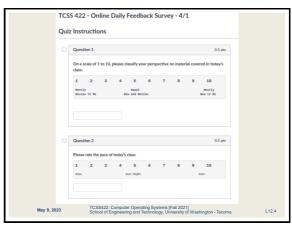
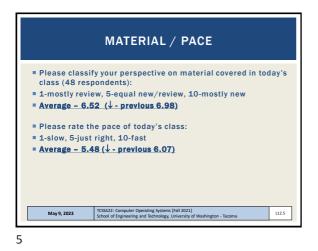
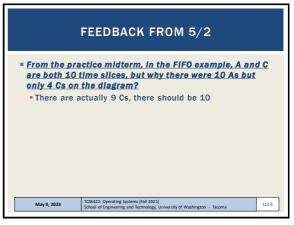
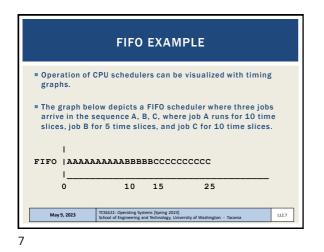


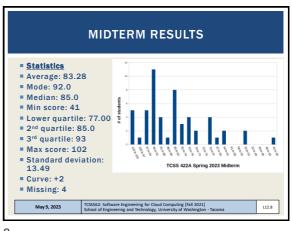
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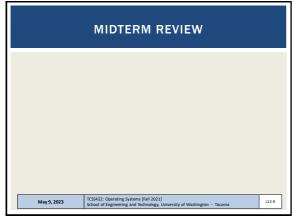




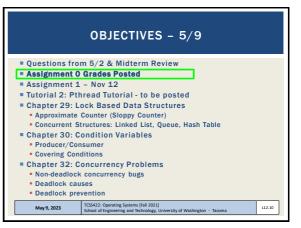


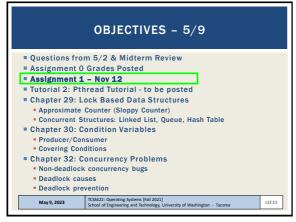






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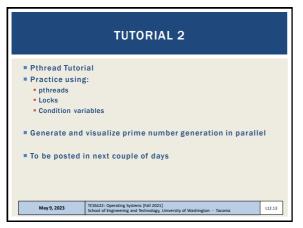


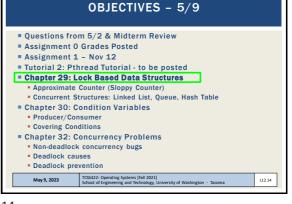




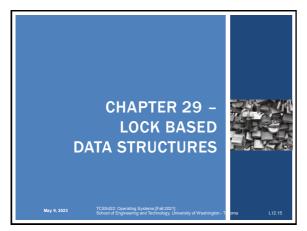
	OBJECTIVES - 5/9			
Questions fro	m 5/2 & Midterm Review			
Assignment 0 Grades Posted				
Assignment 1	- Nov 12			
Tutorial 2: Pthread Tutorial - to be posted				
Chapter 29: Lock Based Data Structures				
Approximate	Counter (Sloppy Counter)			
Concurrent S	tructures: Linked List, Queue, Hash Table			
Chapter 30: C	ondition Variables			
Producer/Cor				
Covering Conditions				
	oncurrency Problems			
Non-deadlock concurrency bugs				
Deadlock causes				
Deadlock prevention				
May 9, 2023 TCSS422: Operating Systems [Fall 2021] School of Engineering and Technology, University of Washington - Tacoma				



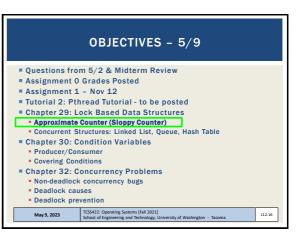


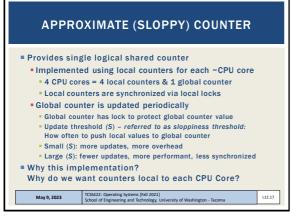


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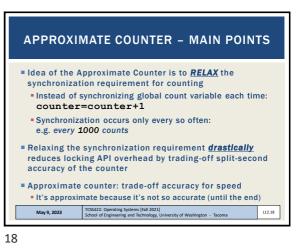


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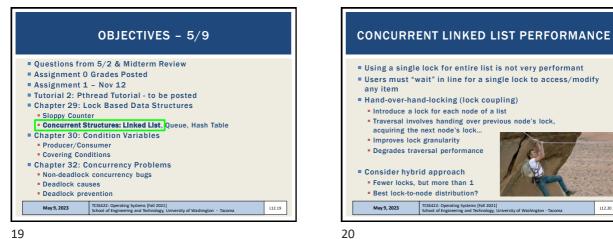




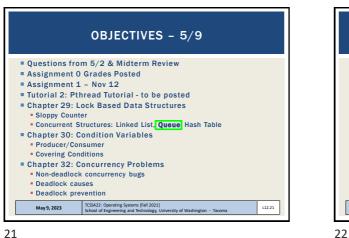


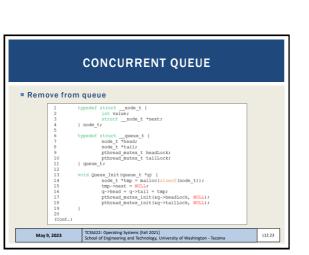


L12.20

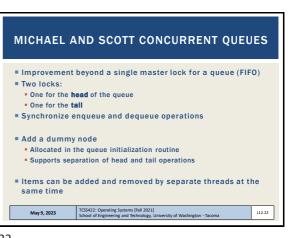


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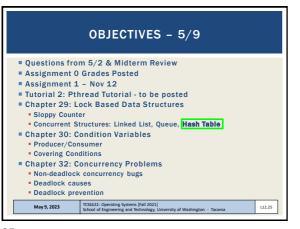


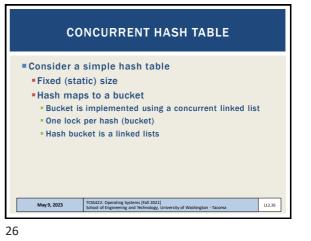


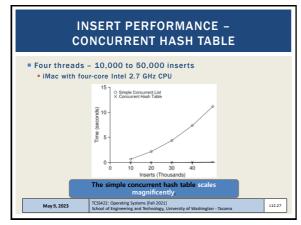
23



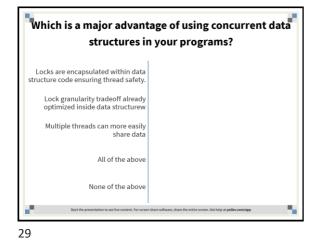
CONCURRENT QUEUE - 2 Add to queue (Cont.) 21 22 23 24 25 26 27 28 29 30 31 32 (Cont.) (Cont.) void Queue_Enqueue(queue_t *q, int value) {
 node_t *tmp = malloc(sizeof(node_t));
 assert(tmp != NULL); tmp->value = value; tmp->next = NULL; pthread_mutex_lock(&q->tailLock); q->tail->next = tmp; q->tail = tmp; pthread_mutex_unlock(&q->tailLock); } TCSS422: Operating Systems [Fall 2021] School of Engineering and Technology, University of Washington - Tacoma May 9, 2023 L12.24

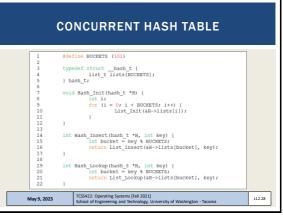


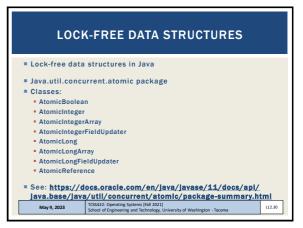




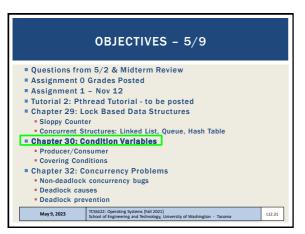
27











CONDITION VARIABLES

There are many cases where a thread wants to

Consider when a precondition must be fulfilled

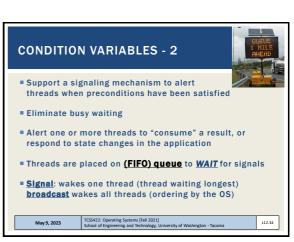
before it is meaningful to proceed ...

wait for another thread before proceeding with



33

execution



CHAPTER 30 -

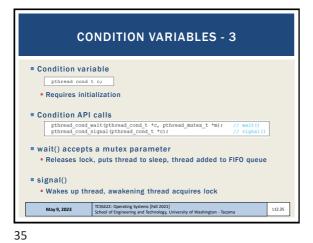
CONDITION VARIABLES

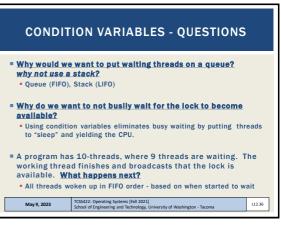
May 9, 2023

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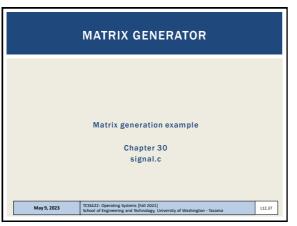
L12.33

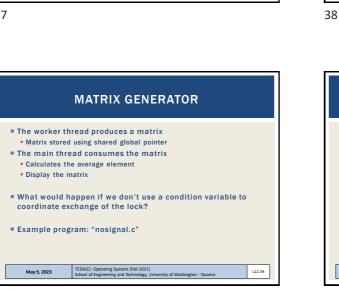




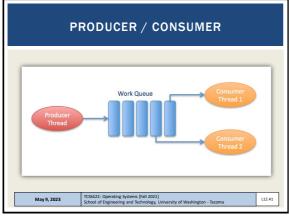


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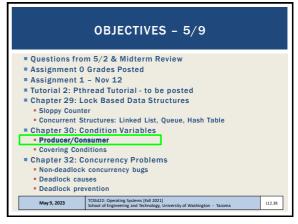


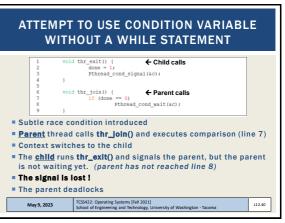


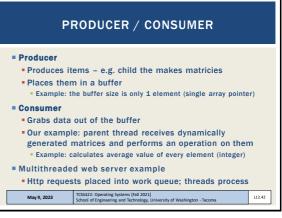
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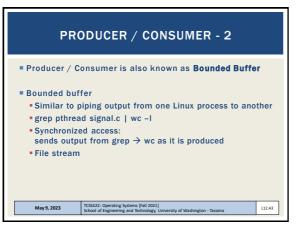






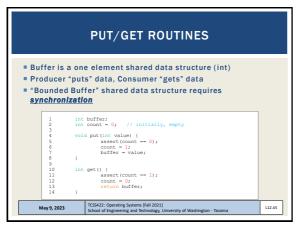




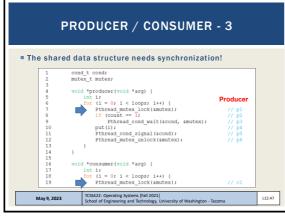




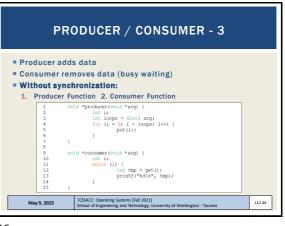
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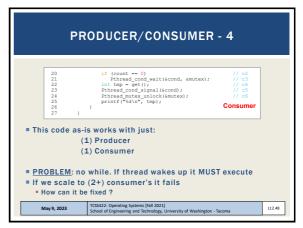


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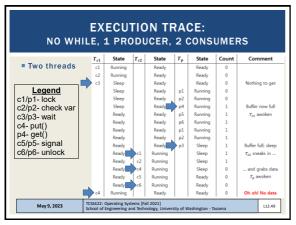


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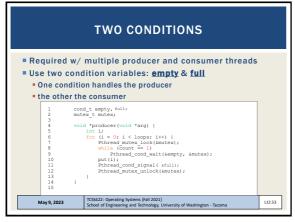




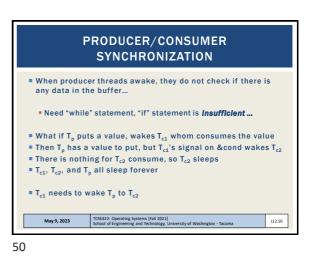


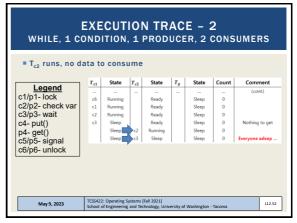
EXECUTION TRACE: WHILE, 1 CONDITION, 1 PRODUCER, 2 CONSUMERS								
	T _{c1}	State	T_{c2}	State	Tp	State	Count	Comment
	c1	Running		Ready		Ready	0	
	c2	Running		Ready		Ready	0	
	c3	Sleep		Ready		Ready	0	Nothing to get
Legend		Sleep	c1	Running		Ready	0	
c1/p1-lock		Sleep	c2	Running		Ready	0	
c2/p2- check var		Sleep	c3	Sleep		Ready	0	Nothing to get
c3/p3- wait		Sleep		Sleep	pl	Running	0	
c4- put()		Sleep		Sleep	p2	Running	0	
p4- get()		Sleep		Sleep	p4	Running	1	Buffer now full
c5/p5- signal		Ready		Sleep	p5	Running	1	T_{c1} awoken
		Ready		Sleep	p 6	Running	1	
c6/p6- unlock		Ready		Sleep	p1	Running	1	
		Ready		Sleep	p2	Running	1	
_		Ready		Sleep	p3	Sleep	1	Must sleep (full)
	c2	Running		Sleep		Sleep	1	Recheck condition
	c4	Running		Sleep		Sleep	0	τ_{c1} grabs data
	c5	Running		Ready		Sleep	0	Oops! Woke T _{c2}
May 9, 2023		Operating Sys f Engineering a			rsity of	Washington - 1	lacoma	L12.51

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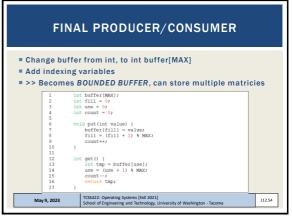


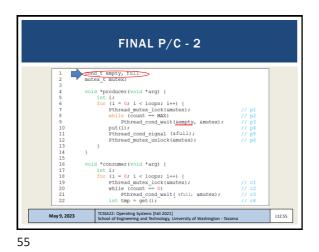
53





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 FINAL P/C - 3

 (cont.)

 \$23\$
 Fthread_cond_signal (sampty);
 // cd

 \$24\$
 print("tdun", tap);
 // cd

 \$26\$
 print("tdun", tap);
 // cd

 \$27\$
 print("tdun", tap);
 // cd

 \$26\$
 print("tdun", tap);
 // cd

 \$27\$
 print("tdun", tap);
 // cd

 \$26\$
 print("tdun", tap);
 // cd

 \$27\$
 print("tdun", tap);
 // cd

 \$26\$
 print("tdun", tap);
 // cd

 \$27\$
 print("tdun", tap);
 // cd

 \$26\$
 print("tdun", tap);
 // cd
 // cd

 \$27\$
 print("tdun", tap);
 // cd
 // cd

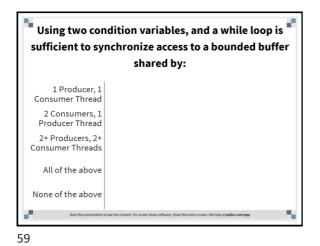
 \$27\$
 print("tdun", tap);
 // cd
 // cd

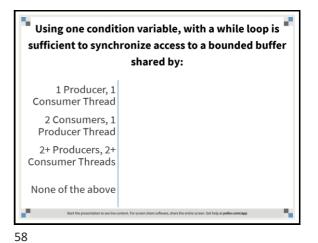
 \$27\$
 print("tdun", tap);
 // cd
 // cd

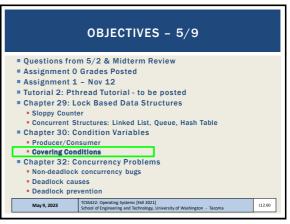
 \$28\$
 print("tdun", tap);</tdun", tap);</td>
 // cd
 // cd</

Using one condition variable, and no while loop is sufficient to synchronize access to a bounded buffer shared by: 1 Producer, 1 Consumer Thread 2 Consumers, 1 Producer Thread 2+ Producers, 2+ Consumer Threads All of the above None of the above

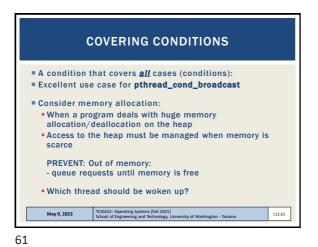
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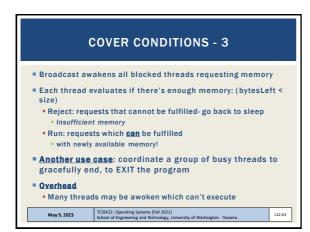




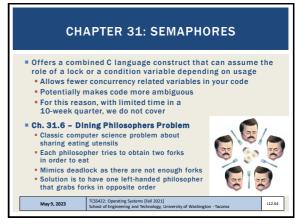
 COVERING CONDITIONS - 2

 // Introduction to condition to conditing to condition to condition to condition to condition

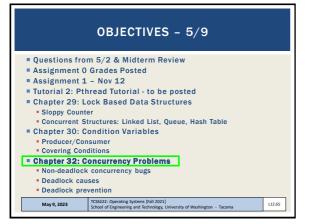
62

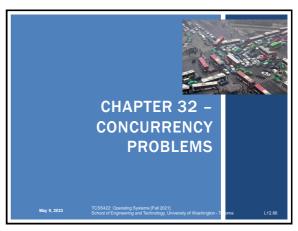




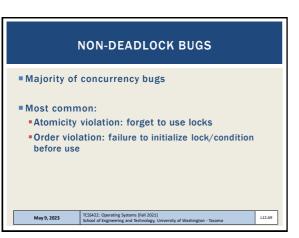


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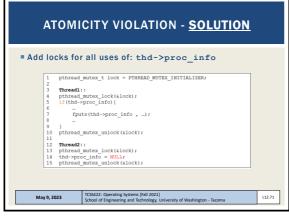




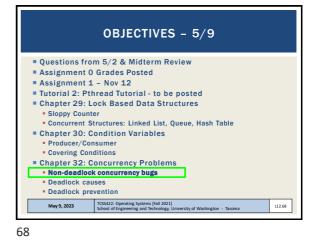
	CONCURRENCY BUGS IN OPEN SOURCE SOFTWARE						
Re • :	 "Learning from Mistakes – A Comprehensive Study on Real World Concurrency Bug Characteristics" Shan Lu et al. Architectural Support For Programming Languages and Operating Systems (ASPLOS 2008), Seattle WA 						
	Application	1	What it does	Non-Deadlock	Deadlock		
	MySQL		Database Server	14	9		
	Apache		Web Server	13	4		
	Mozilla		Web Browser	41	16		
	Open Office	9	Office Suite	6	2		
	Total			74	31		
May 9, 2023 TCSS422: Operating Systems [Fall 2021] School of Engineering and Technology. University of Washington - Tacoma 112.67							

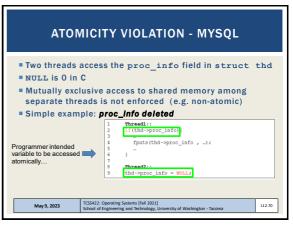


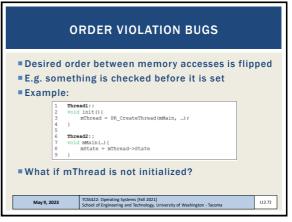
69



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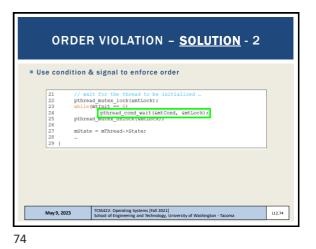








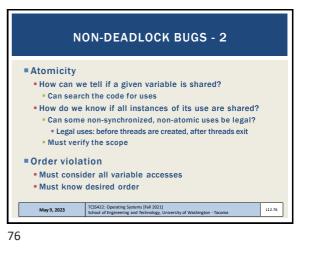
ORI	DER VIOLATION - SOLUTION	
Use condition	on & signal to enforce order	
	<pre>mutex_t mtLock = PTHREAD_MUTEX_INITIALIZER;</pre>	
	cond_t mtCond = PTHREAD_COND_INITIALIZER;	
3 int mtIn	it = 0;	
5 Thread 1		
6 void ini	t(){	
7		
	ead = PR_CreateThread(mMain,);	
9	ignal that the thread has been created.	
	ead mutex lock(&mtLock);	
	it = 1;	
	ead cond signal(&mtCond);	
	ead_mutex_unlock(&mtLock);	
15		
16 }		
18 Thread2:		
19 void mMa	in(){	
20		
	TCSS422: Operating Systems [Fall 2021]	112



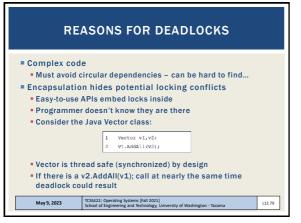
NON-DEADLOCK BUGS - 1 = 97% of Non-Deadlock Bugs were = Atomicity = Order violations = Consider what is involved in "spotting" these bugs in code = >> no use of locking constructs to search for = Desire for automated tool support (IDE) May 9.2023 TCS422: Operating Systems [Fall 2021] School of Engineering and Rebounger, University of Washington - Tecoma

DEADLOCK BUGS 1 Presence of a cycle in code Thread 1 acquires lock L1, waits for lock L2 Thread 2 acquires lock L2, waits for lock L1 Thread 1: Thread 2: lock(L1); lock(L2); ock L1 Thread lock(L2); lock(L1); Wanted by Wanted by Both threads can block, unless one manages to acquire both locks Thread 2 Lock L2 Holds TCSS422: Operating Systems [Fall 2021] School of Engineering and Technology, University of Washington - Tacoma May 9, 2023 L12.77

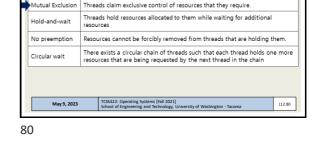
77



OBJECTIVES - 5/9 Questions from 5/2 & Midterm Review Assignment 0 Grades Posted Assignment 1 - Nov 12 Tutorial 2: Pthread Tutorial - to be posted Chapter 29: Lock Based Data Structures Sloppy Counter Concurrent Structures: Linked List, Queue, Hash Table Chapter 30: Condition Variables Producer/Consumer Covering Conditions Chapter 32: Concurrency Problems Non-deadlock concurrency bugs Deadlock causes Deadlock prevention TCSS422: Operating Systems [Fall 2021] School of Engineering and Technology, University of Washington - Tacoma May 9, 2023 L12.78





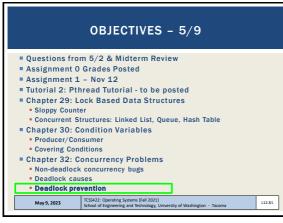


CONDITIONS FOR DEADLOCK

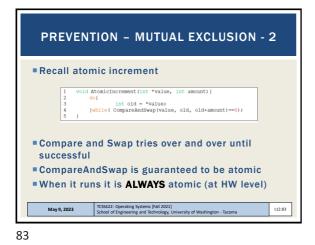
Four conditions are required for dead lock to occur

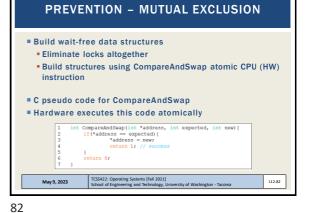
Condition

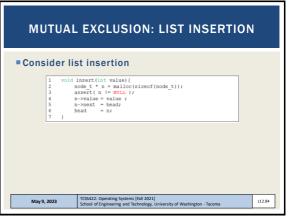
Description



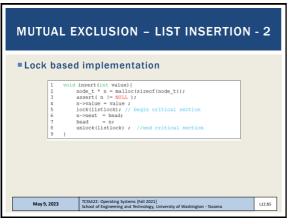
81

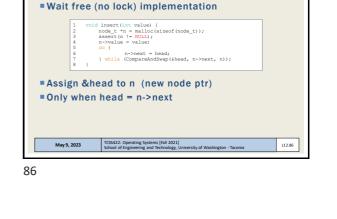




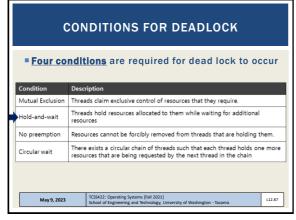




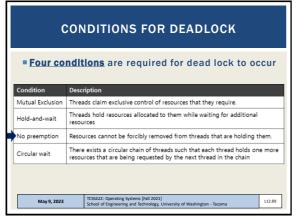




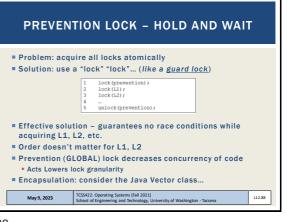
MUTUAL EXCLUSION - LIST INSERTION - 3

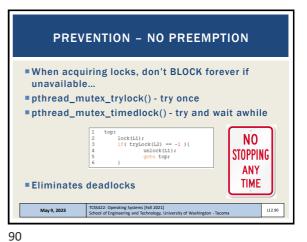


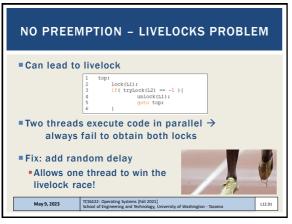
87

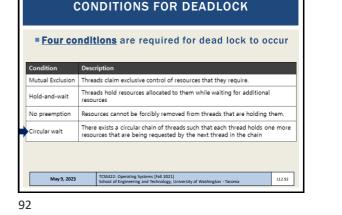


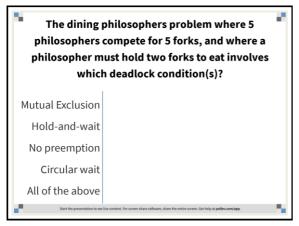
89





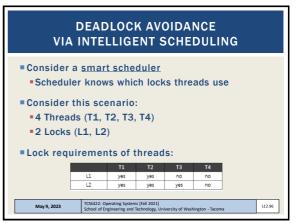




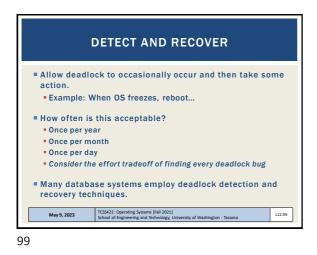


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CONDITIONS FOR DEADLOCK					
If any of the following conditions DOES NOT EXSIST, describe why deadlock can not occur?					
Condition	Description				
Mutual Exclusion	Threads claim exclusive control of resources that they require.				
	Threads claim exclusive control of resources that they require. Threads hold resources allocated to them while waiting for additional resources				
Mutual Exclusion Hold-and-wait No preemption	Threads hold resources allocated to them while waiting for additional				
Hold-and-wait	Threads hold resources allocated to them while waiting for additional resources				



INTELLIGENT SCHEDULING - 2						
Scheduler	Scheduler produces schedule:					
	CPU 1	T3	Т4			
	CPU 2	T1	Т2			
No deadlocConsider:	 No deadlock can occur Consider: 					
	τ1	T2	T3	T4		
	L1 yes	yes	yes	no		
	L2 yes	yes	yes	no		
May 9, 2023	TCSS422: Operating Syst School of Engineering at	ems (Fall 2021) nd Technology, Ur	iversity of Was	hington - Tacom	a	L12.97



INTELLIGENT SCHEDULING - 3					
Scheduler produces schedule					
	CPU 1 T4 CPU 2 T1 T2 T3				
 Scheduler must be conservative and not take risks Slows down execution - many threads There has been limited use of these approaches given the difficulty having intimate lock knowledge about every 					
thread May 9, 2023 TCSS422: Operating Systems [fail 2021] School of Engineering and Technology, University of Washington - Tacoma 112.58					
8					

