

### MATERIAL / PACE

- Please classify your perspective on material covered in today's class (49 respondents):
- 1-mostly review, 5-equal new/review, 10-mostly new
- Average 7.21 ( $\downarrow$  from 7.32)
- Please rate the pace of today's class:
- 1-slow, 5-just right, 10-fast
- Average 5.53 (↓ from 5.63)

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L9.3

### FEEDBACK FROM 4/23

- Is the pthread\_cond\_t data type a queue in itself, OR does the system maintain a queue behind the scenes when an instance of the type is initialized?
- The system maintains a FIFO queue behind the scenes for each condition variable.
- Any thread that waits on the condition adds itself to the queue
- When a signal is fired on the condition variable, threads are woken up in FIFO order

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### FEEDBACK - 2

- Is one instance of the condition data type required per thread requiring access, or is only one instance required per critical region?
- Only one instance of a condition variable is required for each critical region
- Condition variables are associated with a lock variable
- The lock variable protects the critical section
- The condition variable enforces FIFO access to the critical section

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### CONDITION VARIABLE EXAMPLE

Wait example:

```
pthread_mutex_t lock = PTHREAD_MUTEX_INITIALIZER;
pthread_cond_t cond = PTHREAD_COND_INITIALIZER;
pthread mutex lock(&lock);
while (initialized == 0)
   pthread_cond_wait(&cond, &lock);
// Perform work that requires lock
a = a + b;
pthread_mutex_unlock(&lock);
```

- wait puts thread to sleep, and automatically releases lock
- meanwhile: another thread sets the state

State variable set, Enables other thread(s) to proceed above.

pthread mutex lock(&lock); initialized = 1;pthread cond signal (&init); pthread mutex unlock(&lock);

some other code in the program

when awoken, lock is reacquired, state variable passes test, we can then execute a=a+b!!!

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19.6

### **CONDITION VARIABLES**

- Are usually associated with a primitive state variable (int or Boolean)
  - Variable tracks if it is okay to proceed:
  - \*\* Are preconditions for execution met? \*\*
- Introduced in Chatper 27 (Thread API)
- Covered in depth in Chapter 30

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# **OBJECTIVES - 4/28**

- Questions from 4/23
- C Tutorial (Apr 30 11:59p AOE)
- Assignment 1 (May 7 11:59p AOE)
- Chapter 28: Locks
  - Introduction, Lock Granularity
  - Spin Locks, Test and Set, Compare and Swap
- Chapter 29: Lock Based Data Structures
  - Sloppy Counter
  - Concurrent Structures: Linked List, Queue, Hash Table
- Chapter 30: Condition Variables
  - Producer/Consumer
  - Covering Conditions

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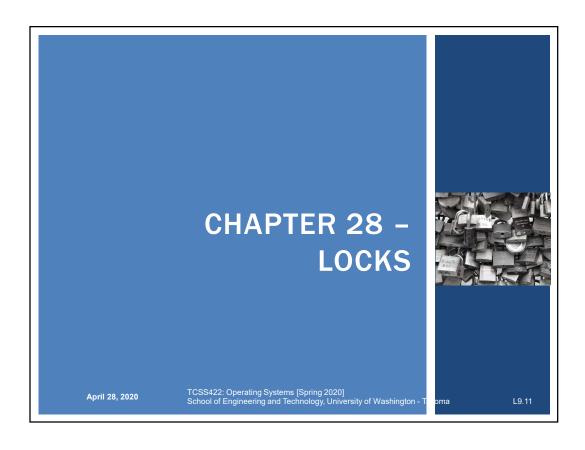
### **MIDTERM**

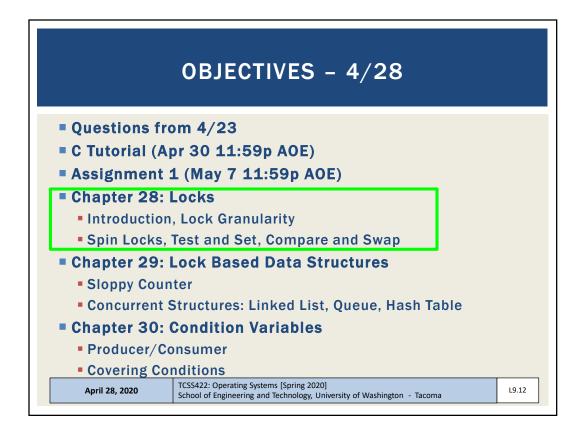
- Tuesday May 5<sup>th</sup>
- ONLINE via Canvas (for 3 hrs 12:30 3:30p)
- Additional hour provided in case of internet issues, etc.
- Open book, note, internet
- Individual work
- **Preparation**:
- Practice quiz: CPU scheduling (to be posted)
  - Auto grading w/ multiple attempts allowed as study aid
- Practice THURSDAY first hour of lecture
  - Series of problems presented with some time to solve
  - Will then work through solutions
- Second hour new material not on midterm

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# LOCKS - 2 Lock variables are called "MUTEX" Short for mutual exclusion (that's what they guarantee) Lock variables store the state of the lock States Locked (acquired or held) Unlocked (available or free) Only 1 thread can hold a lock April 28, 2020 TCSS422: Operating Systems [Spring 2020] School of Engineering and Technology, University of Washington - Tacoma

### LOCKS - 3

- pthread mutex lock(&lock)
  - Try to acquire lock
  - If lock is free, calling thread will acquire the lock
  - Thread with lock enters critical section
    - Thread "owns" the lock
- No other thread can acquire the lock before the owner releases it.

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### LOCKS - 4

- Program can have many mutex (lock) variables to "serialize" many critical sections
- Locks are also used to protect data structures
  - Prevent multiple threads from changing the same data simultaneously
  - Programmer can make sections of code "granular"
    - Fine grained means just one grain of sand at a time through an hour glass
  - Similar to relational database transactions
    - DB transactions prevent multiple users from modifying a table, row, field

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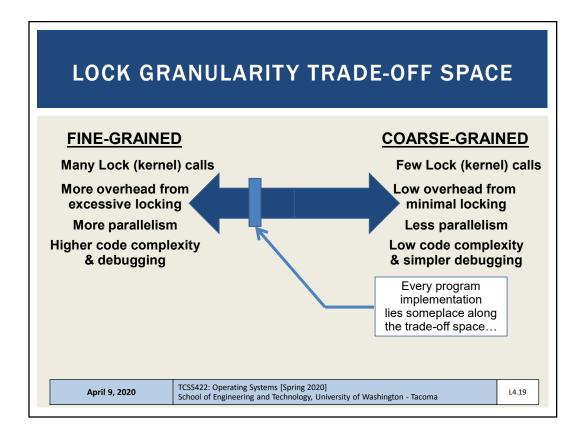
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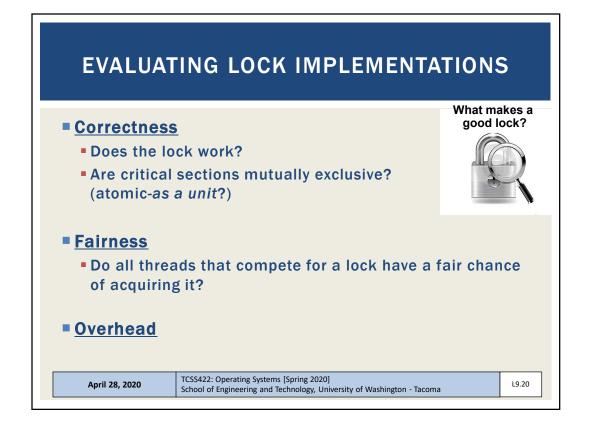
### **FINE GRAINED?** Is this code a good example of "fine grained parallelism"? pthread\_mutex\_lock(&lock); a = b++; b = a \* c; \*d = a + b + c;FILE \* fp = fopen ("file.txt", "r"); fscanf(fp, "%s %s %s %d", str1, str2, str3, &e); ListNode \*node = mylist->head; Int i=0 while (node) { node->title = str1; node->subheading = str2; node->desc = str3; node->end = \*e; node = node->next; i++ e = e - i;pthread\_mutex\_unlock(&lock);

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### FINE GRAINED PARALLELISM pthread\_mutex\_lock(&lock\_a); pthread\_mutex\_lock(&lock\_b); a = b++;pthread\_mutex\_unlock(&lock\_b); pthread\_mutex\_unlock(&lock\_a); pthread\_mutex\_lock(&lock\_b); b = a \* c;pthread\_mutex\_unlock(&lock\_b); pthread\_mutex\_lock(&lock\_d); \*d = a + b + c;pthread\_mutex\_unlock(&lock\_d); FILE \* fp = fopen ("file.txt", "r"); pthread\_mutex\_lock(&lock\_e); fscanf(fp, "%s %s %s %d", str1, str2, str3, &e); pthread\_mutex\_unlock(&lock\_e); ListNode \*node = mylist->head; int i=0 . . . TCSS422: Operating Systems [Spring 2020] April 28, 2020 L9.18 School of Engineering and Technology, University of Washington - Tacoma





### **BUILDING LOCKS**

- Locks require hardware support
  - To minimize overhead, ensure fairness and correctness
  - Special "atomic-as a unit" instructions to support lock implementation
  - Atomic-as a unit exchange instruction
    - XCHG
  - Compare and exchange instruction
    - CMPXCHG
    - **CMPXCHG8B**
    - CMPXCHG16B

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### HISTORICAL IMPLEMENTATION

- To implement mutual exclusion
  - Disable interrupts upon entering critical sections

```
1  void lock() {
2     DisableInterrupts();
3  }
4  void unlock() {
5     EnableInterrupts();
6 }
```

- Any thread could disable system-wide interrupt
  - What if lock is never released?
- On a multiprocessor processor each CPU has its own interrupts
  - Do we disable interrupts for all cores simultaneously?
- While interrupts are disabled, they could be lost
  - If not queued...

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### SPIN LOCK IMPLEMENTATION

- Operate without atomic-as a unit assembly instructions
- "Do-it-yourself" Locks
- Is this lock implementation: (1)Correct? (2)Fair? (3)Performant?



```
typedef struct __lock_t { int flag; } lock_t;
3
    void init(lock_t *mutex) {
         // 0 \rightarrow lock is available, 1 \rightarrow held
         mutex -> flag = 0;
    }
8
   void lock(lock_t *mutex) {
9
        while (mutex->flag == 1) // TEST the flag
                 ; // spin-wait (do nothing)
         mutex->flag = 1; // now SET it !
11
12 }
13
14
   void unlock(lock_t *mutex) {
15
         mutex->flag = 0;
16
```

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### **DIY: CORRECT?**

Correctness requires luck... (e.g. DIY lock is incorrect)

```
Thread1 Thread2

call lock()
while (flag == 1)
interrupt: switch to Thread 2

call lock()
while (flag == 1)
flag = 1;
interrupt: switch to Thread 1

flag = 1; // set flag to 1 (too!)
```

Here both threads have "acquired" the lock simultaneously

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### **DIY: PERFORMANT?**

```
void lock(lock_t *mutex)
  while (mutex->flag == 1);
                              // while lock is unavailable, wait...
  mutex->flag = 1;
}
```

- What is wrong with while(<cond>); ?
- Spin-waiting wastes time actively waiting for another thread
- while (1); will "peg" a CPU core at 100%
  - Continuously loops, and evaluates mutex->flag value...
  - Generates heat...

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### **TEST-AND-SET INSTRUCTION**

- Hardware support required for working locks
- Book presents pseudo code of C implementation
  - TEST-and-SET adds a simple check to the basic spin lock
  - Assumption is this "C code" runs atomically on CPU:

```
int TestAndSet(int *ptr, int new) {
2
        int old = *ptr; // fetch old value at ptr
        *ptr = new; // store 'new' into ptr
3
        return old;
                        // return the old value
```

- lock() method checks that TestAndSet doesn't return 1
- Comparison is in the caller
- Can implement the C version (non-atomic) and have some success on a single-core VM

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### DIY: TEST-AND-SET - 2

- C version: requires preemptive scheduler on single core system
- Lock is never released without a context switch
- single-core VM: occasionally will deadlock, doesn't miscount

```
typedef struct __lock_t {
         int flag;
3
    } lock_t;
   void init(lock_t *lock) {
       // 0 indicates that lock is available,
// 1 that it is held
7
8
         lock -> flag = 0;
   }
10
11 void lock(lock_t *lock) {
        while (TestAndSet(&lock->flag, 1) == 1)
12
13
                 ;
                         // spin-wait
14 }
15
16 void unlock(lock t *lock) {
         lock \rightarrow flag = 0;
17
18
```

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### SPIN LOCK EVALUATION

### Correctness:

Spin locks with atomic Test-and-Set:
 Critical sections won't be executed simultaneously by (2) threads

### Fairness:

No fairness guarantee. Once a thread has a lock, nothing forces it to relinquish it...

### Performance:

- Spin locks perform "busy waiting"
- Spin locks are best for short periods of waiting (< 1 time quantum)</p>
- Performance is slow when multiple threads share a CPU
  - Especially if "spinning" for long periods

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### **COMPARE AND SWAP**

- Checks that the lock variable has the expected value FIRST, before changing its value
  - If so, make assignment
  - Return value at location
- Adds a comparison to TestAndSet
  - Textbook presents C pseudo code
  - Assumption is that the compare-and-swap method runs atomically
- Useful for wait-free synchronization
  - Supports implementation of shared data structures which can be updated atomically (as a unit) using the HW support CompareAndSwap instruction
  - Shared data structure updates become "wait-free"
  - Upcoming in Chapter 32

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### **COMPARE AND SWAP**

int CompareAndSwap(int \*ptr, int expected, int new) {

Compare and Swap

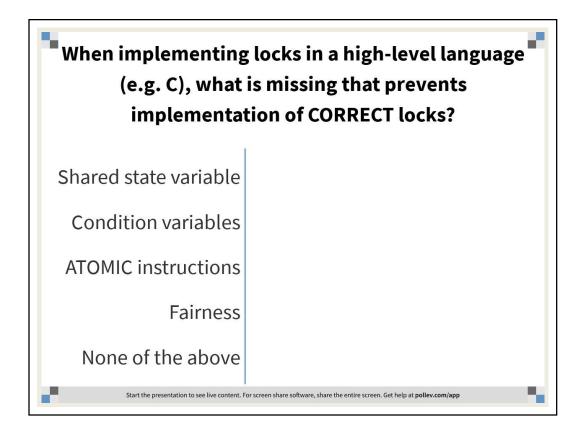
2

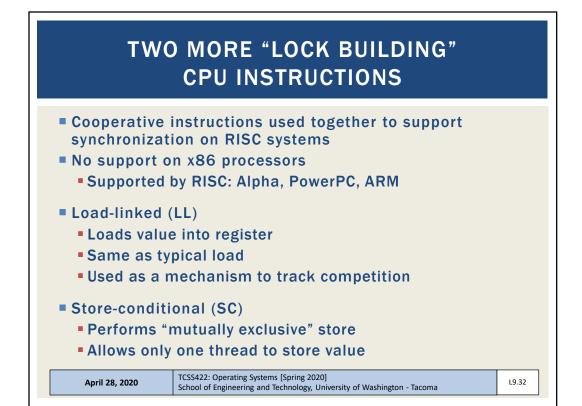
```
int actual = *ptr;
if (actual == expected)
         3
                       *ptr = new;
                 return actual;
           C implementation 1-core VM:
Spin loc
            Count is correct, no deadlock
                       ; // spin
```

- X86 provides "cmpxchg1" compare-and-exchange instruction
  - cmpxchg8b
  - cmpxchg16b

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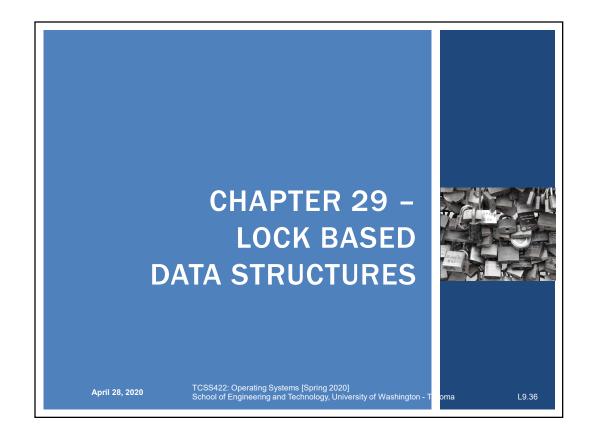




```
LL/SC LOCK
        int LoadLinked(int *ptr) {
   2
            return *ptr;
   3
   5
       int StoreConditional(int *ptr, int value) {
             \  \  \, \hbox{if (no one has updated *ptr since the LoadLinked to this address) } \  \, \{
                     *ptr = value;
                     return 1; // success!
            } else {
   10
                     return 0; // failed to update
            }
   11
   12
      }
LL instruction loads pointer value (ptr)
SC only stores if the load link pointer has not changed
■ Requires HW support
   C code is psuedo code
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                                                                                    L9.33
```

### LL/SC LOCK - 2 void lock(lock t \*lock) { while (1) { 3 while (LoadLinked(&lock->flag) == 1) ; // spin until it's zero if (storeConditional(slock->flag, 1) == 1) return; // if set-it-to-1 was a success: all done otherwise: try it all over again 8 } 9 10 void unlock(lock\_t \*lock) { 11 12 $lock \rightarrow flag = 0;$ 13 ■ Two instruction lock TCSS422: Operating Systems [Spring 2020] April 28, 2020 L9.34 School of Engineering and Technology, University of Washington - Tacoma





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# LOCK-BASED CONCURRENT DATA STRUCTURES

- Adding locks to data structures make them thread safe.
- Considerations:
  - Correctness
  - Performance
  - Lock granularity

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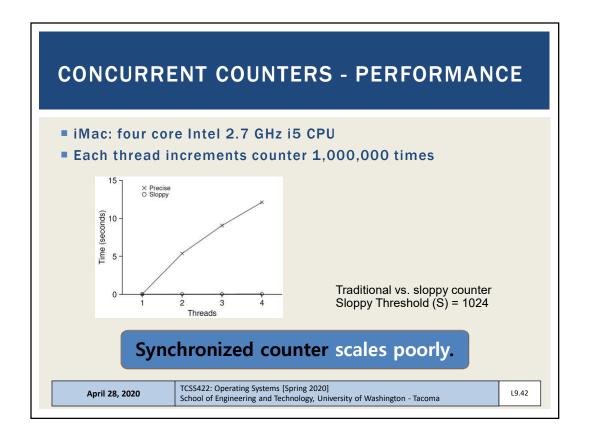
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### COUNTER STRUCTURE W/O LOCK Synchronization weary --- not thread safe typedef struct \_counter\_t { int value; 3 } counter\_t; void init(counter t \*c) { c->value = 0;void increment(counter t \*c) { 10 c->value++; 11 12 13 void decrement(counter\_t \*c) { 14 15 } 16 int get(counter\_t \*c) { 17 18 return c->value; TCSS422: Operating Systems [Spring 2020] School of Engineering and Technology, University of Washington - Tacoma April 28, 2020 L9.39

### **CONCURRENT COUNTER** typedef struct \_\_counter\_t { int value; 3 pthread\_lock\_t lock; } counter t; void init(counter t \*c) { 7 c->value = 0;Pthread\_mutex\_init(&c->lock, NULL); 8 10 void increment(counter\_t \*c) { 11 12 Pthread\_mutex\_lock(&c->lock); 13 c->value++; 14 Pthread\_mutex\_unlock(&c->lock); 15 } Add lock to the counter Require lock to change data TCSS422: Operating Systems [Spring 2020] April 28, 2020 L9.40 School of Engineering and Technology, University of Washington - Tacoma

### **CONCURRENT COUNTER - 2** ■ Decrease counter Get value (Cont.) 17 void decrement(counter t \*c) { Pthread\_mutex\_lock(&c->lock); 18 19 c->value--; 20 Pthread\_mutex\_unlock(&c->lock); 21 22 int get(counter\_t \*c) { 23 24 Pthread\_mutex\_lock(&c->lock); 25 int rc = c->value; 26 Pthread mutex unlock(&c->lock); 27 return rc; 28 TCSS422: Operating Systems [Spring 2020] School of Engineering and Technology, University of Washington - Tacoma April 28, 2020 L9.41



### PERFECT SCALING

- Achieve (N) performance gain with (N) additional resources
- Throughput:
- Transactions per second (tps)
- 1 core
- N = 100 tps
- 10 cores (x10)
- N = 1000 tps(x10)

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### **SLOPPY COUNTER**

- Provides single logical shared counter
  - Implemented using local counters for each ~CPU core
    - 4 CPU cores = 4 local counters & 1 global counter
    - Local counters are synchronized via local locks
  - Global counter is updated periodically
    - Global counter has lock to protect global counter value
    - Sloppiness threshold (S): Update threshold of global counter with local values
    - Small (S): more updates, more overhead
    - Large (S): fewer updates, more performant, less synchronized
- Why this implementation?

Why do we want counters local to each CPU Core?

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### **SLOPPY COUNTER - MAIN POINTS**

- Idea of Sloppy Counter is to <u>RELAX</u> the synchronization requirement for counting
  - Instead of synchronizing global count variable each time: counter=counter+1
  - Synchronization occurs only every so often: e.g. every 1000 counts
- Relaxing the synchronization requirement <u>drastically</u> reduces locking API overhead by trading-off split-second accuracy of the counter
- Sloppy counter: trade-off accuracy for speed
  - It's sloppy because it's not so accurate (until the end)

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### **SLOPPY COUNTER - 2**

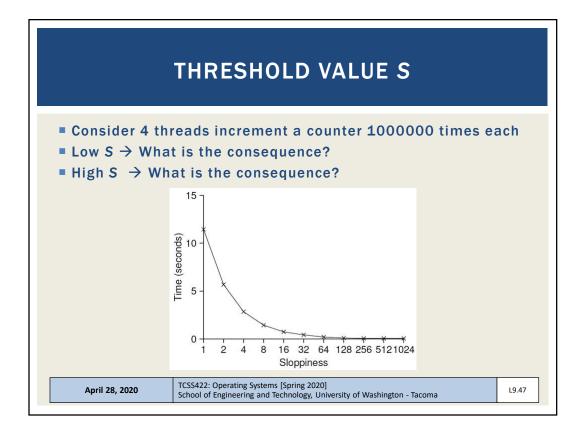
- Update threshold (S) = 5
- Synchronized across four CPU cores
- Threads update local CPU counters

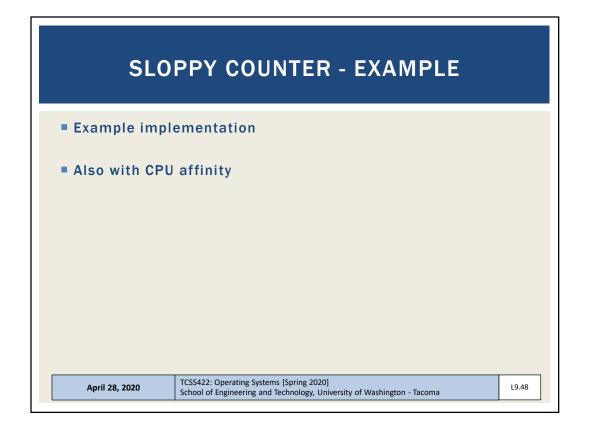
Time	L <sub>1</sub>	L <sub>2</sub>	L <sub>3</sub>	L <sub>4</sub>	G
0	0	0	0	0	0
1	0	0	1	1	0
2	1	0	2	1	0
3	2	0	3	1	0
4	3	0	3	2	0
5	4	1	3	3	0
6	5 → 0	1	3	4	5 (from $L_1$ )
7	0	2	4	5 → 0	10 (from $L_4$ )

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L9.49

L9.50

# **CONCURRENT LINKED LIST - 1**

- Simplification only basic list operations shown
- Structs and initialization:

```
// basic node structure
2
          typedef struct __node_t {
3
                     int key;
                     struct __node_t *next;
5
          } node t;
6
7
          // basic list structure (one used per list)
          typedef struct __list_t {
    node_t *head;
8
9
10
                     pthread_mutex_t lock;
          } list t;
11
12
13
          void List_Init(list_t *L) {
14
                     L->head = NULL;
15
                     pthread_mutex_init(&L->lock, NULL);
16
17
(Cont.)
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```

### **CONCURRENT LINKED LIST - 2**

- Insert adds item to list
- Everything is critical!

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There are two unlocks

```
(Cont.)
18
        int List_Insert(list_t *L, int key) {
19
                 pthread_mutex_lock(&L->lock);
                 node t *new = malloc(sizeof(node t));
20
                 if (new == NULL) {
21
                         perror("malloc");
22
23
                          pthread_mutex_unlock(&L->lock);
24
                 return -1; // fail }
26
                new->key = key;
27
                 new->next = L->head;
28
                L->head = new;
                pthread_mutex_unlock(&L->lock);
29
30
                 return 0; // success
(Cont.)
```

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### **CONCURRENT LINKED LIST - 3**

- Lookup checks list for existence of item with key
- Once again everything is critical
  - Note there are also two unlocks

```
(Cont.)
32
        int List_Lookup(list_t *L, int key) {
33
                 pthread_mutex_lock(&L->lock);
34
                  node_t *curr = L->head;
35
                 while (curr) {
36
                          if (curr->key == key) {
37
                                   pthread mutex unlock(&L->lock);
38
                                   return 0; // success
39
40
                          curr = curr->next;
41
                 pthread_mutex_unlock(&L->lock);
42
43
                  return -1; // failure
44
```

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### **CONCURRENT LINKED LIST**

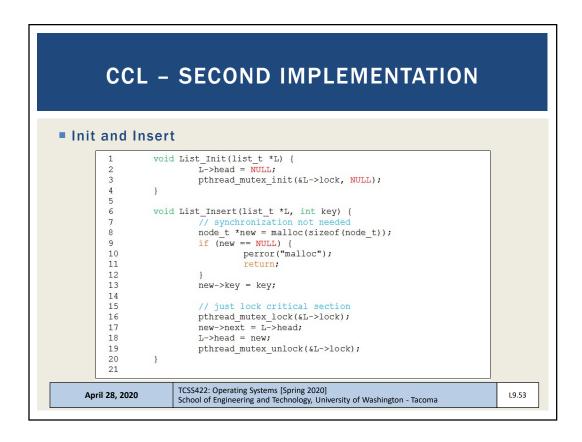
- First Implementation:
  - Lock everything inside Insert() and Lookup()
  - If malloc() fails lock must be released
    - Research has shown "exception-based control flow" to be error
    - 40% of Linux OS bugs occur in rarely taken code paths
    - Unlocking in an exception handler is considered a poor coding
    - There is nothing specifically wrong with this example however
- Second Implementation ...

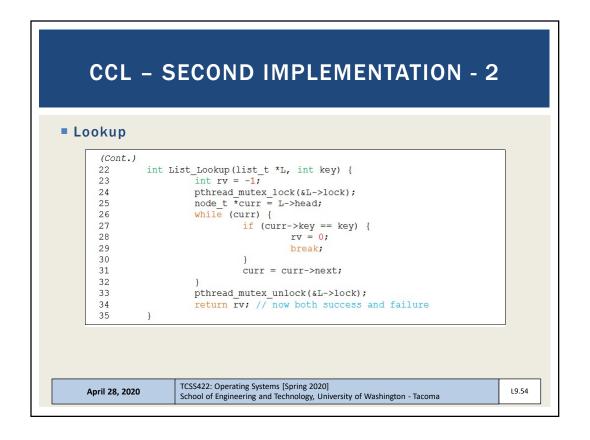
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### **CONCURRENT LINKED LIST PERFORMANCE**

- Using a single lock for entire list is not very performant
- Users must "wait" in line for a single lock to access/modify any item
- Hand-over-hand-locking (lock coupling)
  - Introduce a lock for each node of a list
  - Traversal involves handing over previous node's lock, acquiring the next node's lock...
  - Improves lock granularity
  - Degrades traversal performance
- Consider hybrid approach
  - Fewer locks, but more than 1
  - Best lock-to-node distribution?

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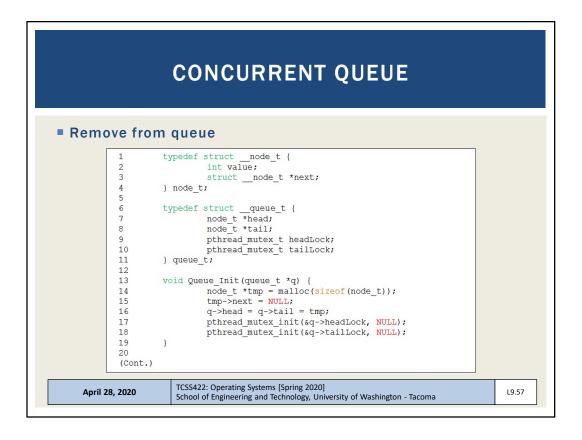
# MICHAEL AND SCOTT CONCURRENT QUEUES

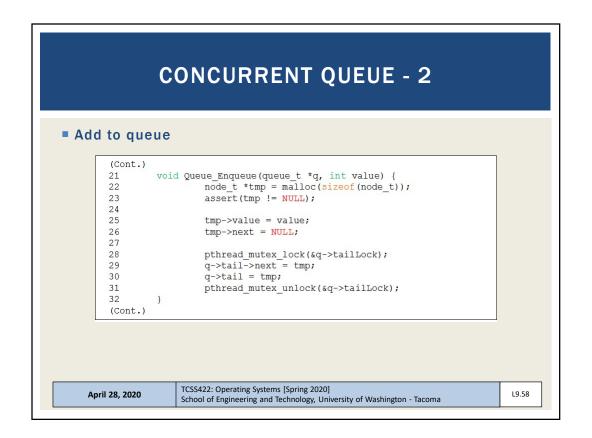
- Improvement beyond a single master lock for a queue (FIFO)
- Two locks:
  - One for the head of the queue
  - One for the tail
- Synchronize enqueue and dequeue operations
- Add a dummy node
  - Allocated in the queue initialization routine
  - Supports separation of head and tail operations
- Items can be added and removed by separate threads at the same time

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### **CONCURRENT HASH TABLE**

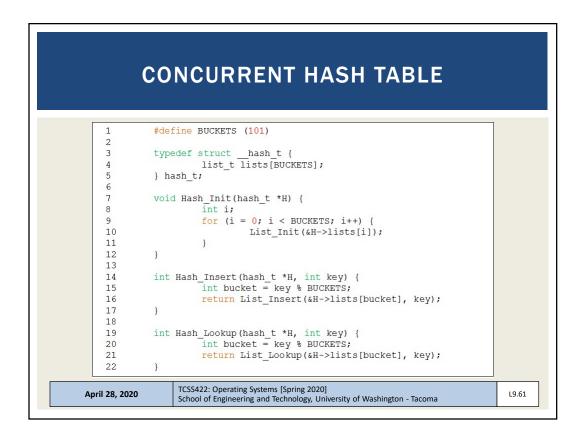
- Consider a simple hash table
  - Fixed (static) size
  - Hash maps to a bucket
    - Bucket is implemented using a concurrent linked list
    - One lock per hash (bucket)
    - Hash bucket is a linked lists

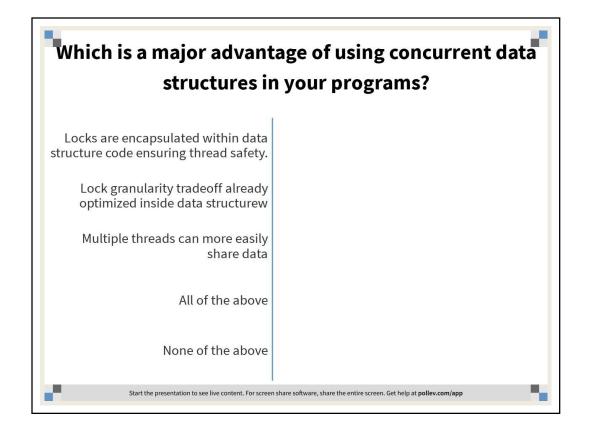
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**INSERT PERFORMANCE -CONCURRENT HASH TABLE** ■ Four threads - 10,000 to 50,000 inserts • iMac with four-core Intel 2.7 GHz CPU O Simple Concurrent List X Concurrent Hash Table Time (seconds) 20 30 Inserts (Thousands) The simple concurrent hash table scales magnificently TCSS422: Operating Systems [Spring 2020] April 28, 2020 L9.60 School of Engineering and Technology, University of Washington - Tacoma



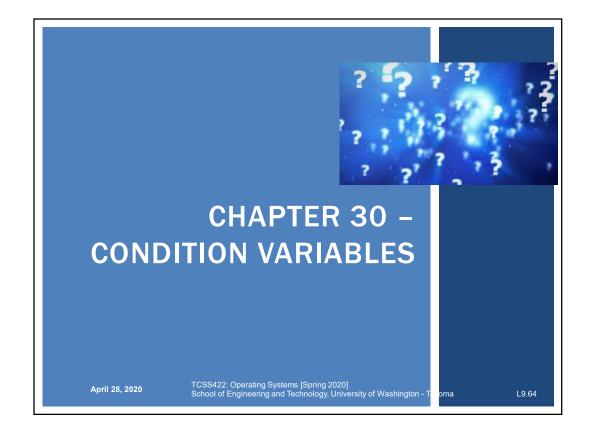


### LOCK-FREE DATA STRUCTURES

- Lock-free data structures in Java
- Java.util.concurrent.atomic package
- Classes:
  - AtomicBoolean
  - AtomicInteger
  - AtomicIntegerArray
  - AtomicIntegerFieldUpdater
  - AtomicLong
  - AtomicLongArray
  - AtomicLongFieldUpdater
  - AtomicReference
- See: <a href="https://docs.oracle.com/en/java/javase/11/docs/api/java.base/java/util/concurrent/atomic/package-summary.html">https://docs.oracle.com/en/java/javase/11/docs/api/java.base/java/util/concurrent/atomic/package-summary.html</a>

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# **OBJECTIVES - 4/28**

- Questions from 4/23
- C Tutorial (Apr 30 11:59p AOE)
- Assignment 1 (May 7 11:59p AOE)
- Chapter 28: Locks
  - Introduction, Lock Granularity
  - Spin Locks, Test and Set, Compare and Swap
- Chapter 29: Lock Based Data Structures
  - Sloppy Counter
  - Concurrent Structures: Linked List, Queue, Hash Table
- Chapter 30: Condition Variables
  - Producer/Consumer
  - Covering Conditions

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### **CONDITION VARIABLES**

- There are many cases where a thread wants to wait for another thread before proceeding with execution
- Consider when a precondition must be fulfilled before it is meaningful to proceed ...

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### **CONDITION VARIABLES - 2**

- Support a signaling mechanism to alert threads when preconditions have been satisfied
- Eliminate busy waiting
- Alert one or more threads to "consume" a result, or respond to state changes in the application
- Threads are placed on an <u>explicit queue</u> (FIFO) to wait for signals
- Signal: wakes one thread broadcast wakes all (ordering by the OS)

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### **CONDITION VARIABLES - 3**

Condition variable

pthread cond t c;

- Requires initialization
- Condition API calls

- wait() accepts a mutex parameter
  - Releases lock, puts thread to sleep
- signal()
  - Wakes up thread, awakening thread acquires lock

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### **CONDITION VARIABLES - QUESTIONS**

- Why would we want to put waiting threads on a queue... why not use a stack?
  - Queue (FIFO), Stack (LIFO)
  - Using condition variables eliminates busy waiting by putting threads to "sleep" and yielding the CPU.
- Why do we want to not busily wait for the lock to become available?
- A program has 10-threads, where 9 threads are waiting. The working thread finishes and broadcasts that the lock is available. What happens next?

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### **MATRIX GENERATOR**

Matrix generation example

Chapter 30 signal.c

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### **MATRIX GENERATOR**

- The main thread, and worker thread (generates matrices) share a single matrix pointer.
- What would happen if we don't use a condition variable to coordinate exchange of the lock?
- Let's try "nosignal.c"

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# **SUBTLE RACE CONDITION:** WITHOUT A WHILE

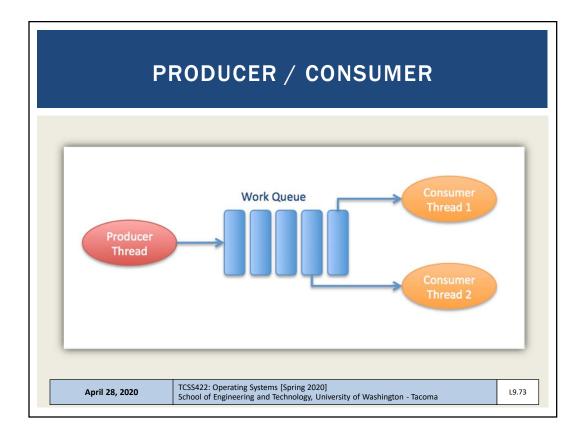
```
void thr_exit() {
                 done = 1;
3
                 Pthread_cond_signal(&c);
        void thr_join() {
                if (done == 0)
                         Pthread_cond_wait(&c);
```

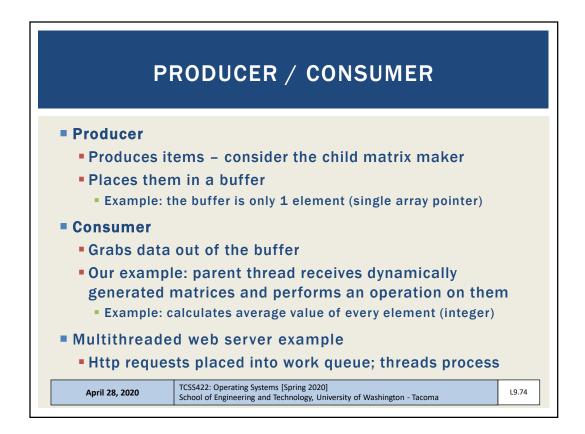
- Parent thread calls thr\_join() and executes the comparison
- The context switches to the child
- The child runs thr\_exit() and signals the parent, but the parent is not waiting yet.
- The signal is lost
- The parent deadlocks

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# PRODUCER / CONSUMER - 2

- Producer / Consumer is also known as Bounded Buffer
- Bounded buffer
  - Similar to piping output from one Linux process to another
  - grep pthread signal.c | wc -l
  - Synchronized access: sends output from grep → wc as it is produced
  - File stream

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L9.75

L9.76

# **PUT/GET ROUTINES**

- Buffer is a one element shared data structure (int)
- Producer "puts" data
- Consumer "gets" data
- Shared data structure requires synchronization

```
int buffer;
        int count = 0; // initially, empty
        void put(int value) {
                 assert(count == 0);
                 count = 1;
buffer = value;
       }
10
      int get() {
11
                 assert(count == 1);
12
                 count = 0;
return buffer;
14
         }
```

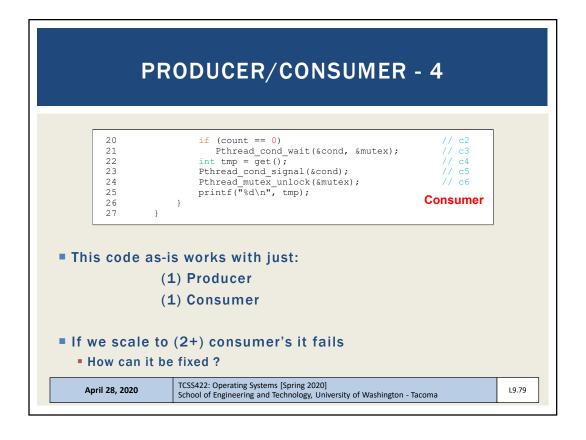
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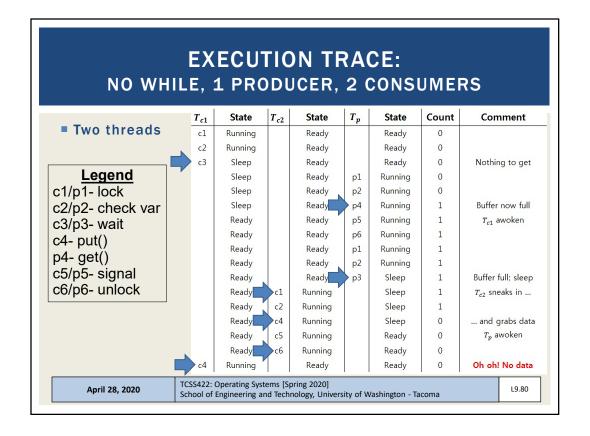
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### PRODUCER / CONSUMER - 3 Producer adds data Consumer removes data (busy waiting) Will this code work (spin locks) with 2-threads? 1. Producer 2. Consumer void \*producer(void \*arg) { 3 int loops = (int) arg; 4 for (i = 0; i < loops; i++) {</pre> 5 put(i); 6 7 8 9 void \*consumer(void \*arg) { 10 11 12 int tmp = get(); 13 printf("%d\n", tmp); 15 TCSS422: Operating Systems [Spring 2020] School of Engineering and Technology, University of Washington - Tacoma April 28, 2020 L9.77

### PRODUCER / CONSUMER - 3 The shared data structure needs synchronization! 1 cond t cond; 2 mutex\_t mutex; 3 void \*producer(void \*arg) { int i; **Producer** for (i = 0; i < loops; i++) { // p1 7 Pthread\_mutex\_lock(&mutex); 8 if (count == 1) // p2 Pthread cond wait(&cond, &mutex); // p3 10 put(i); // p4 11 Pthread cond signal (&cond); // p5 12 Pthread\_mutex\_unlock(&mutex); 13 14 15 16 void \*consumer(void \*arg) { 17 int i; for (i = 0; i < loops; i++) { 18 19 Pthread\_mutex\_lock(&mutex); // c1 TCSS422: Operating Systems [Spring 2020] April 28, 2020 L9.78 School of Engineering and Technology, University of Washington - Tacoma



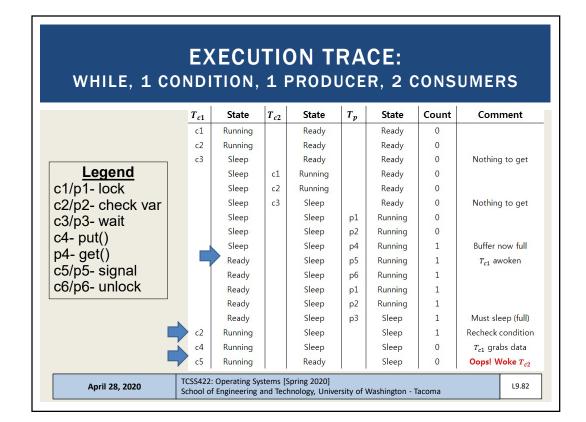


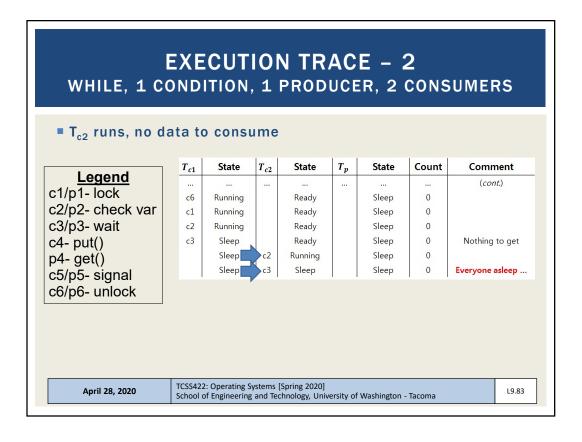
### PRODUCER/CONSUMER **SYNCHRONIZATION**

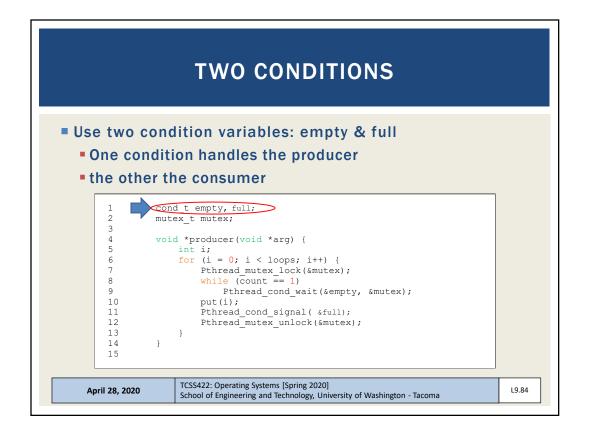
- When producer threads awake, they do not check if there is any data in the buffer...
  - Need while, not if
- What if T<sub>p</sub> puts a value, wakes T<sub>c1</sub> whom consumes the value
- Then  $T_p$  has a value to put, but  $T_{c1}$ 's signal on &cond wakes  $T_{c2}$
- There is nothing for T<sub>c2</sub> consume, so T<sub>c2</sub> sleeps
- $\blacksquare$  T<sub>c1</sub>, T<sub>c2</sub>, and T<sub>p</sub> all sleep forever
- T<sub>c1</sub> needs to wake T<sub>p</sub> to T<sub>c2</sub>

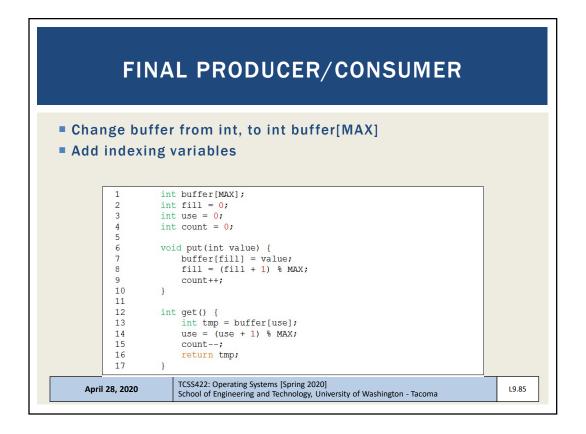
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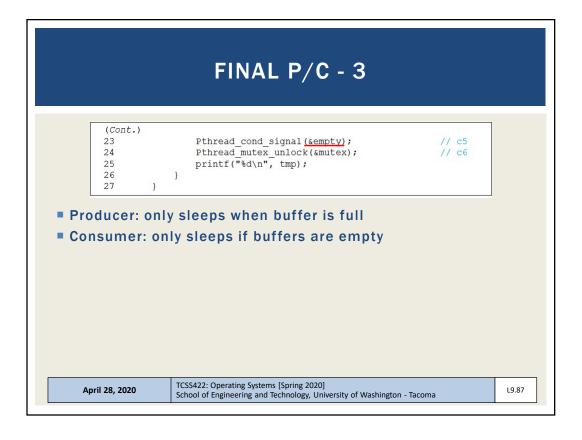




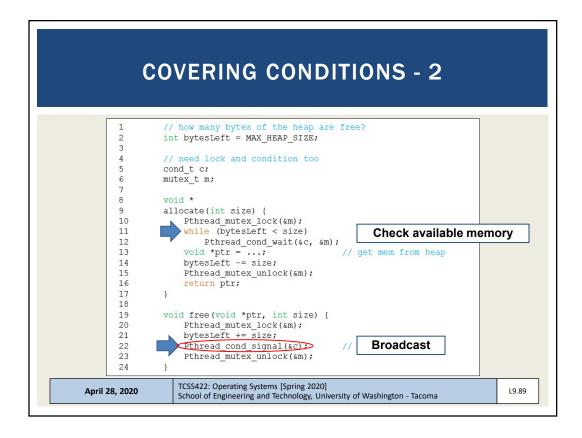




```
FINAL P/C - 2
             cond t empty, full
             mutex_t mutex;
    3
             void *producer(void *arg) {
                 for (i = 0; i < loops; i++) {
                     Pthread_mutex_lock(&mutex);
    8
                     while (count == MAX)
                          Pthread_cond_wait(&empty, &mutex);
                                                                    // p4
// p5
    10
                     put(i);
                      Pthread cond signal (&full);
    11
                     Pthread_mutex_unlock(&mutex);
    12
    13
    14
    15
    16
             void *consumer(void *arg) {
    17
                 int i;
                 for (i = 0; i < loops; i++) {</pre>
    18
    19
                      Pthread_mutex_lock(&mutex);
    20
                      while (count == 0)
    21
                         Pthread_cond_wait(&full, &mutex);
                                                                     // c3
    22
                     int tmp = get();
                                                                     // c4
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                                                                                 L9.86
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```



# COVERING CONDITIONS A condition that covers all cases (conditions): Excellent use case for pthread\_cond\_broadcast Consider memory allocation: When a program deals with huge memory allocation/deallocation on the heap Access to the heap must be managed when memory is scarce PREVENT: Out of memory: - queue requests until memory is free Which thread should be woken up?



# COVER CONDITIONS - 3

- Broadcast awakens all blocked threads requesting memory
- Each thread evaluates if there's enough memory: (bytesLeft < size)</p>
  - Reject: requests that cannot be fulfilled- go back to sleep
    - Insufficient memory
  - Run: requests which can be fulfilled
    - with newly available memory!
- Overhead
  - Many threads may be awoken which can't execute

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