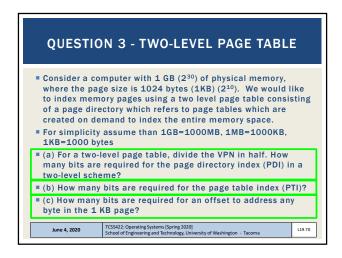


Q2 - 2

(e) Using this memory requirement, how many processes would fill the memory with page table data on a 4GB computer?

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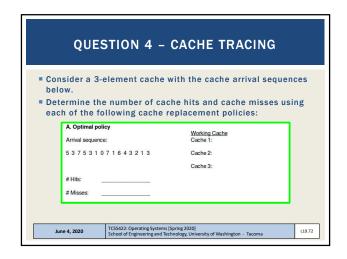
| (d) Assuming each page table entry (PTE) requires 4 bytes of memory, how many extra bits are available for status bits?

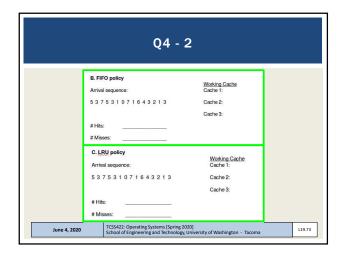
| (e) HelloWorld.c consists of 4 memory pages. One code page, one heap page, one data segment page, and one stack segment page. How large is the two-level page table in bytes with the structure described above that could index the all 4 memory pages of HelloWorld.c?

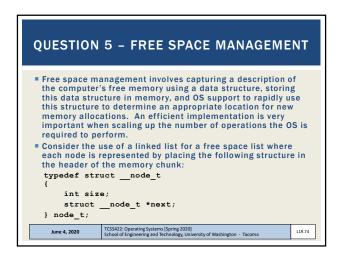
Hint: There should be 2 tables, a page directory, and a page table.

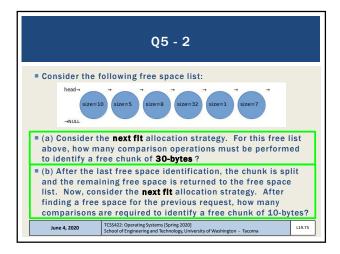
| (f) Assuming the same page table as for HelloWorld.c, using the exact same two-level page table, how large in bytes could the program grow to before needing to expand the page table?

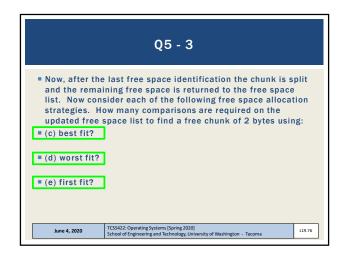
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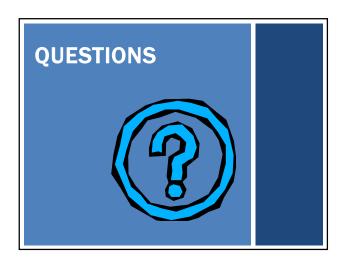


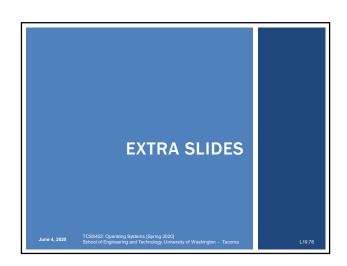


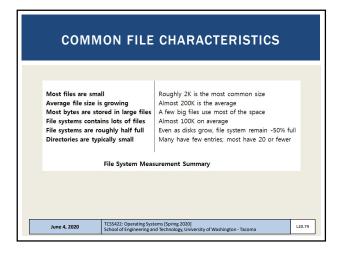


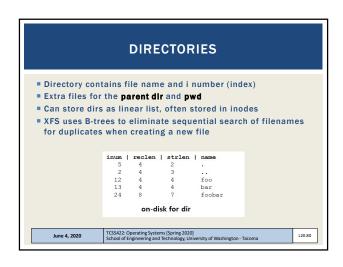


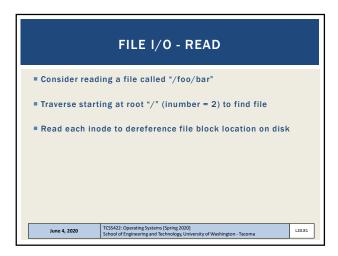


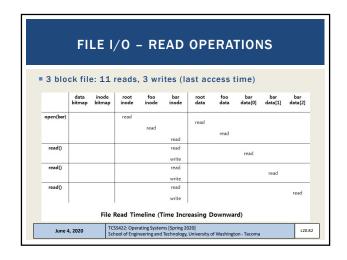


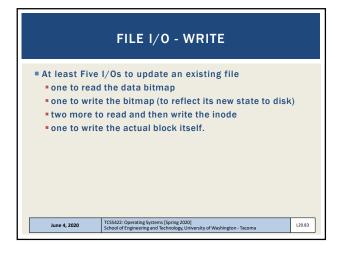


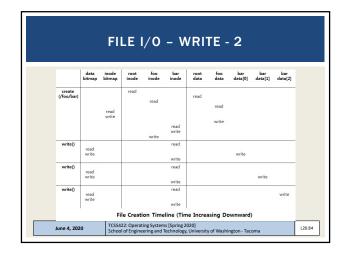


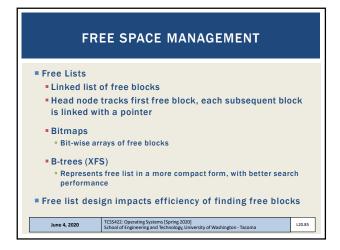


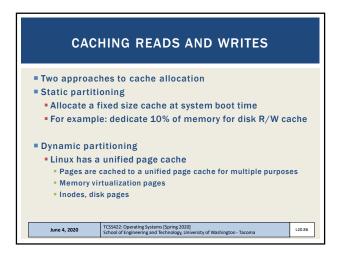


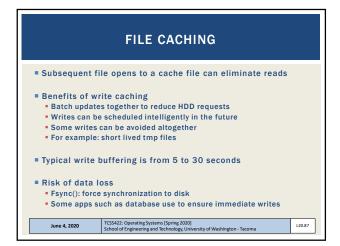














**WJL1** Wes J. Lloyd, 5/30/2020