

OBJECTIVES

- Assignment 0 Introduction to Linux
- Active Reading Quiz Chapter 7
- Feedback from 3/28
- Processes Ch. 4
- C Linux Process API Ch. 5
- Limited Direct Execution Ch. 6
 - Virtualizing the CPU
- Introduction to Scheduling Ch. 7
- Multi-level Feedback Queue Scheduler Ch. 8

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VIRTUAL MACHINE SURVEY

- Please complete the Virtual Machine Survey is wanting an Institute of Technology hosted Ubuntu 16.04 VM
- https://goo.gl/forms/w9VWqkX756yXBUBt1
- ■Submitting results today...

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L3.3

SELECTED FEEDBACK FROM 3/28

- What is context switching?
- Most bash scripts I have seen begin with #!/bin/bash
 - You did not include this in your sample, yet it still worked.
 - Why did it work? and/or why is this usually included if it is not needed?
- What is fork used for? Such as in real-world applications?
- What is CPU virtualization?

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FEEDBACK - 2 How do you schedule processes manualy? Check out the "nice" command Does this command, schedule processes? Why? Why not?

L3.5

CHAPTER 5:
C PROCESS API

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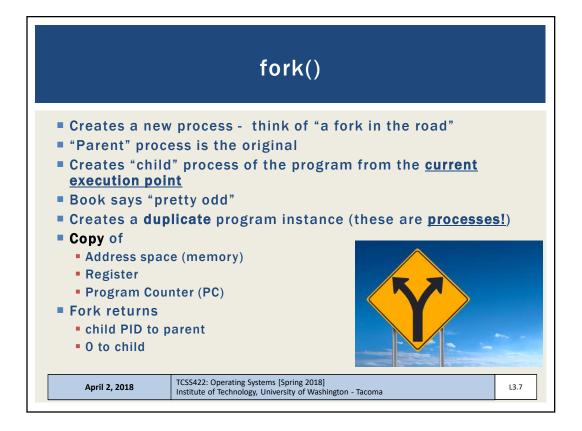
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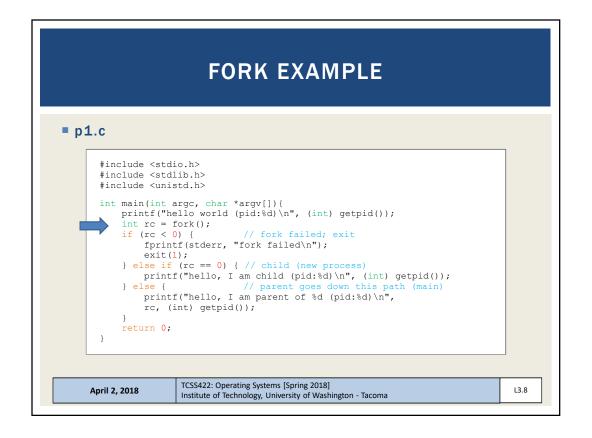
What's an example of a process state that's

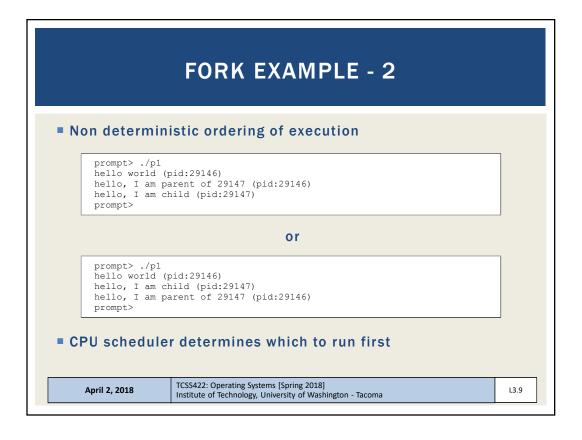
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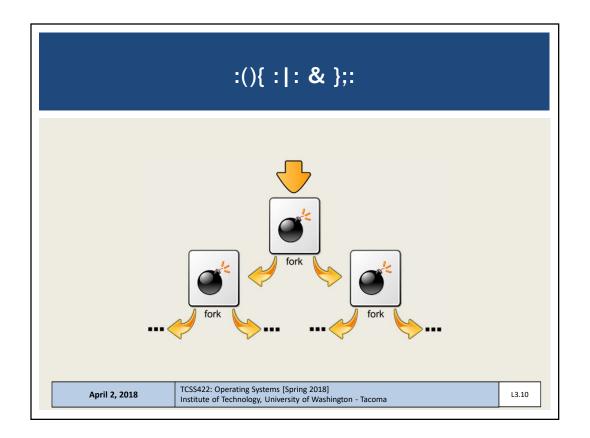
blocked?

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wait()

- wait(), waitpid()
- Called by parent process
- Waits for a child process to finish executing
- Not a sleep() function
- Provides some ordering to multi-process execution



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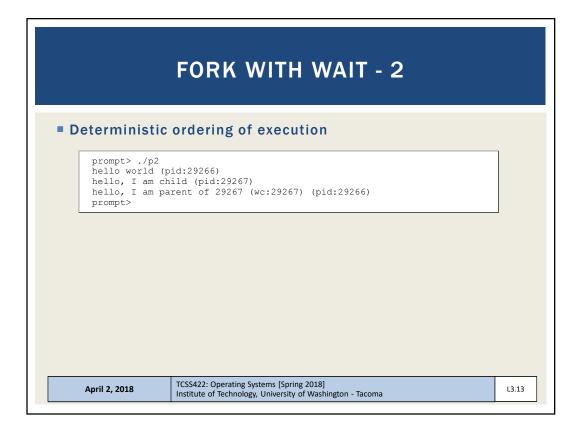
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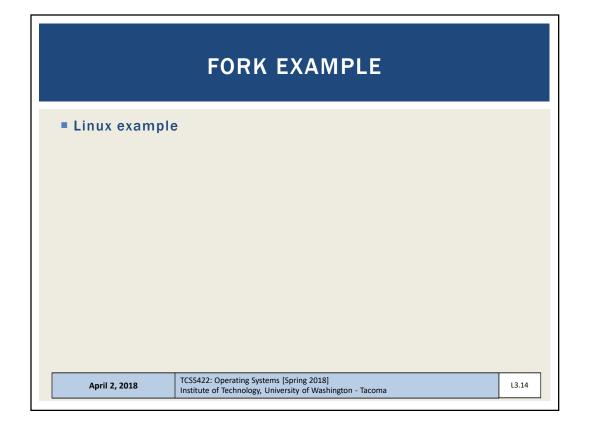
FORK WITH WAIT

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Slides by Wes J. Lloyd





exec()

- Supports running an external program
- 6 types: execl(), execlp(), execle(), execv(), execvp(), execvpe()
- execl(), execlp(), execle(): const char *arg

List of pointers (terminated by null pointer) to strings provided as arguments... (arg0, arg1, .. argn)

Execv(), execvp(), execvpe()
Array of pointers to strings as arguments

Strings are null-terminated
First argument is name of file being executed

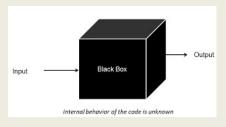
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L3.15

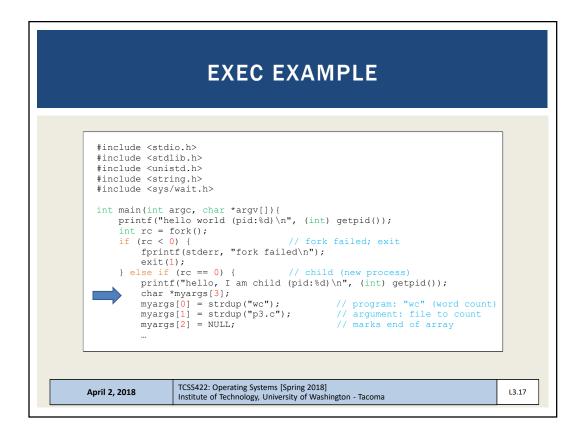
EXEC() - 2

- Common use case:
- Write a new program which wraps a legacy one
- Provide a new interface to an old system: Web services
- Legacy program thought of as a "black box"
- We don't want to know what is inside...

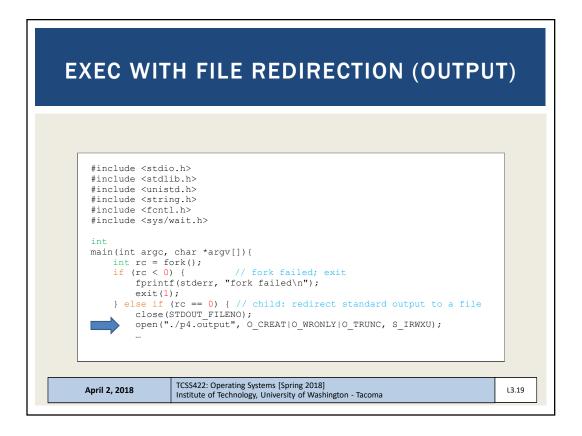


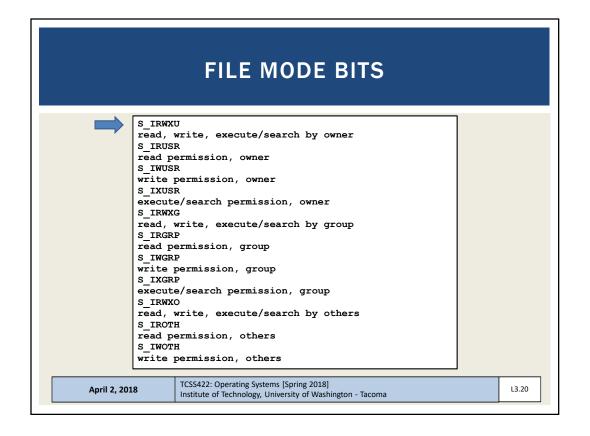
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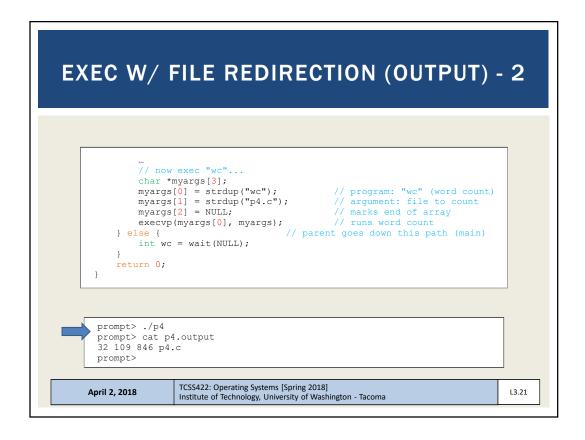
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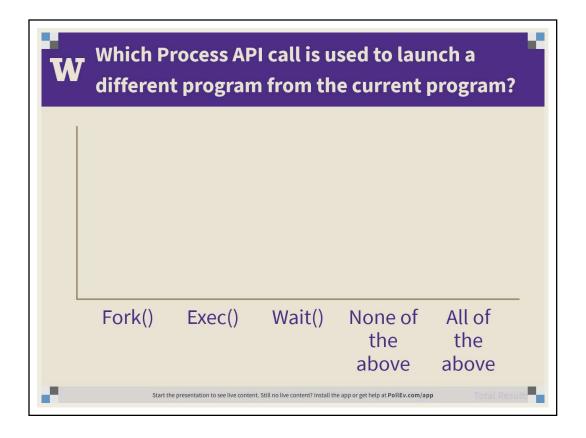


```
EXEC EXAMPLE - 2
          execvp(myargs[0], myargs); // runs word count
          printf("this shouldn't print out");
                                     // parent goes down this path (main)
          int wc = wait(NULL);
          printf("hello, I am parent of %d (wc:%d) (pid:%d) \n",
              rc, wc, (int) getpid());
      return 0;
 prompt> ./p3
 hello world (pid:29383)
 hello, I am child (pid:29384)
29 107 1030 p3.c
 hello, I am parent of 29384 (wc:29384) (pid:29383)
 prompt>
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                                                                                 L3.18
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```









QUESTION: PROCESS API

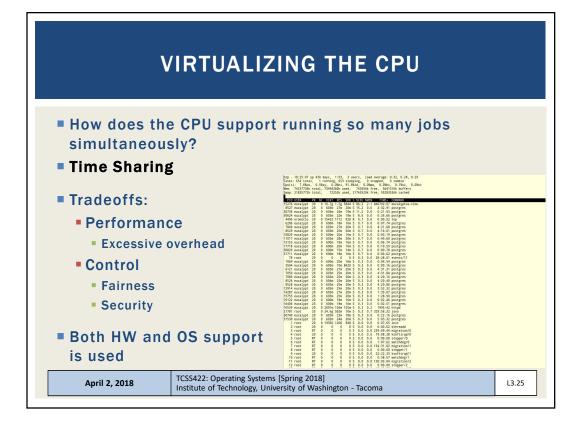
- Which Process API call is used to launch a different program from the current program?
- (a) Fork()
- **(b)** Exec()
- (c) Wait()
- (d) None of the above
- (e) All of the above

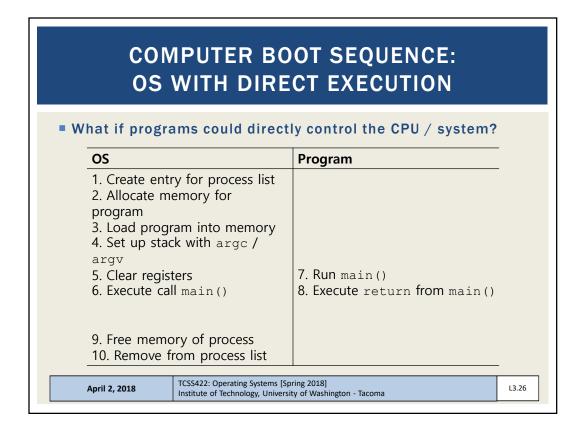
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L3.23

CH. 6: **LIMITED DIRECT EXECUTION** TCSS422: Operating Systems [Spring 2018]
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COMPUTER BOOT SEQUENCE: OS WITH DIRECT EXECUTION

What if programs could directly control the CPU / system?

OS	Program
 Create entry for process list Allocate memory for 	
the OS wouldn't be i and would " <u>ju</u> s	running programs, n control of anything st be a library"
5. Clear registers 6. Execute call main()	7. Run main() 8. Execute return from main()
9. Free memory of process 10. Remove from process list	

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L3.27

DIRECT EXECUTION - 2

■ With direct execution:

How does the OS stop a program from running, and switch to another to support **time sharing?**

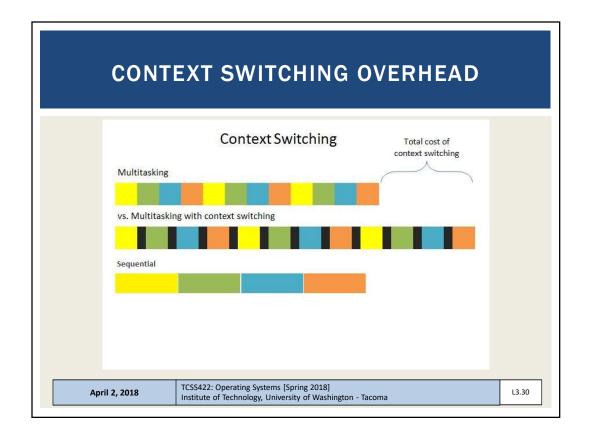
How do programs share disks and perform I/O if they are given direct control? Do they know about each other?

With direct execution, how can dynamic memory structures such as linked lists grow over time?

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CONTROL TRADEOFF Too little control: No security No time sharing Too much control: Too much OS overhead Poor performance for compute & I/O Complex APIs (system calls), difficult to use



LIMITED DIRECT EXECUTION

- OS implements LDE to support time/resource sharing
- Limited direct execution means "only limited" processes can execute DIRECTLY on the CPU in trusted mode
- TRUSTED means the process is trusted, and it can do anything... (e.g. it is a system / kernel level process)
- Enabled by protected (safe) control transfer
- CPU supported context switch
- Provides data isolation

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CPU MODES

- Utilize CPU Privilege Rings (Intel x86)
 - rings 0 (kernel), 1 (VM kernel), 2 (unused), 3 (user)

access <no access

User mode:

Application is running, but w/o direct I/O access

Kernel mode:

OS kernel is running performing restricted operations

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CPU MODES

- <u>User mode: ring 3 untrusted</u>
 - Some instructions and registers are disabled by the CPU
 - Exception registers
 - HALT instruction
 - MMU instructions
 - OS memory access
 - I/O device access
- Kernel mode: ring 0 trusted
 - All instructions and registers enabled

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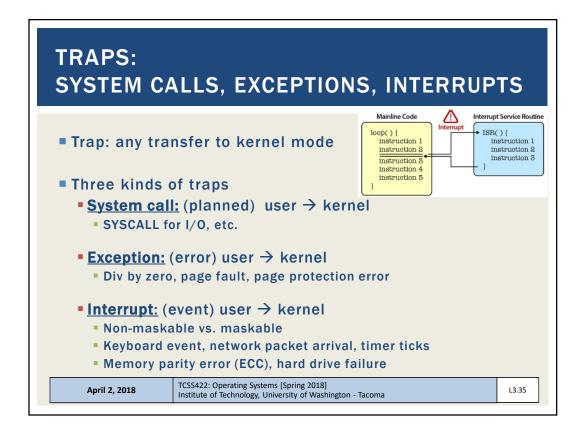
L3.33

SYSTEM CALLS

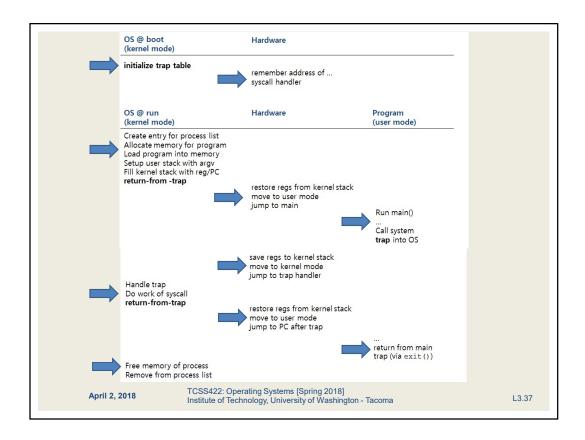
- Implement restricted "OS" operations
- Kernel exposes key functions through an API:
 - Device I/O (e.g. file I/O)
 - Task swapping: context switching between processes
 - Memory management/allocation: malloc()
 - Creating/destroying processes

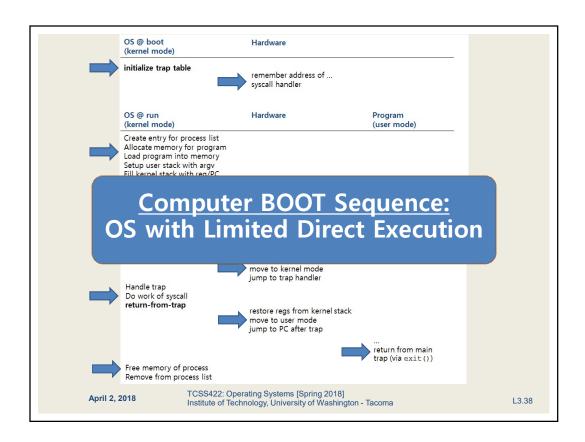
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EXCEPTION TYPES							
Exception type	Synchronous vs. asynchronous	User request vs. coerced	User maskable vs. nonmaskable	Within vs. between instructions	Resume vs. terminate		
I/O device request	Asynchronous	Coerced	Nonmaskable	Between	Resume		
Invoke operating system	Synchronous	User request	Nonmaskable	Between	Resume		
Tracing instruction execution	Synchronous	User request	User maskable	Between	Resume		
Breakpoint	Synchronous	User request	User maskable	Between	Resume		
Integer arithmetic overflow	Synchronous	Coerced	User maskable	Within	Resume		
Floating-point arithmetic overflow or underflow	Synchronous	Coerced	User maskable	Within	Resume		
Page fault	Synchronous	Coerced	Nonmaskable	Within	Resume		
Misaligned memory accesses	Synchronous	Coerced	User maskable	Within	Resume		
Memory protection violation	Synchronous	Coerced	Nonmaskable	Within	Resume		
Using undefined instruction	Synchronous	Coerced	Nonmaskable	Within	Terminate		
Hardware malfunction	Asynchronous	Coerced	Nonmaskable	Within	Terminate		
Power fallure	Asynchronous	Coerced	Nonmaskable	Within	Terminate		





MULTITASKING

- How/when should the OS regain control of the CPU to switch between processes?
- Cooperative multitasking (mostly pre 32-bit)
 - < Windows 95, Mac OSX</p>
 - Opportunistic: running programs must give up control
 - User programs must call a special yield system call
 - When performing I/O
 - Illegal operations
 - (POLLEV)

What problems could you for see with this approach?

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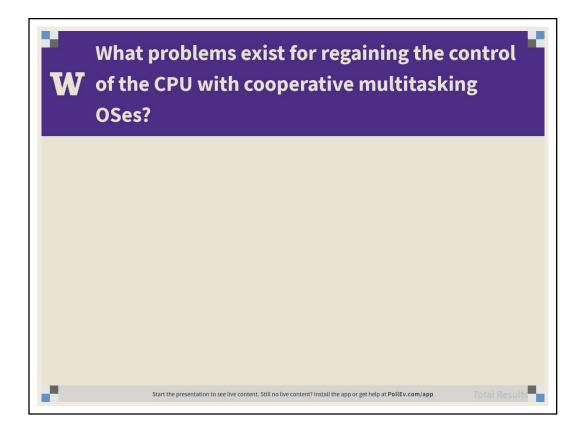
MULTITASKING

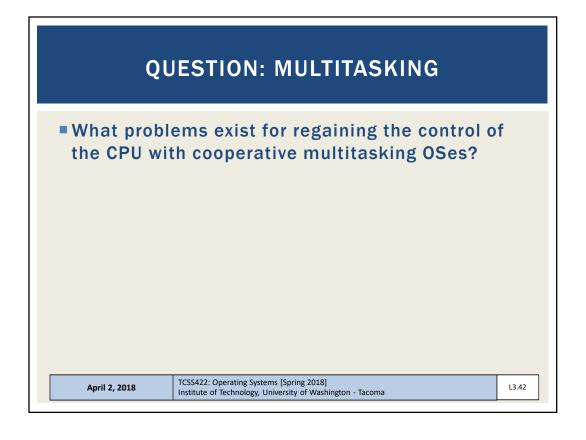
- How/when should the OS regain control of the CPU to switch between processes?
- Cooperative multitacking (mostly nre 32-hit)
 - A process gets stuck in an infinite loop.
 - → Reboot the machine
 - When performing 1/ o
 - Illegal operations
 - (POLLEV)

What problems could you for see with this approach?

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MULTITASKING - 2

- Preemptive multitasking (32 & 64 bit OSes)
- >= Mac OSX, Windows 95+
- **■** Timer interrupt
 - Raised at some regular interval (in ms)
 - Interrupt handling
 - 1. Current program is halted
 - 2. Program states are saved
 - 3. OS Interrupt handler is run (kernel mode)
- (PollEV) What is a good interval for the timer interrupt?

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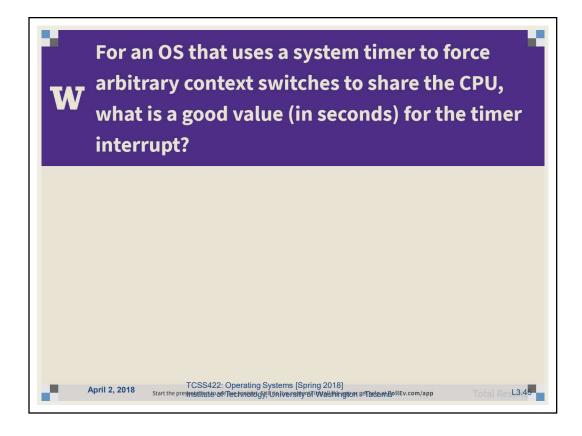
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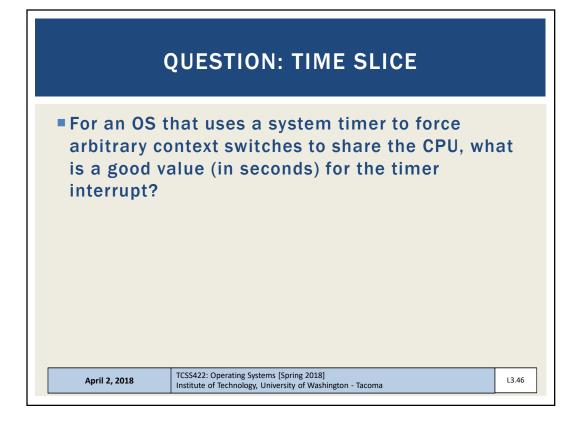
MULTITASKING - 2

- Preemptive multitasking (32 & 64 bit OSes)
- ► >= Mac OSX, Windows 95+
- Timer
 - Rais A timer interrupt gives OS the ability to run again on a CPU.
 - Inter
 - 1. Current program is halted
 - 2. Program states are saved
 - 3. OS Interrupt handler is run (kernel mode)
- (PollEV) What is a good interval for the timer interrupt?

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CONTEXT SWITCH

- Preemptive multitasking initiates "trap" into the OS code to determine:
- Whether to continue running the current process, or switch to a different one.
- If the decision is made to switch, the OS performs a context switch swapping out the current process for a new one.

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L3.47

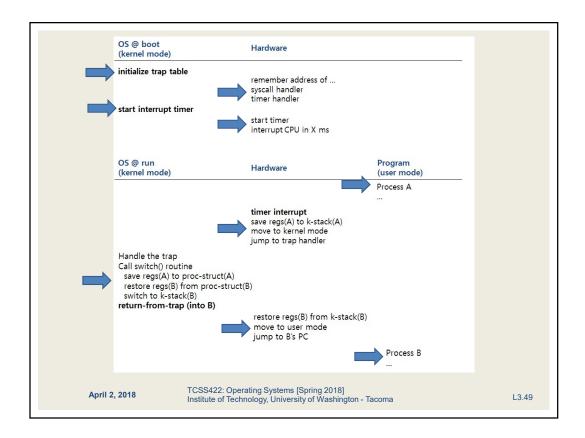
CONTEXT SWITCH - 2

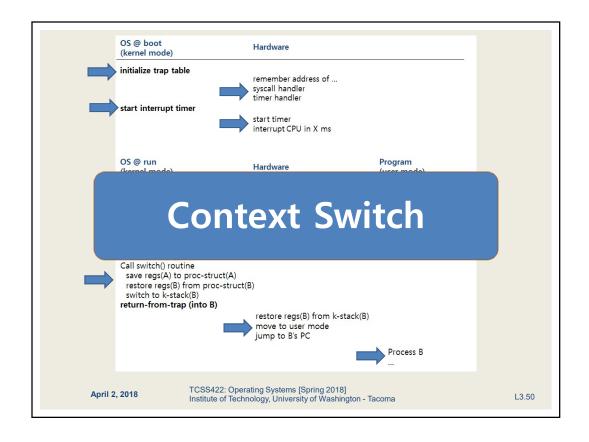
- 1. Save register values of the current process to its kernel stack
 - General purpose registers
 - PC: program counter (instruction pointer)
 - kernel stack pointer
- 2. Restore soon-to-be-executing process from its kernel stack
- 3. Switch to the kernel stack for the soon-to-be-executing process

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INTERRUPTED INTERRUPTS

- What happens if during an interrupt (trap to kernel mode), another interrupt occurs?
- Linux
 - < 2.6 kernel: non-preemptive kernel</p>
 - >= 2.6 kernel: preemptive kernel

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PREEMPTIVE KERNEL

- Use "locks" as markers of regions of nonpreemptibility (non-maskable interrupt)
- Preemption counter (preempt_count)
 - begins at zero
 - increments for each lock acquired (not safe to preempt)
 - decrements when locks are released
- Interrupt can be interrupted when preempt_count=0
 - It is safe to preempt (maskable interrupt)
 - the interrupt is more important

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