















	C PROGRAMIN	IG IN TCSS 422
Many O	Ses are coded primari	ly in C and Assembly Language
Compu	terworld. 2017 Tech Fo	orecast Survey
-	What legacy platforms do y for?	you still support and hire
	None	65%
	DB2	13%
	С	10%
	Cobol	9%
	Assembly language	8%
	Perl	5%
	Delphi Object Pascal	3%
	Fortran	3%
	REXX	3%
	Pascal	2%
	Other	9%























	ABSTRACTIONS
<ul><li>What form</li><li>CPU</li></ul>	of abstraction does the OS provide?
<ul> <li>Process a</li> <li>Memory</li> </ul>	and/or thread
<ul> <li>Address</li> <li>→ large</li> </ul>	space array of bytes
<ul> <li>All progra</li> </ul>	ams see the same "size" of RAM
<ul><li>Disk</li><li>Files</li></ul>	
September 26, 2018	TCSS422: Operating Systems [Fall 2018]         School of Engineering and Technology, University of Washington - Tacoma















	VI	RTUALIZING MEMORY - 2	
Pro	gram to re	ead/write memory:	
1 2 3 4	<pre>#include <u "co="" #inc<="" #include="" <s="" th=""><th>nistd.h&gt; tdio.h&gt; tdlib.h&gt; ommon.h"</th><th></th></u></pre>	nistd.h> tdio.h> tdlib.h> ommon.h"	
5 6 7 8	<pre>int main(int ard {</pre>	gc, char *argv[])	
9 10 11	int ass pri	<pre>t *p = malloc(sizeof(int)); // al: allocate some</pre>	
12 13	*p	<pre>getpid(), (unsigned) p); // a2: print out the</pre>	
15 16 17	WII	<pre>iie (1) {     Spin(1);     *p = *p + 1;     printf("(%d) p: %d\n", getpid(), *p); // a4</pre>	
18 19 20	} ret }	turn 0;	
Septen	nber 26, 2018	TCSS422: Operating Systems [Fall 2018] School of Engineering and Technology, University of Washington - Tacoma	L1.29



VIRTUALIZING MEMORY - 4			
Multiple insta	inces of mem.c		
p [ ( ( ( ( ( ( ( ( ( ( ( ( ( ( ( ( ( (	<pre>rompt&gt; ./mem &amp;; ./mem &amp; 1] 24113 2] 24114 24113) memory address of p: 00200000 24114) memory address of p: 00200000 24113) p: 1 24114) p: 1 24114) p: 2 24113) p: 2 24113) p: 3 24114) p: 3</pre>		
(int*)p receiv	es the same memory location 00200000		
<ul> <li>Why does mo interfere with</li> <li>The OS has "</li> </ul>	difying (int*)p in program #1 (PID=24113), not (int*)p in program #2 (PID=24114) ? virtualized" memory, and provides a "virtual" address		
September 26, 2018	TCSS422: Operating Systems [Fall 2018]         School of Engineering and Technology, University of Washington - Tacoma		



top Taske / N Windows Task Manager	
Task: Windows Task Manager	
Cou(s) and a second sec	
Mem File Options View Help	
Swap: Applications Processes Services Performance Netw	orking Users
1527( Image Name User Name CPU Memory ( I	Description
852: svchost.exe SYSTEM 99 230.504K	Host Proc
30624 splwow64.exe wfloyd 00 1,432 K i	Print driv
449( taskmgr.exe wlloyd 00 2,084K )	Windows
6281 OSPPSVC.EXE NETWO 00 2,048 K 1	Microsoft
852: SearchIndexe SYSTEM 00 3,372 K 1	Microsoft
10825 POWERPNT.E wloyd 00 36,964K	Microsoft
11017 SSScheduler Wildyd 00 864K 1	nuAlee S
17718 PrintIsolation SYSTEM 00 1.140 K	Windows
30829 VBoxTray.exe wlloyd 00 1,764 K	VirtualBox E
71 taskhost.exe wiloyd 00 3,768 K	Host Proc
1061 dwm.exe wlloyd 00 1,132K I	Desktop
3504 GarminService SYSTEM 00 18,004K 0	Germin Se
012 svchost.exe SYSTEM 00 2,796 K 1	Host Proc
708: svchost.exe LOCAL 00 7.156 K	Host Proc
852t spoolsv.exe SYSTEM 00 5,200 K 5	Sociel S
12914 ExpressTray wloyd 00 14,960 K (	Garmin Ex
1428. svchost.exe SYSTEM 00 1,600 K	Host Proc
1575: svchost.exe LOCAL 00 2,924K H	Host Proc
16406 svchost.exe SYSTEM 00 3,052 K	Host Proc
16539 taskeng.exe SYSTEM 00 1,140 K	Task Sche
3074L sychostiexe LOCAL 00 9,264 K	Host Proc
3153( VBoxService SYSTEM 00 1.476 K	internet of the second s
svchost.exe SYSTEM 00 2,684K	Host Proc
Ism.exe SYSTEM 00 1,204K	Local Ses +
Show processes from all users	End Process
Processes 37 CPU Usage 100% Physical 1	Memory 36%
Processes 57 CP0 03age 100/a Prinyscart	HEIRINY, Sola
10 root ki 0 0 0 0 0 0 0 0 0.0	U:56.57 Watchdog/1 120.02.04 microsticn/2



	CONCURRENCY - 2	
1 #incl 2 #incl 3 #incl 4 5 volat 6 int 1 7 8 void 9 10 11 12 13 14 }	ude <stdio.h> ude <stdib.h> ude "common.h" illo int counter = 0; oops; Not the same as Java volatile: Provides a compiler hint than an object may change value unexpectedly (in this case by a separate thread) so aggre optimization must be avoided.</stdib.h></stdio.h>	ue essive
thread.c		]
September 26, 2018	TCSS422: Operating Systems [Fall 2018] School of Engineering and Technology, University of Washington - Tacoma	L1.35

	16 int 17 main 18 { 19 20 21 22 23	<pre>n(int argc, char *argv[]) if (argc != 2) {     fprintf(stderr, "usage: threads <value>\n");         exit(1); } </value></pre>	
	24 25 26 27 28 29 30 31 32 33 }	<pre>loops = ato1(argv[1]); pthread_t p1, p2; printf("Initial value : %d\n", counter); Pthread_create(&amp;p1, NULL, worker, NULL); Pthread_create(&amp;p2, NULL, worker, NULL); Pthread_join(p1, NULL); Pthread_join(p2, NULL); printf("Final value : %d\n", counter); return 0;</pre>	
<ul><li>Program</li><li>Chec</li><li>work</li></ul>	ram creat ck docume ker() meth	tes two threads entation: "man pthread_create" od counts from 0 to argv[1] (loop)	























