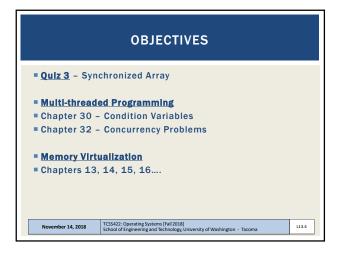
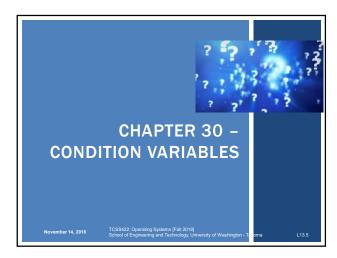


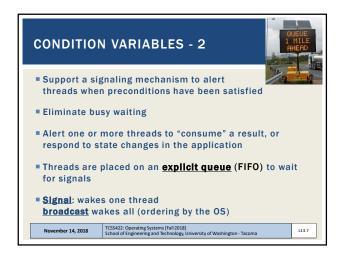
FEEDBACK FROM 11/7				
How can you display the thread ID for debugging purposes?				
■ Can associate an unique int when creating pthread				
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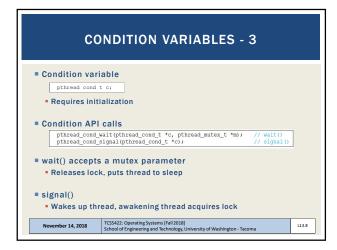
FEEDBACK - 2				
Do you need multiple condition variables (In program 2) or can you use just one?				
 Coordinating the end of program 2 Max LOOPS is reached Consumer threads: run out of matrices 				
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CONDITION VARIABLES There are many cases where a thread wants to wait for another thread before proceeding with execution Consider when a precondition must be fulfilled before it is meaningful to proceed ... TCSS42: Operating Systems [Fall 2018] School of Engineering and Technology, University of Washington - Tacoma



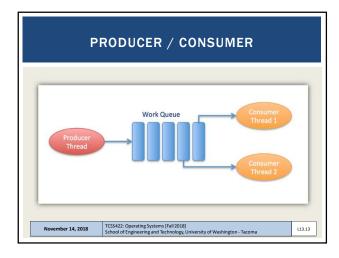


CONDITION VARIABLES - QUESTIONS					
Why would we not use a state	want to put waiting threads on a queue why				
• Queue (FIFO)	, Stack (LIFO)				
	 Using condition variables eliminates busy waiting by putting threads to "sleep" and yielding the CPU. 				
Why do we wa avallable?	ant to not busily walt for the lock to become				
A program has 10-threads, where 9 threads are waiting. The working thread finishes and broadcasts that the lock is available. What happens next?					
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MATRIX GENERATOR				
	Matrix generation example			
	Chapter 30			
	signal.c			
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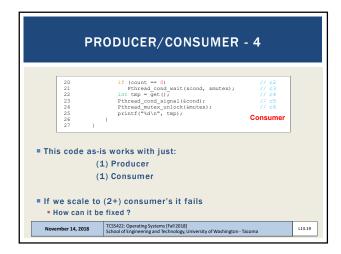
MATRIX GENERATOR					
The main thread, and worker thread (generates matrices) share a single matrix pointer.					
	What would happen if we don't use a condition variable to coordinate exchange of the lock?				
■ Let's try "nosignal.c"					
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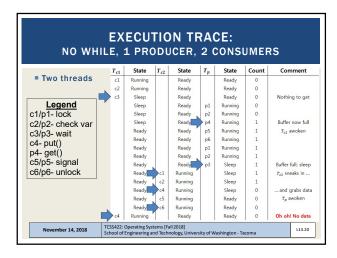
SU	SUBTLE RACE CONDITION: WITHOUT A WHILE				
2 3 4 5	<pre>d thr_exit() { done = 1; Fthread_cond_signal(&c); d thr_join() { if (done == 0) Pthread_cond_wait(&c); } }</pre>				
The context svThe child runs is not waiting	 Parent thread calls thr_join() and executes the comparison The context switches to the child The child runs thr_exit() and signals the parent, but the parent is not waiting yet. The signal is lost 				
The parent deadlocks November 14, 2018 TCSS422-Operating Systems [Fall 2018] School of Engineering and Technology, University of Washington - Tacoma 113.12					



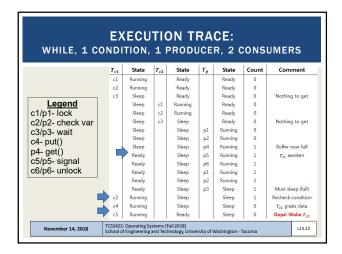
PRODUCER / CONSUMER				
■ Producer				
Produces it	ems – consider the child matrix maker			
Places then	n in a buffer			
Example: tl	he buffer is only 1 element (single array pointer)			
Consumer				
 Grabs data out of the buffer 				
 Our example: parent thread receives dynamically 				
generated matrices and performs an operation on them				
 Example: calculates average value of every element (integer) 				
■ Multithreaded web server example				
• Http requests placed into work queue; threads process				
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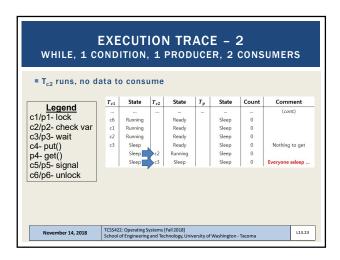
PRODUCER / CONSUMER - 2					
■ Producer / C	onsumer is also known as Bounded Buffe	r			
 Similar to p grep pthrea Synchronize sends outp 	 ■ Producer / Consumer is also known as Bounded Buffer ■ Bounded buffer ■ Similar to piping output from one Linux process to another ■ grep pthread signal.c wc - I ■ Synchronized access: ■ sends output from grep → wc as it is produced ■ File stream 				
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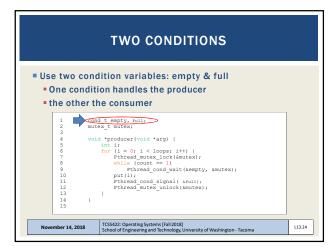


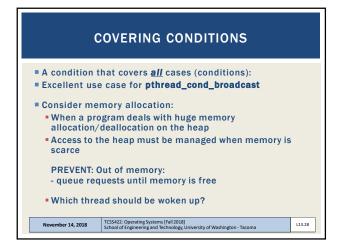


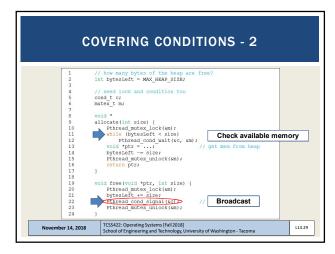
PRODUCER/CONSUMER SYNCHRONIZATION				
When produce any data in th	er threads awake, they do not check if there is e buffer			
• Need while,	not if			
 What if T_p puts a value, wakes T_{c1} whom consumes the value Then T_p has a value to put, but T_{c1}'s signal on &cond wakes T_{c2} There is nothing for T_{c2} consume, so T_{c2} sleeps T_{c1}, T_{c2}, and T_p all sleep forever 				
■ T _{c1} needs to wake T _p to T _{c2}				
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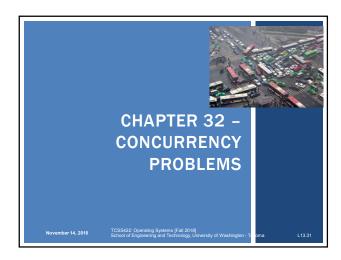


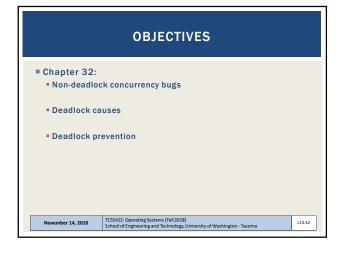


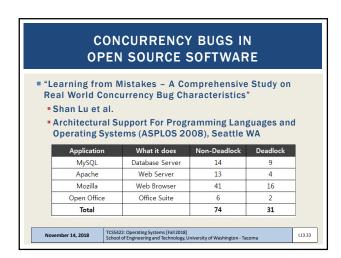


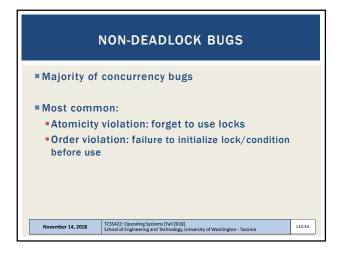


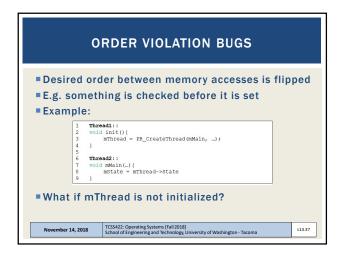
COVER CONDITIONS - 3 Broadcast awakens all blocked threads requesting memory Each thread evaluates if there's enough memory: (bytesLeft < size) Reject: requests that cannot be fulfilled- go back to sleep Insufficient memory Run: requests which can be fulfilled with newly available memory! Overhead Many threads may be awoken which can't execute





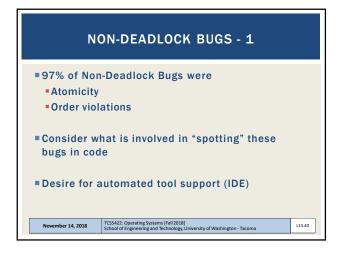


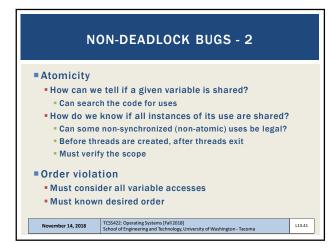


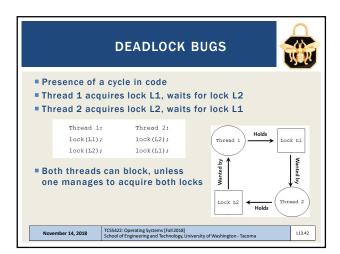


```
Use condition variable to enforce order

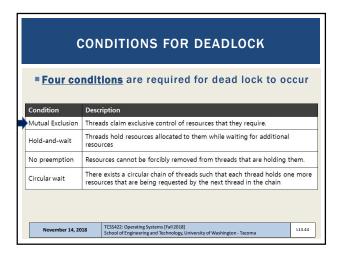
| pthread_mutex_t mtLock = PTHREAD_MUTEX_INITIALIZER;
| pthread_cond_t mtcond = PTHREAD_COND_INITIALIZER;
| int mtInit = 0;
| thread 1:
| void init() {
| mthread = FR_CreateThread(mMain, ...);
| mthread = FR_CreateThread(mMain, ...);
| mthread mutex_lock(smtLock);
| mtInit = 1;
| mt
```

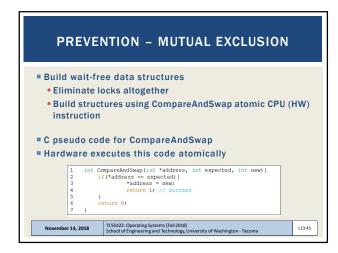


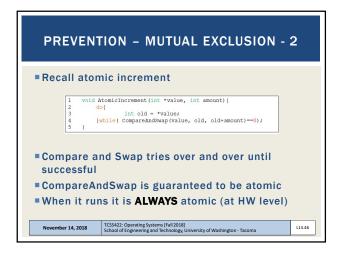


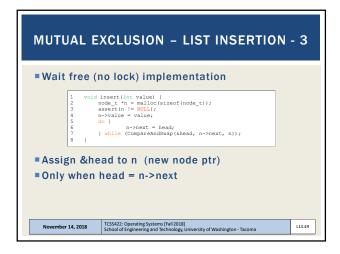


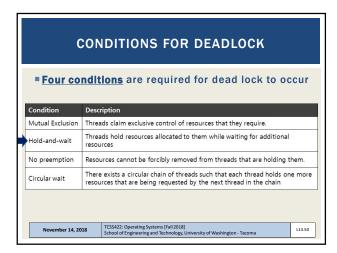
REASONS FOR DEADLOCKS Complex code Must avoid circular dependencies – can be hard to find... Encapsulation hides potential locking conflicts Easy-to-use APIs embed locks inside Programmer doesn't know they are there Consider the Java Vector class: Vector v1, v2; 2 v1. AddAl1(v2); Vector is thread safe (synchronized) by design If there is a v2.AddAl1(v1); call at nearly the same time deadlock could result November 14, 2018 TCSS422: Operating Systems [Fail 2018] School of Engineering and Technology, University of Washington-Tacoma



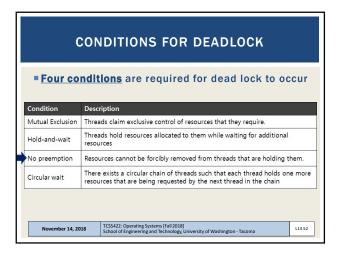


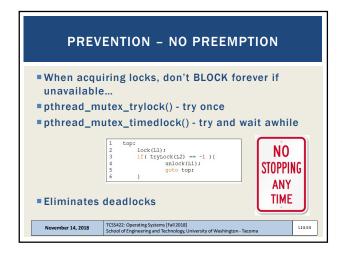


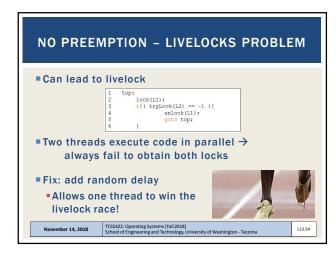


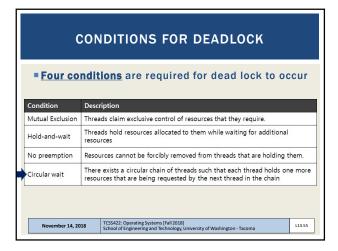


PREVENTION LOCK - HOLD AND WAIT				
Problem: acquire all locks atomically				
solution: use a "lock" "lock" (like a guard lock) 1 lock(prevention); 2 lock(Li); 3 lock(Li2); 4 5 unlock(prevention);				
 Effective solution – guarantees no race conditions while acquiring L1, L2, etc. Order doesn't matter for L1, L2 Prevention (GLOBAL) lock decreases concurrency of code 				
Acts Lowers lock granularity Encapsulation: consider the Java Vector class November 14, 2018 TCSS422: Operating Systems [Fall 2018] L13.51				



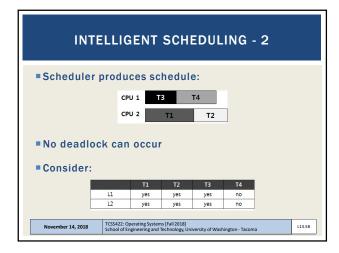


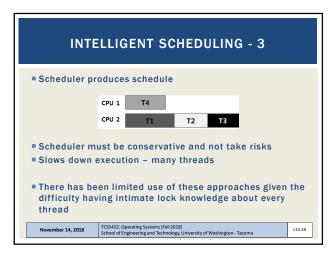




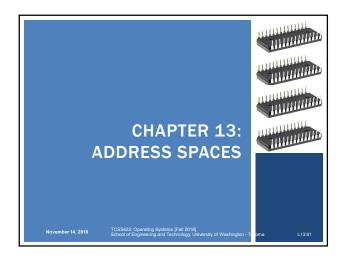
PREVENTION - CIRCULAR WAIT				
 Provide total ordering of lock acquisition throughout code Always acquire locks in same order L1, L2, L3, 				
Never mix: L2, L1, L3; L2, L3, L1; L3, L1, L2				
•Must carry out same ordering through entire program				
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DEADLOCK AVOIDANCE VIA INTELLIGENT SCHEDULING					
 Consider a smart scheduler Scheduler knows which locks threads use Consider this scenario: 4 Threads (T1, T2, T3, T4) 					
■ 2 Locks (L1, L2) ■ Lock requirements of threads: T1 T2 T3 T4					
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DETECT AND RECOVER				
Allow deadlock to occasionally occur and then take some action.				
Example: When OS freezes, reboot				
■ How often is this acceptable?				
Once per year				
Once per month				
Once per day				
Consider the effort tradeoff of finding every deadlock bug				
Many database systems employ deadlock detection and recovery techniques.				
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OBJECTIVES - MEMORY VIRTUALIATION Chapter 13 Introduction to memory virtualization The address space Goals of OS memory virtualization Chapter 14 Memory API Common memory errors Chapter 15 Address translation Base and bounds HW and OS Support Chapter 16 Memory segments, fragmentation November 14, 2018 TCS542: Operating Systems [Fall 2018] School of Engineering and Technology, University of Washington-Taxoma

MEMORY VIRTUALIZATION			
■ What is mem	nory virtualization?		
 This is not "virtual" memory, Classic use of disk space as additional RAM When available RAM was low 			
Less comm	on recently		
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