















































WHILE, 1 C	E) ondi	(ECU tion,	TI( 1 I	DN T PRODU	RA JCE	CE: R, 2 (	ONS	UMERS
	T <sub>c1</sub>	State	T <sub>c2</sub>	State	T <sub>p</sub>	State	Count	Comment
	c1	Running		Ready	· ·	Ready	0	
	c2	Running		Ready		Ready	0	
	c3	Sleep		Ready		Ready	0	Nothing to get
Legend		Sleep	c1	Running		Ready	0	
c1/p1-lock		Sleep	c2	Running		Ready	0	
c2/p2- check var		Sleep	c3	Sleep		Ready	0	Nothing to get
c3/p3- wait		Sleep		Sleep	p1	Running	0	
c4- $nut()$		Sleep		Sleep	p2	Running	0	
$p_{1}^{-}$ $p_{1}^{-}$		Sleep		Sleep	p4	Running	1	Buffer now full
p4-gel()		Ready		Sleep	p5	Running	1	T <sub>c1</sub> awoken
c5/p5- signal		Ready		Sleep	p6	Running	1	
c6/p6- unlock		Ready		Sleep	pl	Running	1	
		Ready		Sleep	p2	Running	1	
	-	Ready		Sleep	p3	Sleep	1	Must sleep (full)
	c2	Running		Sleep		Sleep	1	Recheck condition
	c4	Running		Sleep		Sleep	0	T <sub>c1</sub> grabs data
	- c5	Running		Ready		Sleep	0	Oops! Woke T <sub>c2</sub>
November 5, 2018	TCSS422 School o	: Operating Sy f Engineering	stems [I and Tecl	all 2018] nnology, Unive	rsity of \	Washington - 1	ľacoma	L11.27

	T <sub>c1</sub>	State	T <sub>c2</sub>	State	T <sub>p</sub>	State	Count	Comment
Legend								(cont.)
c1/p1-lock	c6	Running		Ready		Sleep	0	
c2/p2- check var	c1	Running		Ready		Sleep	0	
c3/p3- wait	c2	Running		Ready		Sleep	0	
c4- put()	c3	Sleep		Ready		Sleep	0	Nothing to get
n4- get()		Sleep	C2	Running		Sleep	0	
c5/n5_signal		Sleep	c3	Sleep		Sleep	0	Everyone asleep
c6/p6- unlock								



















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<ul> <li>Learning f</li> <li>Real World</li> <li>Shan Lu e</li> </ul>	from M I Concu et al. Tural Su	irrency Bug Ch	aracteristics	e Study on " guages an
<ul> <li>Architect</li> <li>Operating</li> </ul>	g Syste	ms (ASPLOS 20	008), Seattle	WA
<ul> <li>Architect</li> <li>Operating</li> <li>Applicat</li> </ul>	g Syste	ms (ASPLOS 20 What it does	008), Seattle Non-Deadlock	WA Deadlock
Architect Operating Applicat MySQ	<b>g Syste</b> tion )L	ms (ASPLOS 20 What it does Database Server	008), Seattle Non-Deadlock 14	WA Deadlock 9
Architect     Operating     Applicat     MySQ     Apach	<b>g Syste</b> tion QL he	ms (ASPLOS 20 What it does Database Server Web Server	Non-Deadlock	WA Deadlock 9 4
Architect     Operating     Applicat     MySQ     Apach     Mozill	<b>g Syste</b> tion QL he Ila	ms (ASPLOS 20 What it does Database Server Web Server Web Browser	Non-Deadlock 14 13 41	WA Deadlock 9 4 16
Architect     Operating     Applican     MySQ     Apach     Mozill     Open Of	tion QL Ila ffice	ms (ASPLOS 20 What it does Database Server Web Server Web Browser Office Suite	Non-Deadlock 14 13 41 6	WA Deadlock 9 4 16 2











ORD	ORDER VIOLATION - SOLUTION 2				
21 // 22 ptt 23 whi 24 ptt 25 ptt 27 mst 28 - 29 }	<pre>wait for the thread to be initialized _ rread_mutex_lock(amtLock); lin(minti == cond_wait(amtCond, intLock); rread_mutex_unlock(intLock); tate = mfhread-&gt;State;</pre>				
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PREVENT	FION - MUTUAL EXCLUSION - :	2
Recall aton	nic increment	
1 void # 2 dd 3 4 }; 5 }	<pre>AtomicIncrement(int *value, int amount){     o(         int old = *value; while( CompareAndSwap(value, old, old+amount)==0);</pre>	
Compare a successful	nd Swap tries over and over until	
CompareAr	ndSwap is guaranteed to be atomic	
■When it rur	ns it is <b>ALWAYS</b> atomic (at HW level)	
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Lock I	based implementation
	<pre>void insert(int value){     node, t * n = malloc(sizeof(mode_t));     assert(n != NULL);     n-value = value ;     lock(listlock); // begin critical section     n-value = n;     unlock(listlock); //end critical section     } }</pre>



