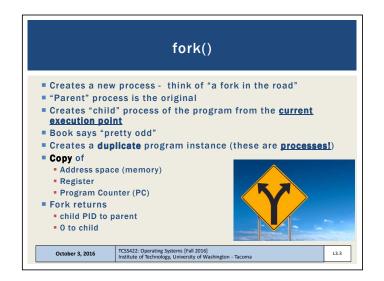
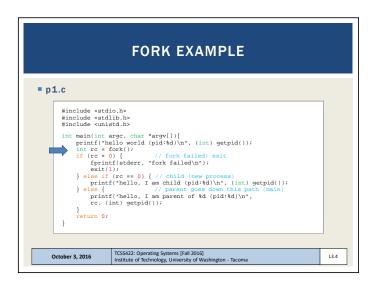
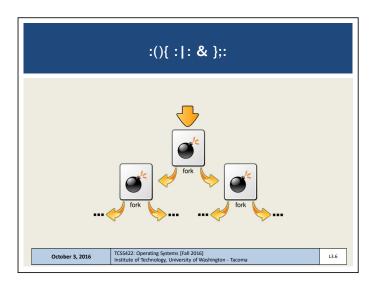


	OBJECTIVES	
Process API -	Ch. 5	
Limited Direct	t Execution – Ch. 6	
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		FORK EXAMPLE - 2	
Non	determini	istic ordering of execution	
1		oid:29146) wrent of 29147 (pid:29146) ild (pid:29147)	
		or	,
1		oid:29146) Mild (pid:29147) rent of 29147 (pid:29146)	
CPU	schedule	r determines which to run first	J
	ber 3, 2016	TCSS422: Operating Systems [Fall 2016]	13.5



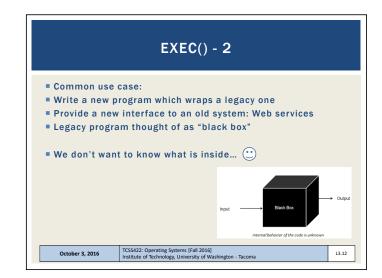
	wait()
Not a sleep()	ent process ild process to finish executing
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	FORK WITH WAIT	
<pre>printf("hh int rc = 1 if (rc < 0 fprint exit() } else if int w print;</pre>	<pre>lib_hs std.hs /wait.hs ardc,char *argv[]){ ello world (piditd)\n*, (int) getpid()); fork(); // fork failed, exit tf(stderr, *fork failed\n*); 1; (rc == 0) { // child (new process) f(chello, 1 am child (piditd)\n*, (int) getpid()); </pre>	
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	FORK WITH WAIT - 2	
Deterministic	ordering of execution	
	vid:29266) ilid (pid:29267) rent of 29267 (wc:29267) (pid:29266)	
		_

	FORK EXAMPLE	
Linux example	2	
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	exec()
Supports run	ning an external program
6 types: exect	(), execlp(), execle(), execv(), execvp(), execvpe()
execl(), execl	o(), execle(): const char *arg
	rs (terminated by null pointer) vided as arguments (arg0, arg1, argn)
Execv(), execv Array of point	p(), execvpe() ers to strings as arguments
Strings are nu First argumer	Ill-terminated It is name of file being executed
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EXEC EX	AMPLE	
<pre>fprintf(stderr, "fork failed) exit(1); else if (rc == 0) { // printf("hello, I am child (pic char *myargs[3]; myargs[0] = strdup("wc");</pre>	<pre>fork failed; exit "); child (new process)</pre>	
October 3, 2016 TCSS422: Operating Systems [Fall 20 Institute of Technology, University o		L3.13

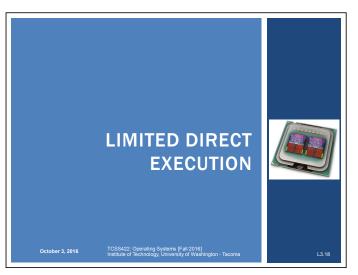
	EXEC EXAMPLE - 2	
printi } else { int wo printi	<pre>p(myargs[0], myargs); // runs word count {("this shouldn't print out");</pre>	
29 107 1030 p	nild (pid:29384)	
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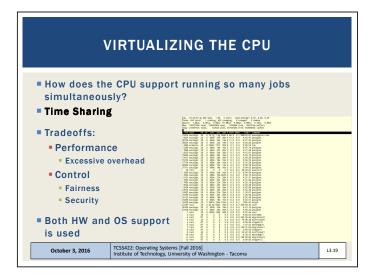
EXEC WIT	H FILE REDIRECTION (OUTPUT
<pre>int rc = f if (rc < 0 fprint exit(1 } else if close()</pre>	<pre>ib.b> id.b> id.b> il.b> char *argv[]){ ork(); // fork failed; exit f(sdefr, *fork failed)n*); </pre>
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	FILE MODE BITS
•	<pre>S_IRWXU read, write, execute/search by owner S_IRUSR read permission, owner S_IRUSR secute/search permission, owner S_IRUSR read, write, execute/search by group S_IRKRP read permission, group S_IRKRP write permission, group S_IRKRP secute/search permission, group S_IRKRP secute/search permission, group S_IRKRP secute/search permission, group S_IRKRP read, write, execute/search by others S_IRKRP read permission, others S_INCH read permission, others</pre>
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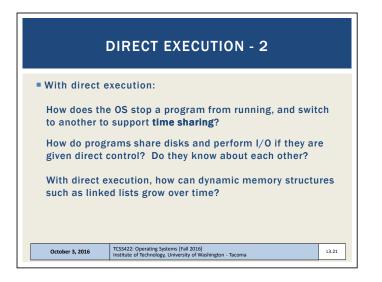
char myarg: myarg: myarg: execvj } else {	<pre>w exec "wc" myargs[3]; [0] = strdup("wc"); // program: "wc" (word count) s(1] = strdup("p4.c"); // argument: file to count s(2] = NUL; // marks end of array p(myargs[0], myargs); // runs word count p(myargs[0], myargs); // parent goes down this path (main) c = wait(NULL);</pre>
prompt> ./p4 prompt> cat p 32 109 846 p4 prompt>	



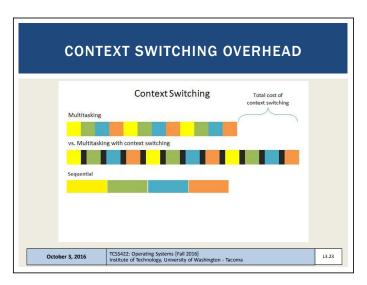


DIRECT EXECUTION					
What if progra	ams could direct	ly control the CPU / system?			
OS		Program			
1. Create entr 2. Allocate m	y for process list emory for				
Computer BOOT Sequence: OS with Direct Execution					
6. EXECUTE Call main() 8. EXECUTE return from main()					
	ory of process rom process list				
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CONTROL TRADEOFF				
Too much c	ontrol:			
No security	/			
No time sh	aring			
Too little co	ntrol:			
Too much OS overhead				
Poor performante	rmance for compute & I/O			
	Pls (system calls), difficult to use			
·				
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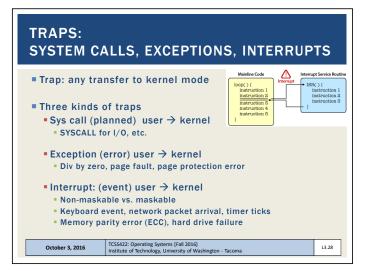


LIMITED DIRECT EXECUTION • OS implements LDE to support time/resource sharing • Enabled by protected (safe) control transfer • CPU supported context switch • Provides data isolation

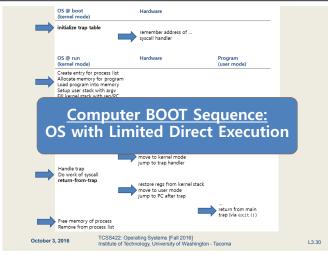
	CPU MODES	
 rings 0 (kernal acc User mode: Application i Kernel mode 	Privilege Rings (Intel x86) el), 1 (VM kernel), 2 (unused), 3 (user) ess mo access s running, but w/o direct I/O access : running performing restricted operations	
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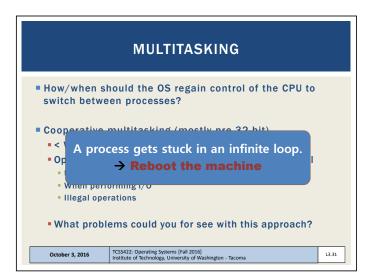
	CPU MODES		
	ing 3 - untrusted		
	uctions and registers are disabled by the CPU		
Exception registers			
HALT instru			
MMU instru	ctions		
OS memory	access		
I/O device a	access		
Kernel mode	ring 0 – trusted		
 All instruction 	ons and registers enabled		
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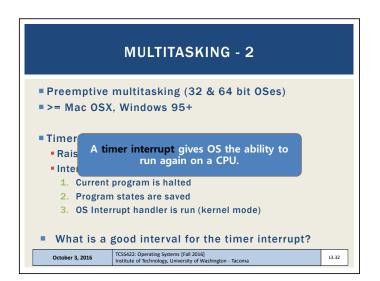
 Enable restricted "OS" operations Kernel exposes key functions through an API: Device I/O Task swapping: context switch Memory management/allocation: malloc() Creating/destroying processes 		SYSTEM CALLS	
	 Kernel expos Device I/O Task swapp Memory mage 	es key functions through an API: bing: context switch anagement/allocation: malloc()	



EXCEPTION TYPES						
Exception type	Synchronous vs. asynchronous	User request vs. coerced	User maskable vs. nonmaskable	Within vs. between Instructions	Resume vs. terminate	
I/O device request	Asynchronous	Coerced	Nonmaskable	Between	Resume	
Invoke operating system	Synchronous	User request	Nonmaskable	Between	Resume	
Tracing instruction execution	Synchronous	User request	User maskable	Between	Resume	
Breakpoint	Synchronous	User request	User maskable	Between	Resume	
Integer arithmetic overflow	Synchronous	Coerced	User maskable	Within	Resume	
Floating-point arithmetic overflow or underflow	Synchronous	Coerced	User maskable	Within	Resume	
Page fault	Synchronous	Coerced	Nonmaskable	Within	Resume	
Misaligned memory accesses	Synchronous	Coerced	User maskable	Within	Resume	
Memory protection violation	Synchronous	Coerced	Nonmaskable	Within	Resume	
Using undefined instruction	Synchronous	Coerced	Nonmaskable	Within	Terminate	
Hardware malfunction	Asynchronous	Coerced	Nonmaskable	Within	Terminate	
Powerfailure	Asynchronous	Coerced	Nonmaskable	Within	Terminate	
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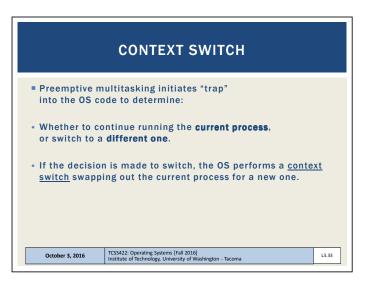


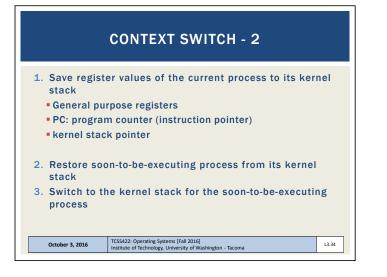
Slides by Wes J. Lloyd

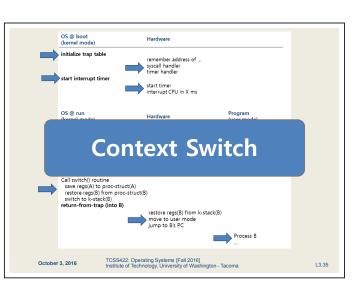
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INTERRUPTED INTERRUPTS				
	s if during an interrupt (trap to kernel er interrupt occurs?			
	l: non-preemptive kernel el: preemptive kernel			
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