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TCSS 422 - OFFICE HRS - SPRING 2024

Tuesdays after class until 7:00pm

Hybrid (In-person/Zoom)

- This session will be in person in CP 229.
- Zoom will be monitored when no student is in CP 229.
- Thursdays after class until 7:00pm Hybrid (In-person/Zoom)
 - Additional office time will be held on Thursdays after class when there is high demand indicated by a busy Tuesday office hour
 - When Thursday Office Hours are planned, Zoom links will be shared via Canvas
 - Questions after class on Thursdays are always entertained even when the formal office hour is not scheduled

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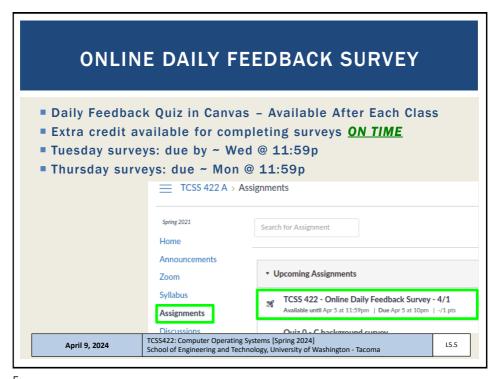
OBJECTIVES - 4/9

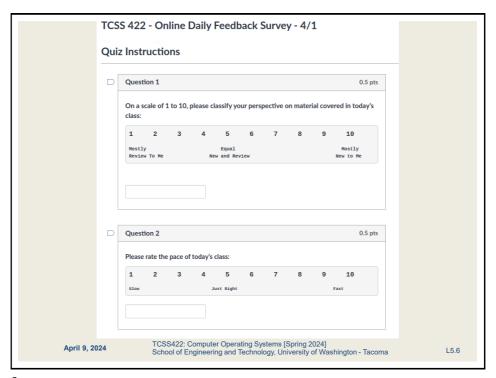
- Questions from 4/4
- Assignment 0
- C Tutorial Pointers, Strings, Exec in C
- Chapter 6: Limited Direct Execution
- Chapter 7: Scheduling Introduction
 - Scheduling metrics
 - Turnaround time, Jain's Fairness Index, Response time
 - FIFO, SJF, STCF, RR schedulers
- Chapter 8: Multi-level Feedback Queue
 - MLFO Scheduler
 - Job Starvation
 - Gaming the Scheduler
 - Examples

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15.4





MATERIAL / PACE

- Please classify your perspective on material covered in today's class (33 respondents):
- 1-mostly review, 5-equal new/review, 10-mostly new
- Average -6.44 (\downarrow previous 6.56)
- Please rate the pace of today's class:
- 1-slow, 5-just right, 10-fast
- Average 5.21 (\downarrow previous 5.38)

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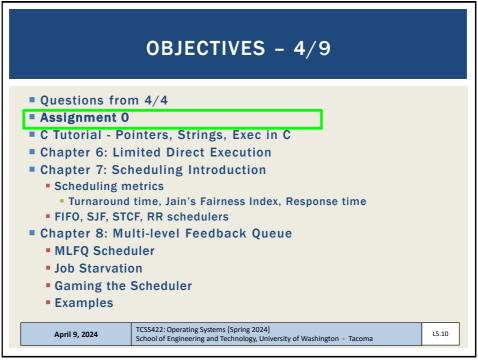
FEEDBACK FROM 4/4

- Wait function parameter being null is still a bit unclear
- There are two variants for the wait() API
- wait(), and waitpid()
 - See 'man 2 wait' for manual page
- wait() takes an optional integer as a 'flag'
- These flags provide instructions for how the API should behave
- If NULL, then there is no special behavior
- The manual page details specific behavior for various constants:
- WIFEXITED return true if child terminated normally
- WEXITSTATUS return exit status of child
- WIFSIGNALED returns true if child process terminated by signal

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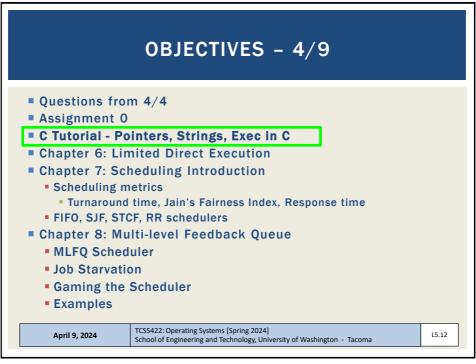
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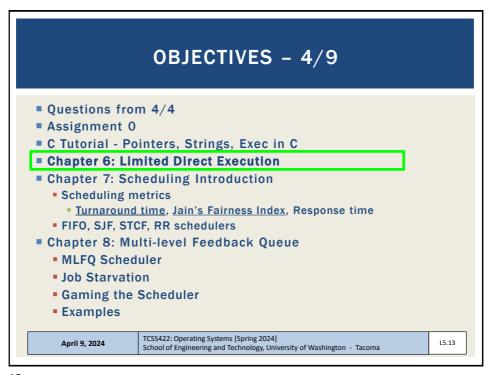


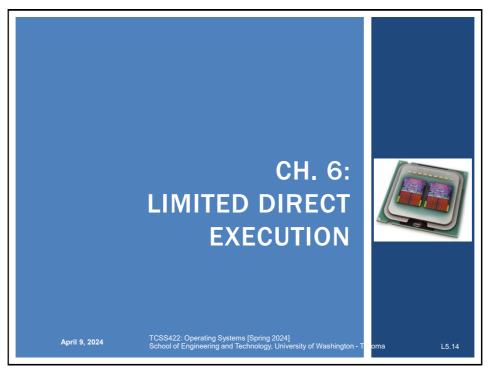


TCSS 422 - SET VMS School of Engineering and Technology hosted Ubuntu 22.04 VMs for TCSS 422 - Spring 2024 are created Connection information on how to access SET VMs has been emailed to students who requested BMs TCSS422: Operating Systems [Spring 2024] School of Engineering and Technology, University of Washington - Tacoma

11



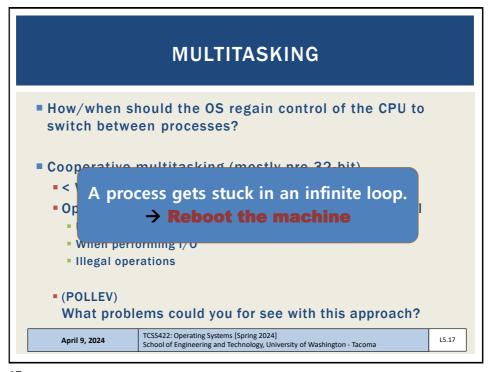




CHAPTER 6 Chapter 6: Limited Direct Execution Direct execution Limited direct execution CPU modes System calls and traps Cooperative multi-tasking Context switching and preemptive multi-tasking TCSS422: Operating Systems [Spring 2024] School of Engineering and Technology, University of Washington - Tacoma

15

MULTITASKING How/when should the OS regain control of the CPU to switch between processes? Cooperative multitasking (mostly pre 32-bit) Vindows 95, Mac OSX Opportunistic: running programs must give up control User programs must call a special yield system call When performing I/O Illegal operations (POLLEV) What problems could you for see with this approach? April 9, 2024 TCSS422: Operating Systems (Spring 2024) School of Engineering and Technology, University of Washington - Tacoma





QUESTION: MULTITASKING

■ What problems exist for regaining the control of the CPU with cooperative multitasking OSes?

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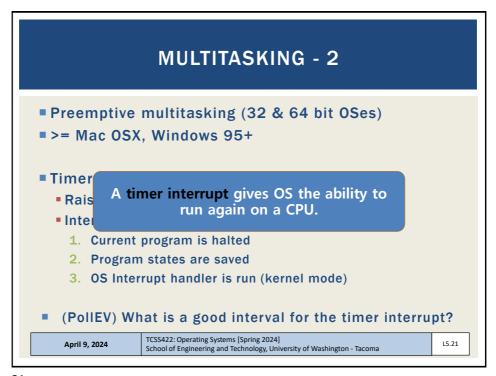
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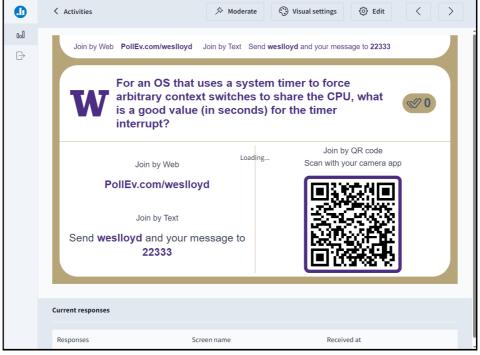
MULTITASKING - 2

- Preemptive multitasking (32 & 64 bit OSes)
- >= Mac OSX, Windows 95+
- Timer interrupt
 - Raised at some regular interval (in ms)
 - Interrupt handling
 - 1. Current program is halted
 - 2. Program states are saved
 - 3. OS Interrupt handler is run (kernel mode)
- (PollEV) What is a good interval for the timer interrupt?

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QUESTION: TIME SLICE

For an OS that uses a system timer to force arbitrary context switches to share the CPU, what is a good value (in seconds) for the timer interrupt?

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23

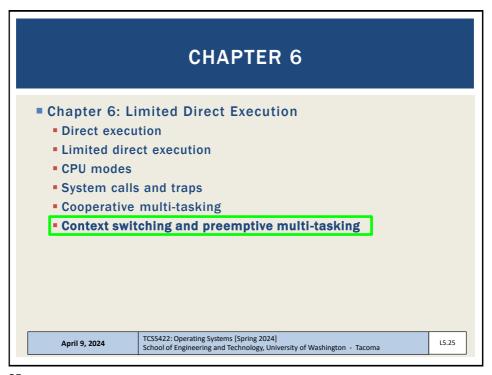
QUESTION: TIME SLICE

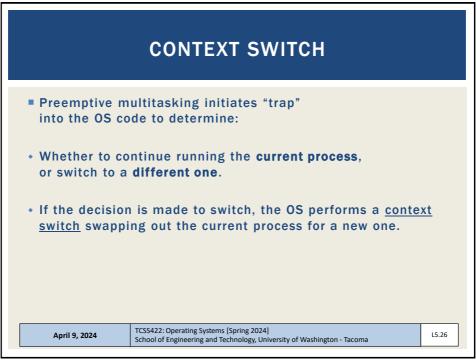
- For an OS that uses a system timer to force arbitrary context switches to share the CPU, what is a good value (in seconds) for the timer interrupt?
 - Typical time slice for process execution is 10 to 100 milliseconds
 - Typical context switch overhead is (switch between processes) 0.01 milliseconds
 - 0.1% of the time slice (1/1000th)

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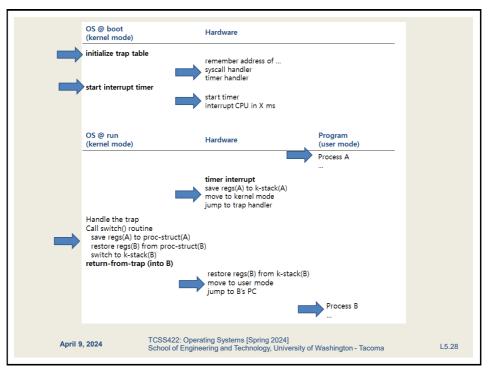
L5.24

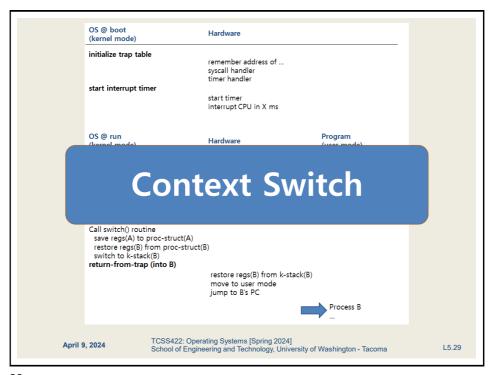


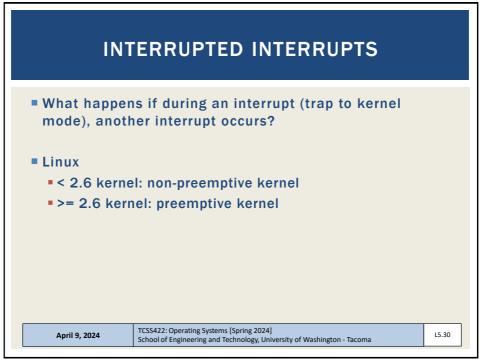


CONTEXT SWITCH - 2 1. Save register values of the current process to its kernel stack General purpose registers PC: program counter (instruction pointer) kernel stack pointer 2. Restore soon-to-be-executing process from its kernel stack 3. Switch to the kernel stack for the soon-to-be-executing process April 9, 2024 TCSS422: Operating Systems [Spring 2024] School of Engineering and Technology, University of Washington - Tacoma

27







PREEMPTIVE KERNEL

- Use "locks" as markers of regions of nonpreemptibility (non-maskable interrupt)
- Preemption counter (preempt_count)
 - begins at zero
 - increments for each lock acquired (not safe to preempt)
 - decrements when locks are released
- ■Interrupt can be interrupted when preempt count=0
 - It is safe to preempt (maskable interrupt)
 - the interrupt is more important

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31

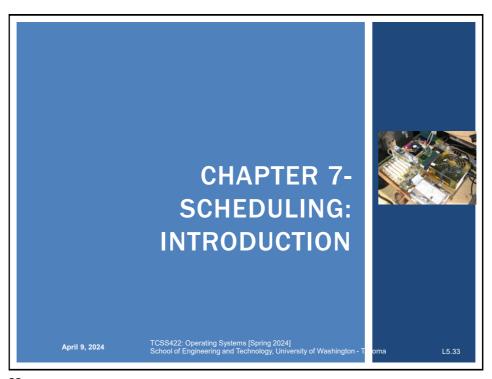
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15 32



■ Metrics: A standard measure to quantify to what degree a system possesses some property. Metrics provide <u>repeatable</u> techniques to quantify and compare systems. ■ Measurements are the numbers derived from the application of metrics ■ Scheduling Metric #1: <u>Turnaround time</u> ■ The time at which the job completes minus the time at which the job arrived in the system $T_{turnaround} = T_{completion} - T_{arrival}$ ■ How is turnaround time different than execution time? April 9, 2024 | TCSS422: Operating Systems [Spring 2024] | School of Engineering and Technology, University of Washington - Tacoma

SCHEDULING METRICS - 2

- Scheduling Metric #2: Fairness
 - Jain's fairness index
 - Quantifies if jobs receive a fair share of system resources

$$\mathcal{J}(x_1,x_2,\ldots,x_n) = rac{(\sum_{i=1}^n x_i)^2}{n \cdot \sum_{i=1}^n x_i^2}$$

- n processes
- x_i is time share of each process
- worst case = 1/n
- best case = 1
- Consider n=3, worst case = .333, best case=1
- With n=3 and x_1 =.2, x_2 =.7, x_3 =.1, fairness=.62
- With n=3 and x_1 =.33, x_2 =.33, x_3 =.33, fairness=1

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L5.35

35

With n=3 and
$$x_1$$
=.2, x_2 =.7, x_3 =.1

$$\mathcal{J}(x_1,x_2,\ldots,x_n) = rac{(\sum_{i=1}^n x_i)^2}{n\cdot\sum_{i=1}^n x_i^2}$$

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L5.36

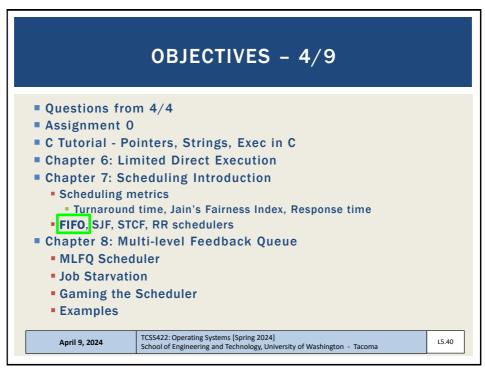
With n=3 and
$$\mathbf{x}_1$$
=.33, \mathbf{x}_2 =.33, \mathbf{x}_3 =.33
$$\mathcal{J}(x_1,x_2,\ldots,x_n) = \frac{(\sum_{i=1}^n x_i)^2}{n\cdot\sum_{i=1}^n x_i^2}$$
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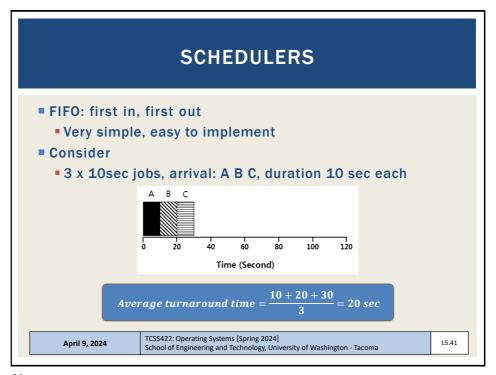
With n=3 and
$$x_1$$
=.33, x_2 =.33, x_3 =.33
$$3 + 33 + 33 + 33 = (99)^2 \qquad 99999 \sim 1$$

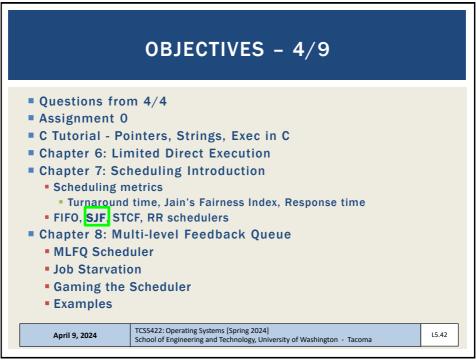
$$7 \cdot (33^2 + 33^2 + 33^2 + 1689)$$

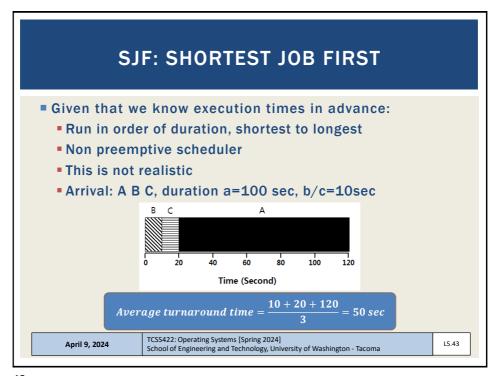
$$7 \cdot (1089 + 1089 + 1689)$$

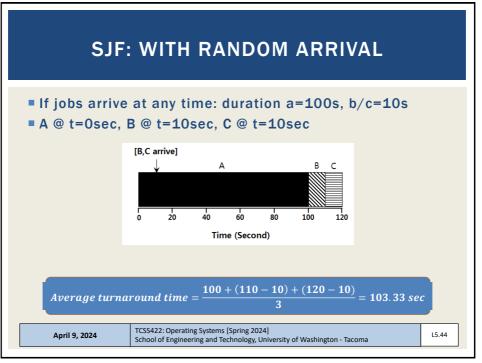
$$7 \cdot (3267) \rightarrow 980$$
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$$7 \cdot (258422 \cdot 0 \text{ Operating Systems (Spring 2024) School of Engineering and Technology, University of Washington - Tacoma L5.39$$

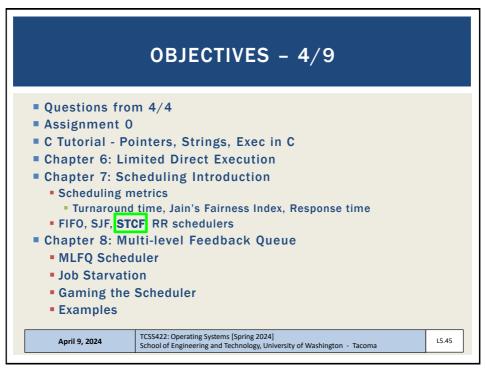


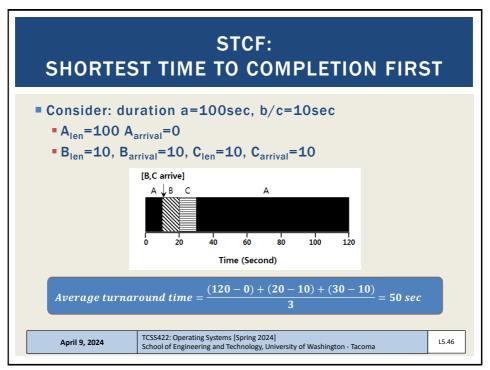




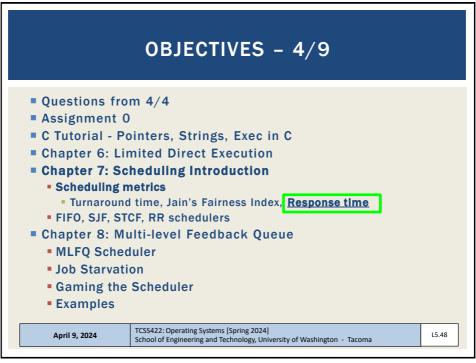


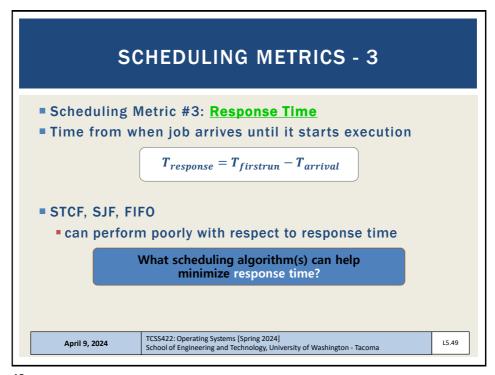


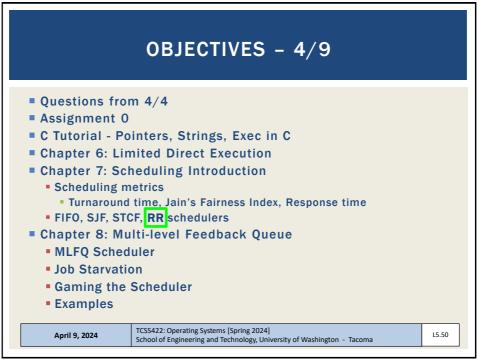


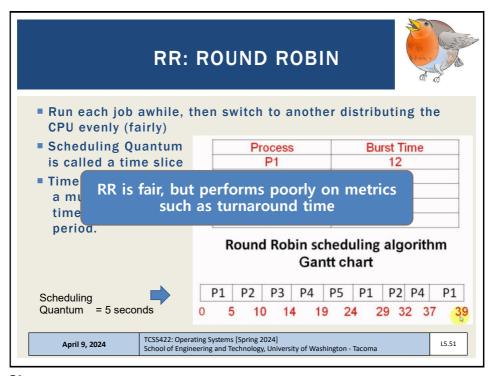


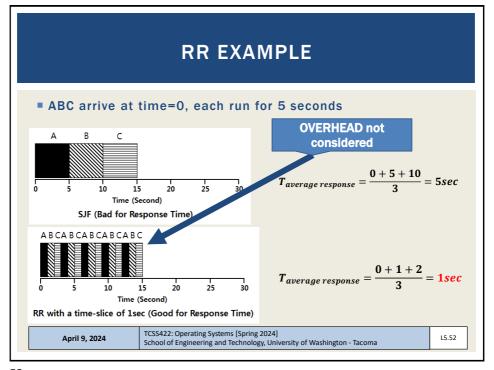


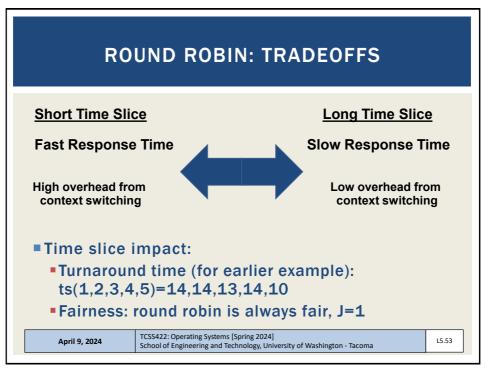


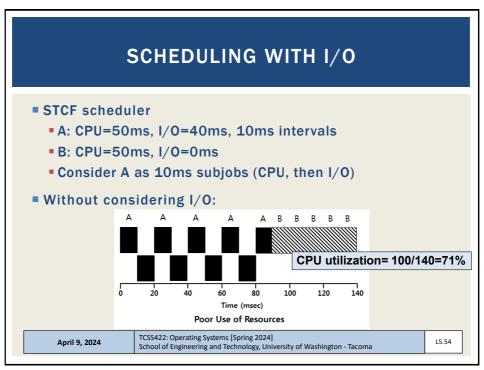


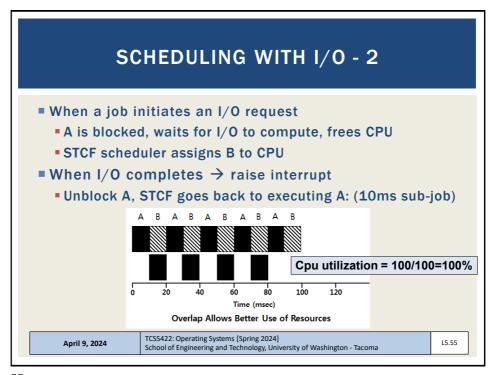


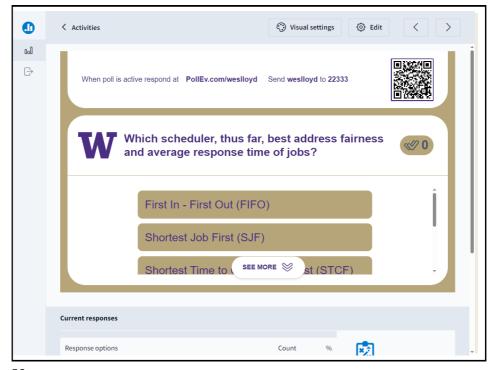






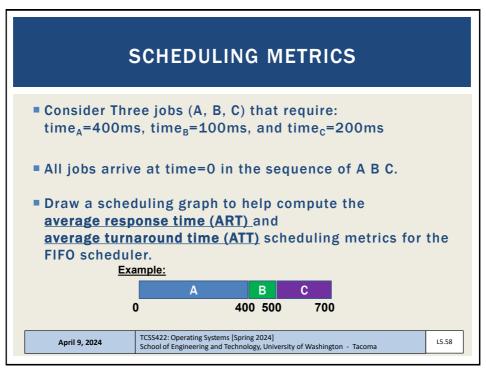


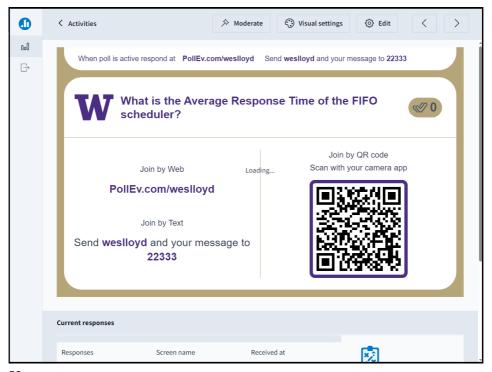


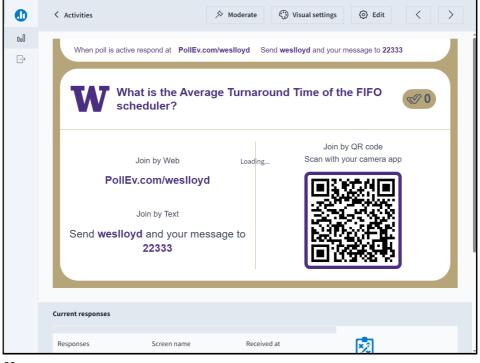


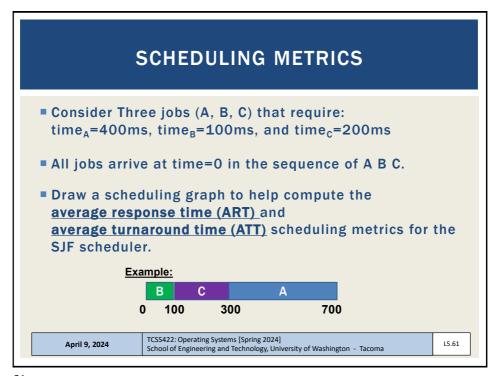
QUESTION: SCHEDULING FAIRNESS Which scheduler, this far, best addresses fairness and average response time of jobs? First In – First Out (FIFO) Shortest Job First (SJF) Shortest Time to Completion First (STCF) Round Robin (RR) None of the Above All of the Above All of the Above

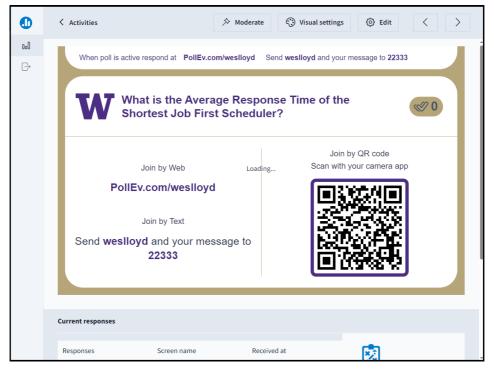
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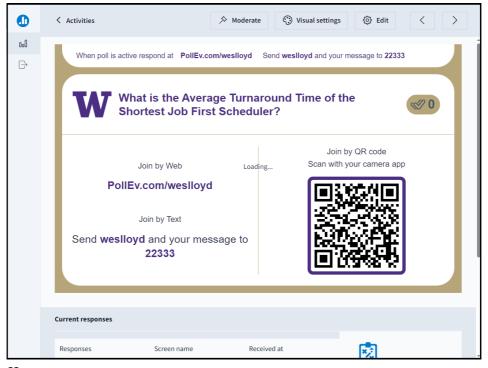


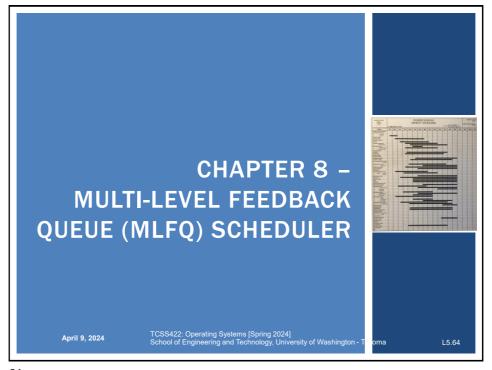


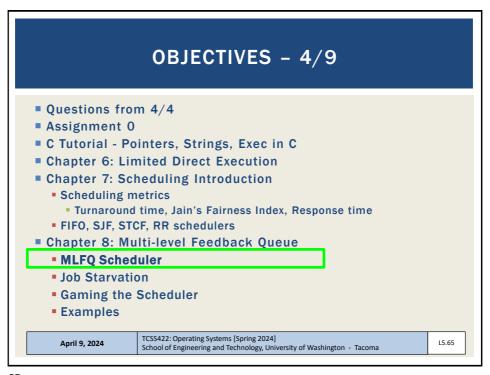


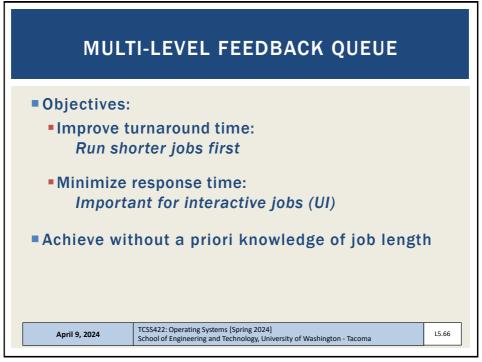


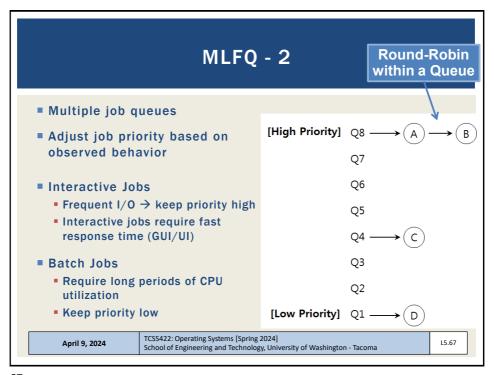




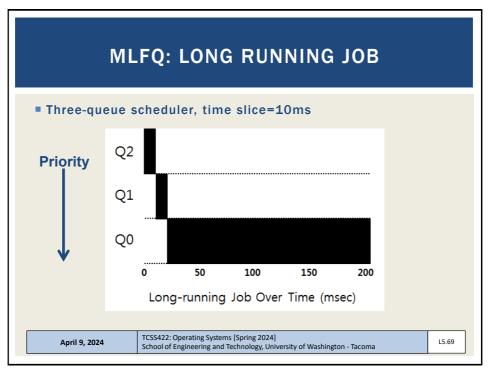


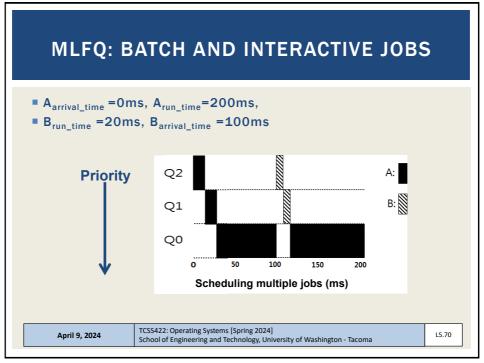


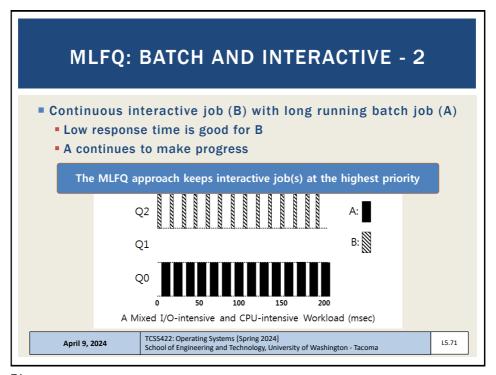


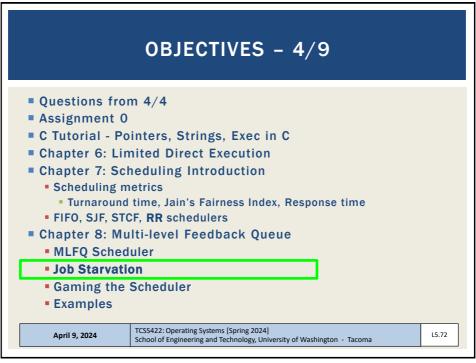


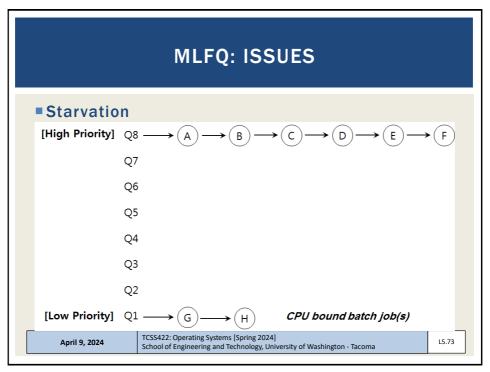


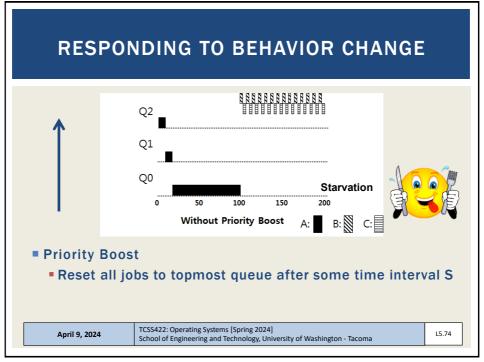


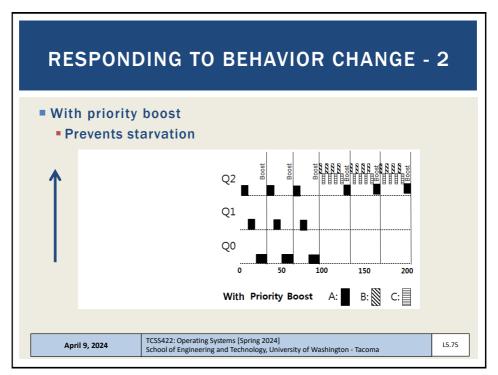


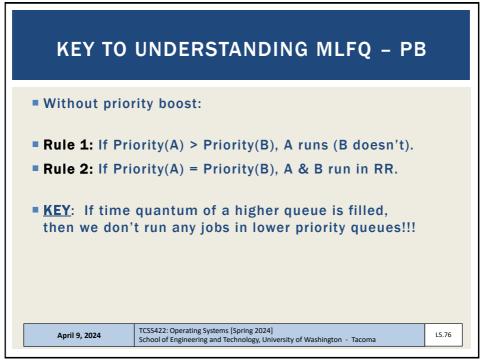


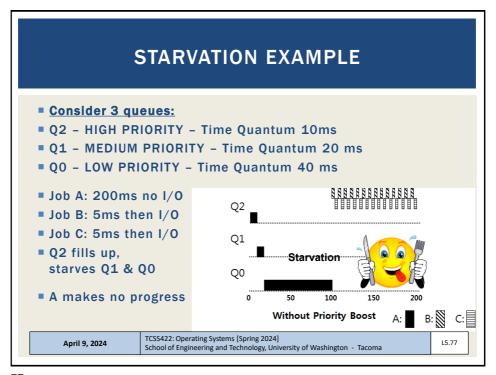


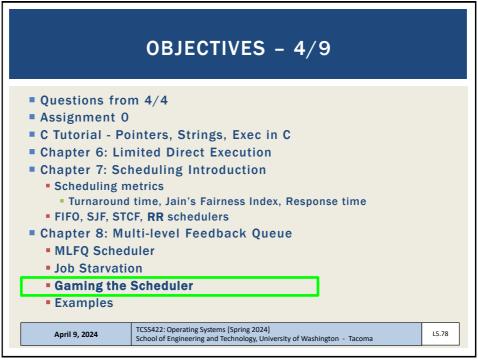


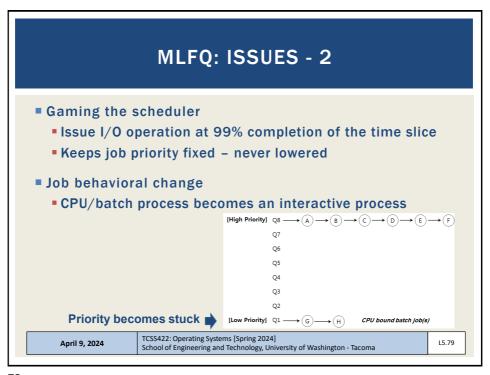


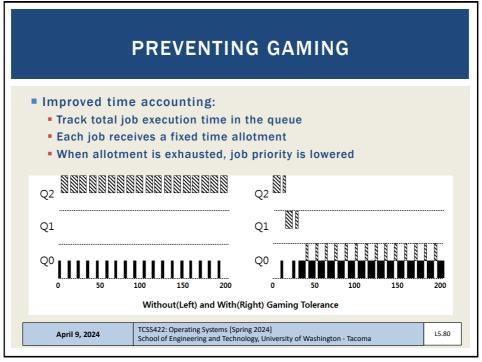


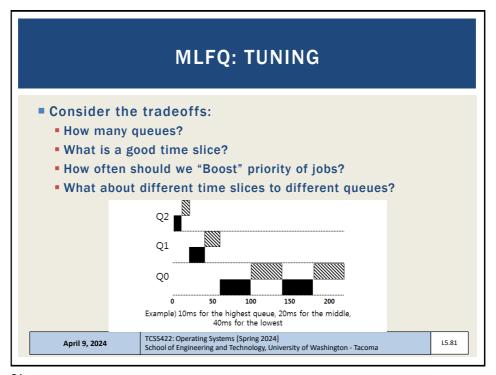


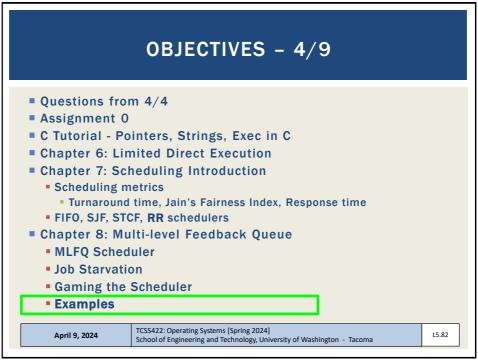












PRACTICAL EXAMPLE

- Oracle Solaris MLFQ implementation
 - 60 Queues →
 w/ slowly increasing time slice (high to low priority)
 - Provides sys admins with set of editable table(s)
 - Supports adjusting time slices, boost intervals, priority changes, etc.
- Advice
 - Provide OS with hints about the process
 - Nice command → Linux

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L5.83

83

MLFQ RULE SUMMARY

- The refined set of MLFQ rules:
- Rule 1: If Priority(A) > Priority(B), A runs (B doesn't).
- Rule 2: If Priority(A) = Priority(B), A & B run in RR.
- Rule 3: When a job enters the system, it is placed at the highest priority.
- Rule 4: Once a job uses up its time allotment at a given level (regardless of how many times it has given up the CPU), its priority is reduced(i.e., it moves down on queue).
- Rule 5: After some time period S, move all the jobs in the system to the topmost queue.

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L5.84

Jackson deploys a 3-level MLFQ scheduler. The time slice is 1 for high priority jobs, 2 for medium priority, and 4 for low priority. This MLFQ scheduler performs a Priority Boost every 6 timer units. When the priority boost fires, the current job is preempted, and the next scheduled job is run in round-robin order.

 Job
 Arrival Time
 Job Length

 A
 T=0
 4

 B
 T=0
 16

 C
 T=0
 8

(11 points) Show a scheduling graph for the MLFQ scheduler for the jobs above. Draw vertical lines for key events and be sure to label the X-axis times as in the example. Please draw clearly. An unreadable graph will loose points.

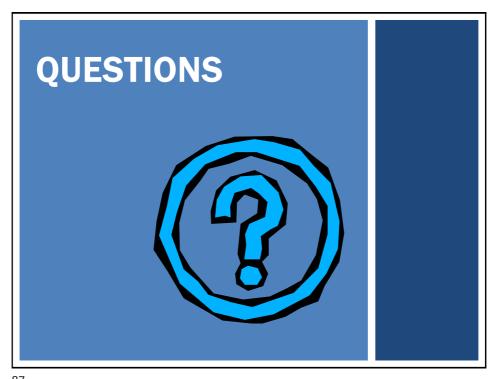
HIGH |
MED |
LOW |

85

EXAMPLE

- Question:
- Given a system with a quantum length of 10 ms in its highest queue, how often would you have to boost jobs back to the highest priority level to guarantee that a single long-running (and potentially starving) job gets at least 5% of the CPU?
- Some combination of n short jobs runs for a total of 10 ms per cycle without relinquishing the CPU
 - E.g. 2 jobs = 5 ms ea; 3 jobs = 3.33 ms ea, 10 jobs = 1 ms ea
 - n jobs always uses full time quantum (10 ms)
 - Batch jobs starts, runs for full quantum of 10ms
 - All other jobs run and context switch totaling the quantum per cycle
 - If 10ms is 5% of the CPU, when must the priority boost be ???
 - ANSWER → Priority boost should occur every 200ms

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8/