

TCSS 422: OPERATING SYSTEMS

The Process API & Limited Direct Execution

Wes J. Lloyd
School of Engineering and Technology
University of Washington - Tacoma

April 10, 2025

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OBJECTIVES – 4/10

■ Questions from 4/8

■ C Review Survey – Closes Friday April 11

■ Assignment 0 - Update

■ Chapter 5: Process API

- fork(), wait(), exec()

■ Chapter 6: Limited Direct Execution

- Direct execution
- Limited direct execution
- CPU modes
- System calls and traps
- Cooperative multi-tasking
- Context switching and preemptive multi-tasking

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TEXT BOOK COUPON

- 15% off textbook code: **PUBLISHPAGES15** (*through Fri Apr 11*)
- <https://www.lulu.com/shop/andrea-arpaci-dusseau-and-remzi-arpaci-dusseau/operating-systems-three-easy-pieces-hardcover-version-110/hardcover/product-15gjeeky.html?q=three+easy+pieces+operating+systems&page=1&pageSize=4>
- With coupon textbook is only \$33.79 + tax & shipping

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TCSS 422 – OFFICE HRS – SPRING 2025

- Office Hours plan for Spring (by Zoom):
- Monday 11:30am - 12:30p GTA Xinghan
- Tuesday 11:30am - 12:30p GTA Xinghan
- Wednesday 11:00am - 12:00p Instructor Wes
- Friday 12:00pm - 1:00p Wes or Xinghan
- No office hours this Friday April 11th
 - Scheduling conflict for Wes & Xinghan
- Instructor is available after class at 6pm in CP 229 each day

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ONLINE DAILY FEEDBACK SURVEY

■ Daily Feedback Quiz in Canvas – Available After Each Class

■ Extra credit available for completing surveys **ON TIME**

■ Tuesday surveys: due by ~ Wed @ 11:59p

■ Thursday surveys: due ~ Mon @ 11:59p

TCSS 422 A > Assignments

Spring 2021

Search for Assignment

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Assignments

Discussions

Upcoming Assignments

TCSS 422 - Online Daily Feedback Survey - 4/1

Available until Apr 5 at 11:59pm | Due Apr 5 at 10pm | -/1 pts

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TCSS 422 - Online Daily Feedback Survey - 4/1

Quiz Instructions

Question 1

0.5 pts

On a scale of 1 to 10, please classify your perspective on material covered in today's class:

12345678910

Mostly Review To MeEqual New and ReviewMostly New to Me

Question 2

0.5 pts

Please rate the pace of today's class:

12345678910

SlowJust RightFast

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Slides by Wes J. Lloyd

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MATERIAL / PACE

- Please classify your perspective on material covered in today’s class (39 of 63 respondents – 61.9% !!) **precipitous drop =/**
- 1-mostly review, 5-equal new/review, 10-mostly new
- **Average – 6.64 (↑ - previous 5.86)**

- Please rate the pace of today’s class:
- 1-slow, 5-just right, 10-fast
- **Average – 5.29 (↑ - previous 5.11)**

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FEEDBACK FROM 4/8

- **So, it's advantageous for a context switch to occur when a program/process is blocked, but not when it's ready to run and just waiting?**
- A context switch is the act of **stopping** a running process, removing it from the CPU, and moving it to a blocked state (voluntary CS), or placing it on the run queue (non-voluntary CS)
- On the run queue, jobs of the same priority compete to be rescheduled to run on the CPU based on how long they've waited
- When a program is blocked, it remains blocked until an interrupt fires signaling that the I/O result is available
 - Process moves to the run queue and waits to be rescheduled to run on the CPU

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FEEDBACK - 2

- How is it communicated to the OS that a process is in a good state for a voluntary context switch?
- Changing a process's state from RUNNING to BLOCKED will automatically remove it from the CPU (voluntary C/S)
- The OS does not perform voluntary context switches

- Example:
- Call to sleep function: `sleep(1) ;`
- Sleep function uses a kernel API to request an interrupt (signal) be generated to wake up the process when the sleep time elapses
- Sleep function moves process from RUNNING to BLOCKED

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CHATGPT ACTIVITY

- **sleep() is the C function in Linux that sleeps for n seconds**
- 1. Using ChatGPT (or another LLM) determine if Linux C sleep() is a blocking API call
- 2. Using ChatGPT or Google determine which header file sleep() is defined in

- We suspect that sleep() uses a Linux kernel API call to actually do the sleeping
- WHY ? (discuss)
- Linux kernel API calls are privileged functions which every-day programmers do not usually call
- Linux sleep() provides a convenience wrapper function to make the programmer's life easier

- 3. Using Chat GPT, determine which Linux kernel API function(s) are used to implement the user space sleep() function which serves as a wrapper.

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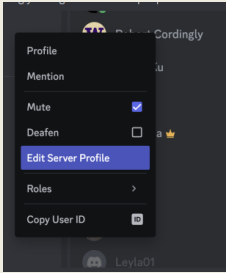
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TCSS 422 DISCORD SERVER

- Please join the TCSS 422 A – Spring 2025 Discord Server
- <https://discord.gg/Jh5Cp8TMYn>
- Under Edit Server Profile:
Please update your ‘Server Nickname’
to your real name or UW NET ID
THANK YOU



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 - fork(), wait(), exec()
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C REVIEW SURVEY - AVAILABLE THRU 4/11

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OBJECTIVES – 4/10

- Questions from 4/8
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- **Assignment 0 - Update**
- Chapter 4: Linux process data structure - task_struct
- Chapter 5: Process API
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ASSIGNMENT 0

- In the homework, it specifies to use “non-interactive” commands. What does this mean exactly?
- An non-interactive command does not require any input from the user (i.e. from the keyboard)
- Non-interactive commands and scripts can run entirely on their own without intervention
- These commands are considered “headless” in that they don’t feature a USER INTERFACE, either a GUI, or TUI
- What is a TUI?
 - *Text-based User Interface
 - TUI is also a bird



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TCSS 422 – SET VMS

- Request submitted for School of Engineering and Technology hosted Ubuntu 24.04 VMs for TCSS 422 – Spring 2025

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FINISH CHAPTER 4

- Switch to Lecture 3 Slides
- Slides L3.43 to L3.41

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CHAPTER 5: C PROCESS API

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fork()

- Creates a new process - think of “a fork in the road”
- “Parent” process is the original
- Creates “child” process of the program from the current execution point
- Book says “pretty odd”
- Creates a **duplicate** program instance (these are processes!)
- **Copy** of
 - Address space (memory)
 - Register
 - Program Counter (PC)
- Fork returns
 - child PID to parent
 - 0 to child



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FORK EXAMPLE

■ p1.c

```
#include <stdio.h>
#include <stdlib.h>
#include <unistd.h>

int main(int argc, char *argv[]){
    printf("hello world (pid:%d)\n", (int) getpid());
    int rc = fork();
    if (rc < 0) {          // fork failed; exit
        fprintf(stderr, "fork failed\n");
        exit(1);
    } else if (rc == 0) {  // child (new process)
        printf("hello, I am child (pid:%d)\n", (int) getpid());
    } else {              // parent goes down this path (main)
        printf("hello, I am parent of %d (pid:%d)\n",
               rc, (int) getpid());
    }
    return 0;
}
```

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FORK EXAMPLE - 2

■ Non deterministic ordering of execution

```
prompt> ./p1
hello world (pid:29146)
hello, I am parent of 29147 (pid:29146)
hello, I am child (pid:29147)
prompt>
```

or

```
prompt> ./p1
hello world (pid:29146)
hello, I am child (pid:29147)
hello, I am parent of 29147 (pid:29146)
prompt>
```

■ CPU scheduler determines which to run first

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:(){:|: & }::

```
graph TD; P[Parent] -- fork --> C1[Child 1]; P -- fork --> C2[Child 2]; C1 -- fork --> C1_1[Child 1.1]; C1 -- fork --> C1_2[Child 1.2]; C2 -- fork --> C2_1[Child 2.1]; C2 -- fork --> C2_2[Child 2.2];
```

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wait()

- `wait()`, `waitpid()`
- Called by parent process
- Waits for a child process to finish executing
- Not a `sleep()` function
- Provides some ordering to multi-process execution



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FORK WITH WAIT

```
#include <stdio.h>
#include <stdlib.h>
#include <unistd.h>
#include <sys/wait.h>

int main(int argc, char *argv[]){
    printf("hello world (pid:%d)\n", (int) getpid());
    int rc = fork();
    if (rc < 0) {          // fork failed; exit
        fprintf(stderr, "fork failed\n");
        exit(1);
    } else if (rc == 0) { // child (new process)
        printf("hello, I am child (pid:%d)\n", (int) getpid());
    } else {              // parent goes down this path (main)
        int wc = wait(NULL);
        printf("hello, I am parent of %d (wc:%d) (pid:%d)\n",
            rc, wc, (int) getpid());
    }
    return 0;
}
```

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FORK WITH WAIT - 2

▪ Deterministic ordering of execution

```
prompt> ./p2
hello world (pid:29266)
hello, I am child (pid:29267)
hello, I am parent of 29267 (wc:29267) (pid:29266)
prompt>
```

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FORK EXAMPLE

▪ Linux example

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exec()

- Supports running an external program by “transferring control”
- 6 types: `execl()`, `execlp()`, `execle()`, `execv()`, `execvp()`, `execvpe()`
- `execl()`, `execlp()`, `execle()`: `const char *arg` (example: `execl.c`)
Provide cmd and args as individual params to the function
Each arg is a pointer to a null-terminated string
ODD: pass a variable number of args: (`arg0`, `arg1`, .. `argn`)
- `execv()`, `execvp()`, `execvpe()` (example: `exec.c`)
Provide cmd and args as an Array of pointers to strings
Strings are null-terminated
First argument is name of command being executed
Fixed number of args passed in

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EXEC EXAMPLE

```
#include <stdio.h>
#include <stdlib.h>
#include <unistd.h>
#include <string.h>
#include <sys/wait.h>

int main(int argc, char *argv[]){
    printf("hello world (pid:%d)\n", (int) getpid());
    int rc = fork();
    if (rc < 0) {                // fork failed; exit
        fprintf(stderr, "fork failed\n");
        exit(1);
    } else if (rc == 0) {        // child (new process)
        printf("hello, I am child (pid:%d)\n", (int) getpid());
        char *myargs[3];
        myargs[0] = strdup("wc");           // program: "wc" (word count)
        myargs[1] = strdup("p3.c");         // argument: file to count
        myargs[2] = NULL;                  // marks end of array
        ...
    }
```

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EXEC EXAMPLE - 2

```
...
    execvp(myargs[0], myargs); // runs word count
    printf("this shouldn't print out");
} else {                // parent goes down this path (main)
    int wc = wait(NULL);
    printf("hello, I am parent of %d (wc:%d) (pid:%d)\n",
           rc, wc, (int) getpid());
    return 0;
}
```

```
prompt> ./p3
hello world (pid:29383)
hello, I am child (pid:29384)
29 107 1030 p3.c
hello, I am parent of 29384 (wc:29384) (pid:29383)
prompt>
```

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EXEC WITH FILE REDIRECTION (OUTPUT)

■ Example:

<https://faculty.washington.edu/wlloyd/courses/tcss422/examples/exec2.c>

```
#include <stdio.h>
#include <stdlib.h>
#include <unistd.h>
#include <string.h>
#include <fcntl.h>
#include <sys/wait.h>

int
main(int argc, char *argv[]){
    int rc = fork();
    if (rc < 0) {                // fork failed; exit
        fprintf(stderr, "fork failed\n");
        exit(1);
    } else if (rc == 0) { // child: redirect standard output to a file
        close(STDOUT_FILENO);
        open("./p4.output", O_CREAT|O_WRONLY|O_TRUNC, S_IRWXU);
        ...
    }
}
```

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FILE MODE BITS

➡

```
S_IRWXU
read, write, execute/search by owner
S_IRUSR
read permission, owner
S_IWUSR
write permission, owner
S_IXUSR
execute/search permission, owner
S_IRWXG
read, write, execute/search by group
S_IRGRP
read permission, group
S_IWGRP
write permission, group
S_IXGRP
execute/search permission, group
S_IRWXO
read, write, execute/search by others
S_IROTH
read permission, others
S_IWOTH
write permission, others
```

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EXEC W/ FILE REDIRECTION (OUTPUT) - 2

```
...  
// now exec "wc"...  
char *myargs[3];  
myargs[0] = strdup("wc");           // program: "wc" (word count)  
myargs[1] = strdup("p4.c");         // argument: file to count  
myargs[2] = NULL;                   // marks end of array  
execvp(myargs[0], myargs);          // runs word count  
} else {                             // parent goes down this path (main)  
    int wc = wait(NULL);  
}  
return 0;  
}
```

prompt> ./p4
prompt> cat p4.output
32 109 846 p4.c
prompt>

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Activities

Visual settingsEdit

When poll is active respond at PollEv.com/weslloyd Send weslloyd to 22333

W

Which Process API call is used to launch a different program from the current program?

0

Fork()

Exec()

Wait()

More of the above

SEE MORE

Current responses

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QUESTION: PROCESS API

- Which Process API call is used to launch a different program from the current program?
- (a) Fork()
- (b) Exec()
- (c) Wait()
- (d) None of the above
- (e) All of the above

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WE WILL RETURN AT
5:00PM

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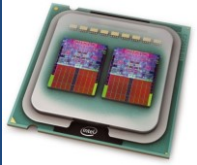
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CH. 6:
LIMITED DIRECT
EXECUTION



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COMPUTER BOOT SEQUENCE:
OS WITH DIRECT EXECUTION

▪ What if programs could directly control the CPU / system?

OS	Program
1. Create entry for process list 2. Allocate memory for program 3. Load program into memory 4. Set up stack with <code>argc / argv</code> 5. Clear registers 6. Execute <code>call main()</code>	7. Run <code>main()</code> 8. Execute <code>return from main()</code>
9. Free memory of process 10. Remove from process list	

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COMPUTER BOOT SEQUENCE:
OS WITH DIRECT EXECUTION

▪ What if programs could directly control the CPU / system?

OS	Program
1. Create entry for process list 2. Allocate memory for	
Without <i>limits</i> on running programs, the OS wouldn't be in control of anything and would "just be a library"	
3. Load program into memory 4. Set up stack with <code>argc / argv</code> 5. Clear registers 6. Execute <code>call main()</code>	7. Run <code>main()</code> 8. Execute <code>return from main()</code>
9. Free memory of process 10. Remove from process list	

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DIRECT EXECUTION - 2

■ With direct execution:

How does the OS stop a program from running, and switch to another to support **time sharing**?

How do programs share disks and perform I/O if they are given direct control? Do they know about each other?

With direct execution, how can dynamic memory structures such as linked lists grow over time?

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CONTROL TRADEOFF

■ Too little control:

- No security
- No time sharing

■ Too much control:

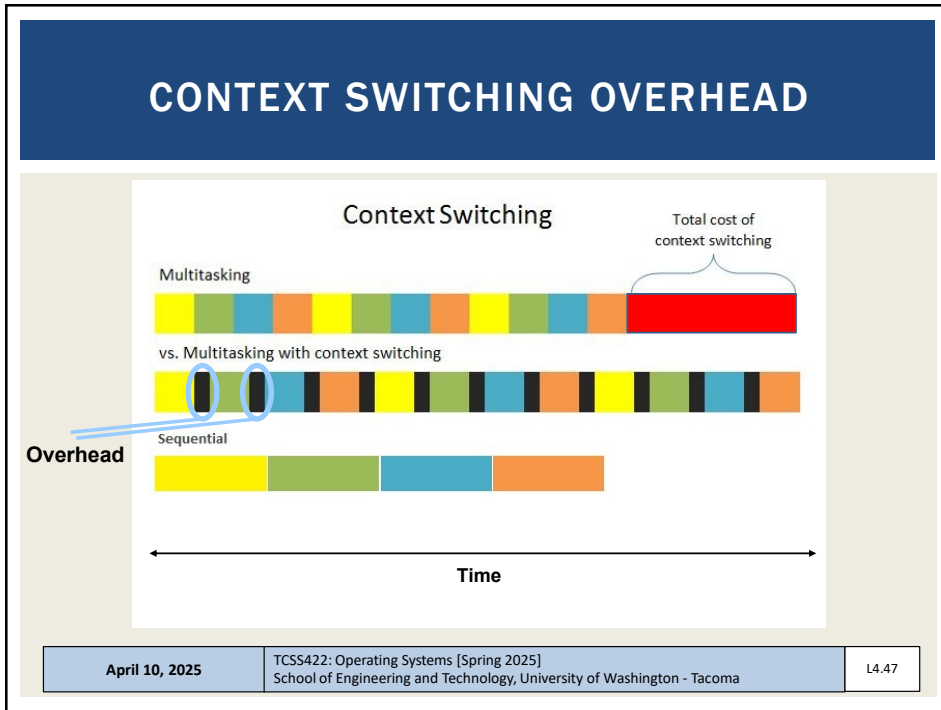
- Too much OS overhead
- Poor performance for compute & I/O
- Complex APIs (system calls), difficult to use

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LIMITED DIRECT EXECUTION

- OS implements LDE to support time/resource sharing
- Limited direct execution means “only limited” processes can execute **DIRECTLY** on the CPU in trusted mode
- **TRUSTED** means the process is trusted, and it can do anything... (e.g. it is a system / kernel level process)
- Enabled by *protected (safe) control transfer*
- CPU supported context switch
- Provides data isolation

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CPU MODES

- Utilize CPU Privilege Rings (Intel x86)
 - rings 0 (kernel), 1 (VM kernel), 2 (unused), 3 (user)

access

←

no access

- User mode:**
Application is running, but w/o direct I/O access
- Kernel mode:**
OS kernel is running performing restricted operations

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CPU MODES

- User mode: ring 3 - untrusted**
 - Some instructions and registers are disabled by the CPU
 - Exception registers
 - HALT instruction
 - MMU instructions
 - OS memory access
 - I/O device access
- Kernel mode: ring 0 – trusted**
 - All instructions and registers enabled

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SYSTEM CALLS

- Implement restricted “OS” operations
- Kernel exposes key functions through an API:
 - Device I/O (e.g. file I/O)
 - Task swapping: context switching between processes
 - Memory management/allocation: malloc()
 - Creating/destroying processes

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TRAPS:
SYSTEM CALLS, EXCEPTIONS, INTERRUPTS

▪ Trap: any transfer to kernel mode

▪ Three kinds of traps

- **System call:** (planned) user → kernel
 - SYSCALL for I/O, etc.
- **Exception:** (error) user → kernel
 - Div by zero, page fault, page protection error
- **Interrupt:** (event) user → kernel
 - Non-maskable vs. maskable
 - Keyboard event, network packet arrival, timer ticks
 - Memory parity error (ECC), hard drive failure

Mainline Code

```
loop() {  
  instruction 1  
  instruction 2  
  instruction 3  
  instruction 4  
  instruction 5  
}
```

Interrupt

Interrupt Service Routine

```
ISR() {  
  instruction 1  
  instruction 2  
  instruction 3  
}
```

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EXCEPTION TYPES

Exception type	Synchronous vs. asynchronous	User request vs. coerced	User maskable vs. nonmaskable	Within vs. between instructions	Resume vs. terminate
I/O device request	Asynchronous	Coerced	Nonmaskable	Between	Resume
Invoke operating system	Synchronous	User request	Nonmaskable	Between	Resume
Tracing instruction execution	Synchronous	User request	User maskable	Between	Resume
Breakpoint	Synchronous	User request	User maskable	Between	Resume
Integer arithmetic overflow	Synchronous	Coerced	User maskable	Within	Resume
Floating-point arithmetic overflow or underflow	Synchronous	Coerced	User maskable	Within	Resume
Page fault	Synchronous	Coerced	Nonmaskable	Within	Resume
Misaligned memory accesses	Synchronous	Coerced	User maskable	Within	Resume
Memory protection violation	Synchronous	Coerced	Nonmaskable	Within	Resume
Using undefined instruction	Synchronous	Coerced	Nonmaskable	Within	Terminate
Hardware malfunction	Asynchronous	Coerced	Nonmaskable	Within	Terminate
Power failure	Asynchronous	Coerced	Nonmaskable	Within	Terminate

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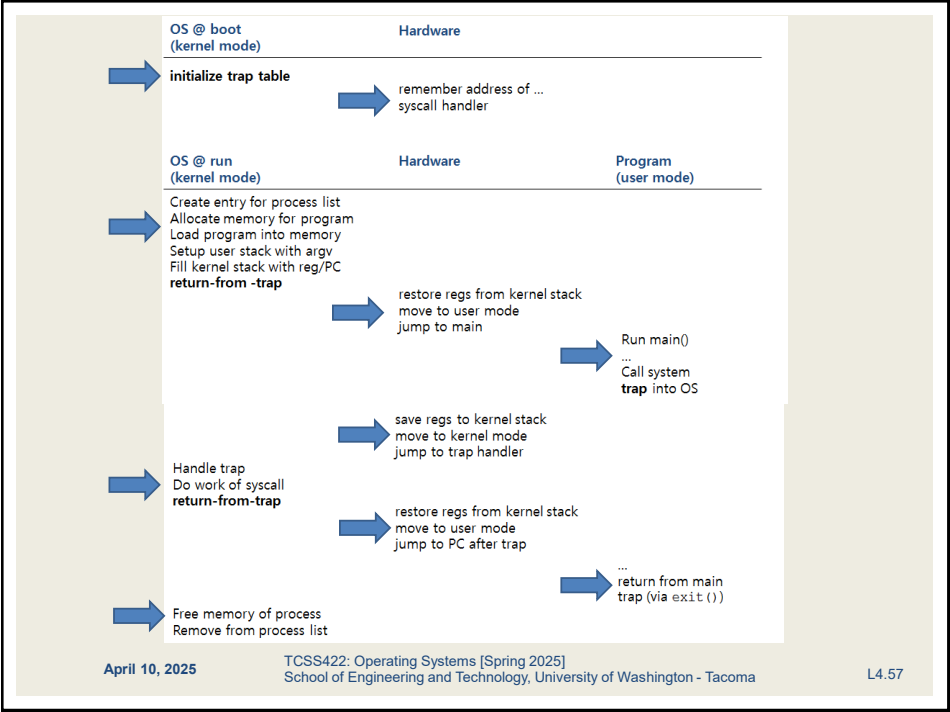
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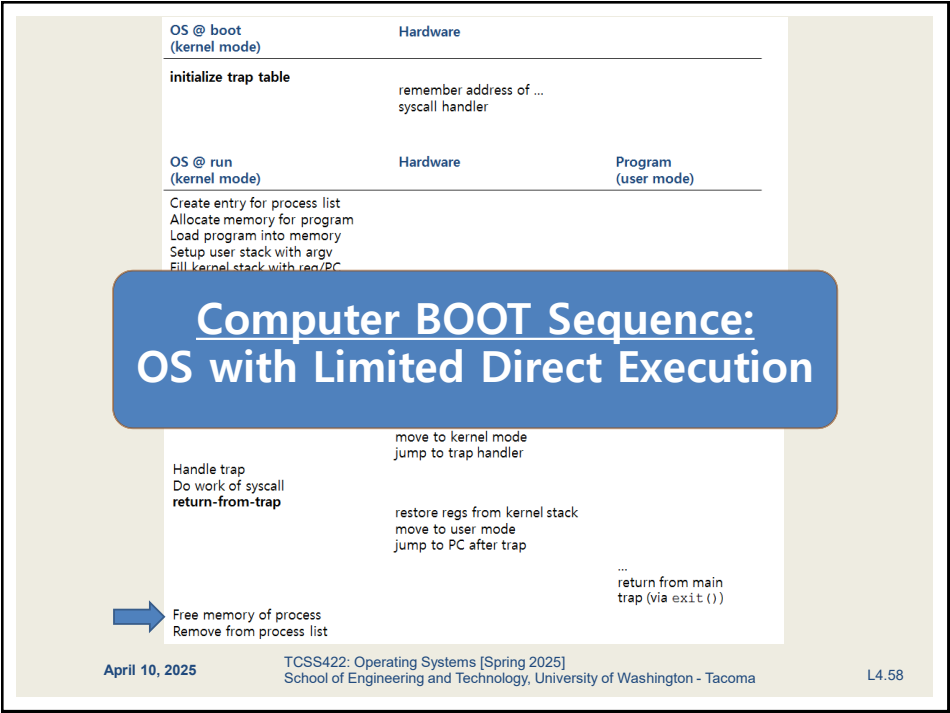
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OBJECTIVES – 4/10

- Questions from 4/8
- C Review Survey – Closes Friday April 11
- Assignment 0 - Update
- Chapter 5: Process API
 - fork(), wait(), exec()
- Chapter 6: Limited Direct Execution
 - Direct execution
 - Limited direct execution
 - CPU modes
 - System calls and traps
 - **Cooperative multi-tasking**
 - Context switching and preemptive multi-tasking

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MULTITASKING

- How/when should the OS regain control of the CPU to switch between processes?
- Cooperative multitasking (mostly pre 32-bit)
 - < Windows 95, Mac OSX
 - Opportunistic: running programs must give up control
 - User programs must call a special **yield** system call
 - When performing I/O
 - Illegal operations
 - (POLLEV)

What problems could you for see with this approach?

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MULTITASKING

- How/when should the OS regain control of the CPU to switch between processes?
- Cooperative multitasking (mostly pre 32-bit)
 - < Voluntary
 - Operational
 - When performing I/O
 - Illegal operations
- (POLLEV)
What problems could you for see with this approach?

A process gets stuck in an infinite loop.
→ **Reboot the machine**

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Activities

ModerateVisual settingsEdit

Join by WebPollEv.com/weslloydJoin by TextSend weslloyd and your message to 22333

W

What problems exist for regaining control of the CPU with cooperative multitasking OSes?

0

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Scan with your camera app

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Current responses

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QUESTION: MULTITASKING

- What problems exist for regaining the control of the CPU with cooperative multitasking OSes?

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MULTITASKING - 2

- Preemptive multitasking (32 & 64 bit OSes)
- $\geq$ Mac OSX, Windows 95+
- Timer interrupt
 - Raised at some regular interval (in ms)
 - Interrupt handling
 1. Current program is halted
 2. Program states are saved
 3. OS Interrupt handler is run (kernel mode)
- (PollEV) What is a good interval for the timer interrupt?

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MULTITASKING - 2

- Preemptive multitasking (32 & 64 bit OSes)
 - >= Mac OSX, Windows 95+
- Timer interrupt
 - A timer interrupt gives OS the ability to run again on a CPU.
 - Raised by hardware
 - Interrupt handler
 - 1. Current program is halted
 - 2. Program states are saved
 - 3. OS Interrupt handler is run (kernel mode)
- (PollEV) What is a good interval for the timer interrupt?

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Activities

Moderate

Visual settings

Edit

<

>

When poll is active respond at

PollEv.com /weslloyd

Send weslloyd and your message to 22333

W

For an OS that uses a system timer to force arbitrary context switches to share the CPU, what is a good value (in seconds) for the timer interrupt?

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Loading...

Nobody has responded yet.

Hang tight! Responses are coming in.

Current responses

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QUESTION: TIME SLICE

- For an OS that uses a system timer to force arbitrary context switches to share the CPU, what is a good value (in seconds) for the timer interrupt?

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QUESTION: TIME SLICE

- For an OS that uses a system timer to force arbitrary context switches to share the CPU, what is a good value (in seconds) for the timer interrupt?
 - Typical time slice for process execution is 10 to 100 milliseconds
 - Typical context switch overhead is (*switch between processes*) 0.01 milliseconds
 - 0.1% of the time slice ($1/1000^{\text{th}}$)

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OBJECTIVES – 4/10

- Questions from 4/8
- C Review Survey – Closes Friday April 11
- Assignment 0 - Update
- Chapter 5: Process API
 - `fork()`, `wait()`, `exec()`
- Chapter 6: Limited Direct Execution
 - Direct execution
 - Limited direct execution
 - CPU modes
 - System calls and traps
 - Cooperative multi-tasking
 - **Context switching and preemptive multi-tasking**

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CONTEXT SWITCH

- Preemptive multitasking initiates “trap” into the OS code to determine:
 - ◆ Whether to continue running the **current process**, or switch to a **different one**.
 - ◆ If the decision is made to switch, the OS performs a context switch swapping out the current process for a new one.

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CONTEXT SWITCH - 2

1. Save register values of the current process to its kernel stack

- General purpose registers
- PC: program counter (instruction pointer)
- kernel stack pointer

2. Restore soon-to-be-executing process from its kernel stack

3. Switch to the kernel stack for the soon-to-be-executing process

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The diagram illustrates the context switch process across three tracks: OS @ boot (kernel mode), OS @ run (kernel mode), and Program (user mode). Hardware actions are shown between the OS tracks.

- OS @ boot (kernel mode):**
 - initialize trap table
 - start interrupt timer
- Hardware:**
 - remember address of ... syscall handler, timer handler
 - start timer, interrupt CPU in X ms
- OS @ run (kernel mode):**
 - timer interrupt: save regs(A) to k-stack(A), move to kernel mode, jump to trap handler
 - Handle the trap: Call switch() routine, save regs(A) to proc-struct(A), restore regs(B) from proc-struct(B), switch to k-stack(B), return-from-trap (into B)
 - return-from-trap (into B): restore regs(B) from k-stack(B), move to user mode, jump to B's PC
- Program (user mode):**
 - Process A ...
 - Process B ...

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OS @ boot
(kernel mode)

initialize trap table

start interrupt timer

OS @ run
(kernel mode)

Hardware

remember address of ...
syscall handler
timer handler

start timer
interrupt CPU in X ms

Hardware

Program
(user mode)

Call switch() routine
save regs(A) to proc-struct(A)
restore regs(B) from proc-struct(B)
switch to k-stack(B)
return-from-trap (into B)

restore regs(B) from k-stack(B)
move to user mode
jump to B's PC

Process B
...

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INTERRUPTED INTERRUPTS

■ What happens if during an interrupt (trap to kernel mode), another interrupt occurs?

■ Linux

- < 2.6 kernel: non-preemptive kernel
- >= 2.6 kernel: preemptive kernel

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PREEMPTIVE KERNEL

- Use “locks” as markers of regions of non-preemptibility (non-maskable interrupt)
- Preemption counter (`preempt_count`)
 - begins at zero
 - increments for each lock acquired (not safe to preempt)
 - decrements when locks are released
- Interrupt can be interrupted when `preempt_count=0`
 - It is safe to preempt (maskable interrupt)
 - the interrupt is more important

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QUESTIONS



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