

TEXT BOOK COUPON

- 15% off textbook code: **POETRY15** (through Friday Apr 5)
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- With coupon textbook is only \$33.79 + tax & shipping

April 4, 2024

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TCSS 422 - OFFICE HRS - SPRING 2024

Tuesdays after class until 7:00pm

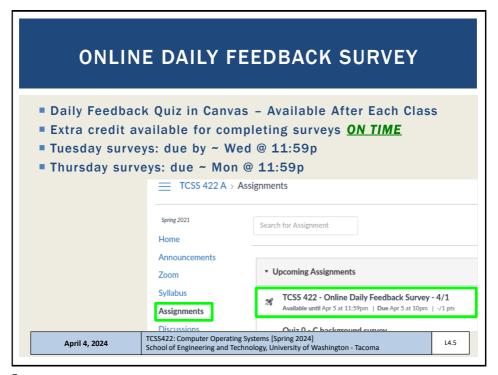
Hybrid (In-person/Zoom)

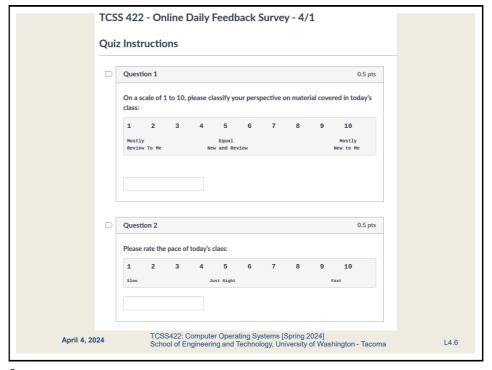
- This session will be in person in CP 229.
- Zoom will be monitored when no student is in CP 229.
- Thursdays after class until 7:00pm Hybrid (In-person/Zoom)
 - Additional office time will be held on Thursdays after class when there is high demand indicated by a busy Tuesday office hour
 - When Thursday Office Hours are planned, Zoom links will be shared via Canvas
 - Questions after class on Thursdays are always entertained even when the formal office hour is not scheduled

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MATERIAL / PACE

- Please classify your perspective on material covered in today's class (32 respondents):
- 1-mostly review, 5-equal new/review, 10-mostly new
- Average 6.56 (↑ previous 6.49)
- Please rate the pace of today's class:
- 1-slow, 5-just right, 10-fast
- Average 5.38 (↑ previous 5.31)

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FEEDBACK FROM 4/2

- I am interested in differentiating the responsibilities between the run-time stack and the memory heap. I understand the gist of these ideas but still feel unfamiliar with their specifics.
 - What data is stored on the heap?
 - What data is stored on the stack?
- What is the difference between voluntary and involuntary context switches (C/S)?
 - A voluntary C/S occurs when a process performs privileged operations such as I/O that BLOCK and WAIT for a response
 - This is considered a voluntary C/S because the user program has elected to perform the I/O and needs to WAIT anyways.
 - It's a perfect time to for the CPU to C/S and perform other work
- UNCLEAR: The processes of going from running to blocked to ready then back to running.

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FEEDBACK FROM 4/4

- I understand that using malloc() while a program is running requires using free() if we want to prevent memory leaks, but isn't it true that most modern operating systems recover the allocated memory after a program exits?
- YES, when the process ends, the operating system will claim all memory allocated for the code, stack, heap, and data segments
- If the program only runs for a short time, then it may be acceptable not to "free()" memory on the heap
- The issue is with programs that run forever (i.e. servers)
 - Web applications may "run forever"
 - if there is a memory leak in a web application, it could cause the web application server to eventually crash

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FEEDBACK - 2

- I originally thought one of the main reasons we program in C on our Virtual Machines was so that we did not accidentally use malloc() and cause permanent damage to our memory by making it nonreusable.
- When writing privileged kernel-level code, you may use "kmalloc()" which stands for "kernel malloc".
- Errors with dynamic memory allocation in the kernel may result in the corruption of the kernel's memory which is catastrophic if not recoverable
- If a user program fails, it is no big deal to the system
- If the kernel errors, the system may go down

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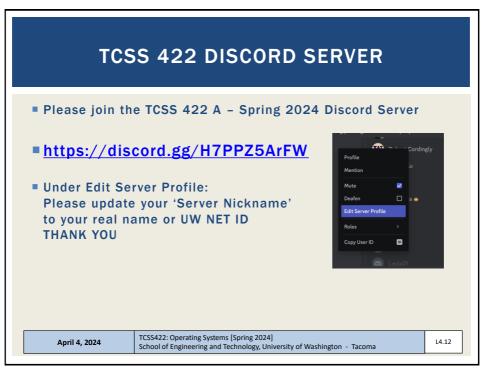
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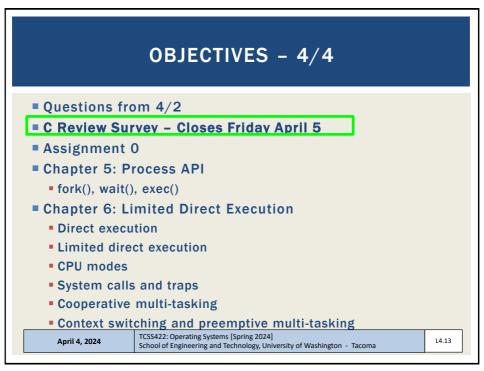
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L4.10

FEEDBACK - 3 ■ We covered context switches quickly so I wonder how exactly they are implemented and better examples of where we use them? A programmer can "use" a voluntary context switch by performing a blocking operation where the system must wait for I/O etc. In this case the CPU is not busy, and is reclaimed for some other process by the OS Otherwise the user does not <u>cause</u> or <u>enact</u> a context switch. Context switches are generated by the operating system when a process runs for more than a "time slice" which is from ~ 3 to 10 milliseconds depending how busy the system is We will cover context switches in more detail in Chapter 6 TCSS422: Operating Systems [Spring 2024] April 4, 2024 L4.11 School of Engineering and Technology, University of Washington - Tacoma

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OBJECTIVES - 4/4

- Questions from 4/2
- C Review Survey Closes Friday April 5
- Assignment 0
- Chapter 4: Linux process data structure task_struct
- Chapter 5: Process API
 - fork(), wait(), exec()
- Chapter 6: Limited Direct Execution
 - Direct execution
 - Limited direct execution
 - CPU modes
 - System calls and traps
 - Cooperative multi-tasking
 - Context switching and preemptive multi-tasking

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FEEDBACK ON ASSIGNMENT O

- In the homework, it specifies to use "non-interactive" commands. What does this mean exactly?
- An non-interactive command does not require any input from the user (i.e. from the keyboard)
- Non-interactive commands and scripts can run entirely on their own without intervention
- These commands are considered "headless" in that they don't feature a USER INTERFACE, either a GUI, or TUI
- What is a TUI?
 - *Text-based User Interface
 - TUI is also a bird

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TCSS 422 - SET VMS Request submitted for School of Engineering and Technology hosted Ubuntu 22.04 VMs for TCSS 422 - Spring 2024

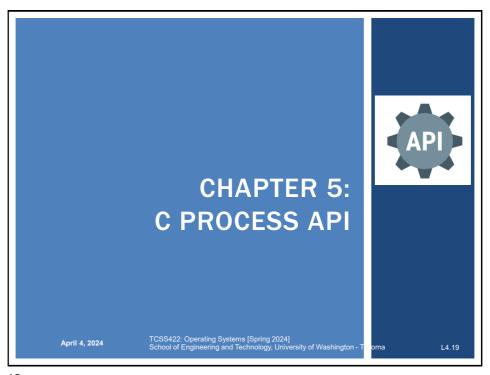
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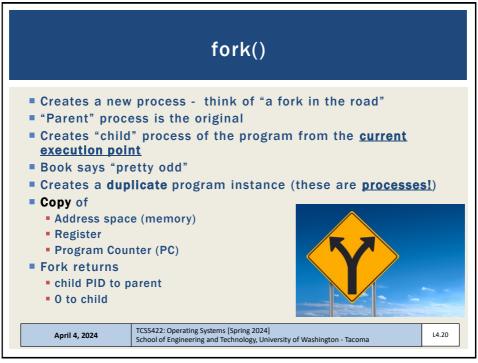
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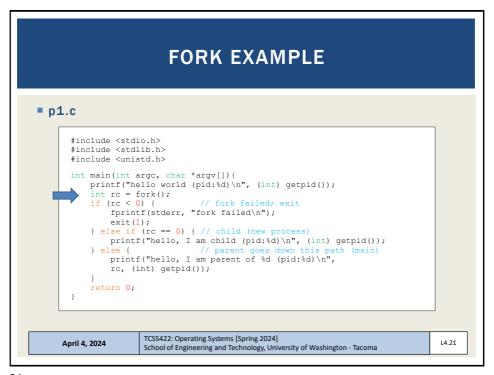
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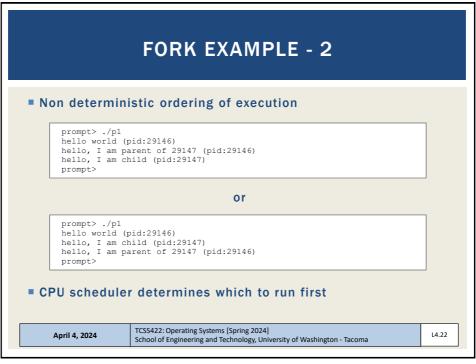
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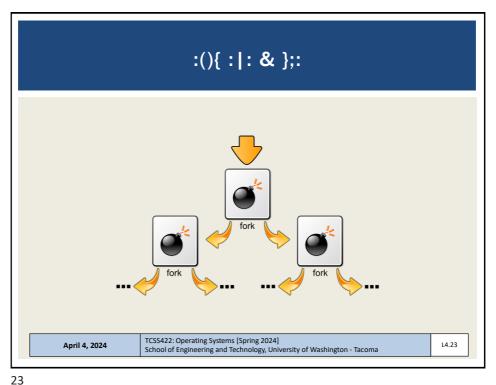
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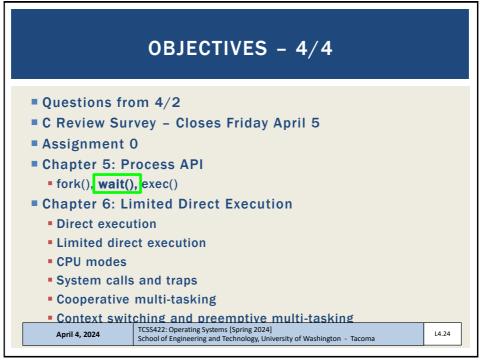


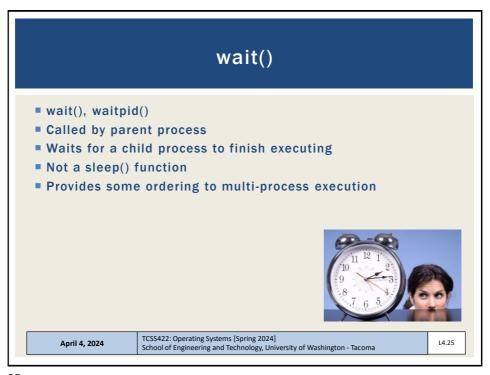




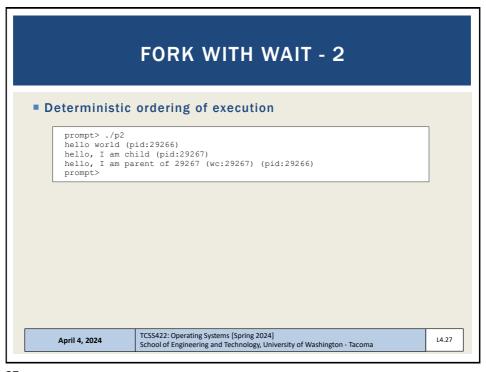


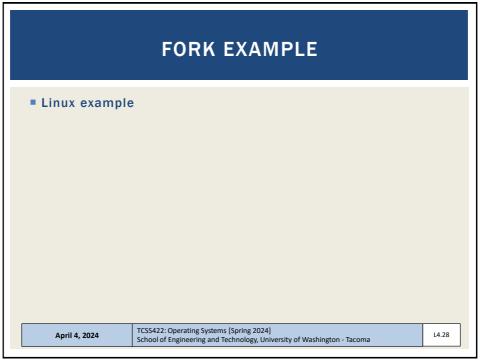






```
FORK WITH WAIT
  #include <stdio.h>
 #include <stdlib.h>
 #include <unistd.h>
 #include <sys/wait.h>
 int main(int argc, char *argv[]){
    printf("hello world (pid:%d)\n", (int) getpid());
      fprintf(stderr, "fork failed\n");
      exit(1);
} else if (rc == 0) { // child (new process)
          printf("hello, I am child (pid:%d)\n", (int) getpid());
                              // parent goes down this path (main)
         int wc = wait(NULL);
          printf("hello, I am parent of %d (wc:%d) (pid:%d) \n",
          rc, wc, (int) getpid());
      return 0;
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                                                                                   L4.26
```





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exec()

- Supports running an external program by "transferring control"
- 6 types: execl(), execlp(), execle(), execv(), execvp(), execvpe()
- execl(), execlp(), execle(): const char *arg (example: execl.c)

Provide cmd and args as individual params to the function Each arg is a pointer to a null-terminated string **ODD**: pass a variable number of args: (arg0, arg1, .. argn)

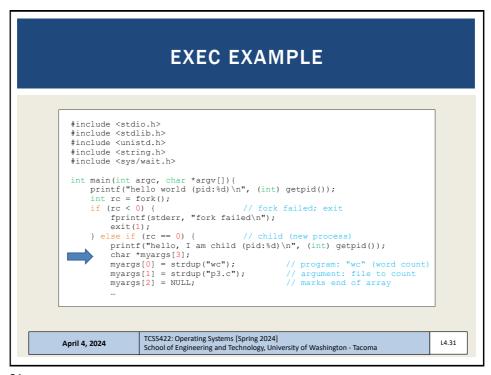
execv(), execvp(), execvpe() (example: exec.c) Provide cmd and args as an Array of pointers to strings

Strings are null-terminated First argument is name of command being executed Fixed number of args passed in

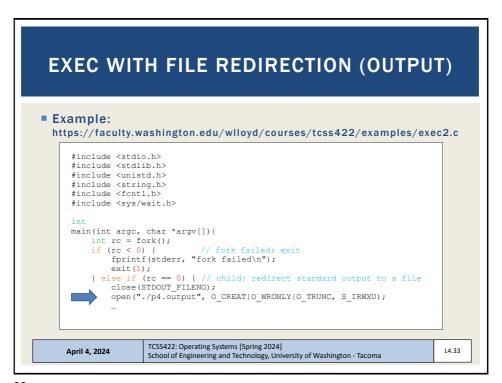
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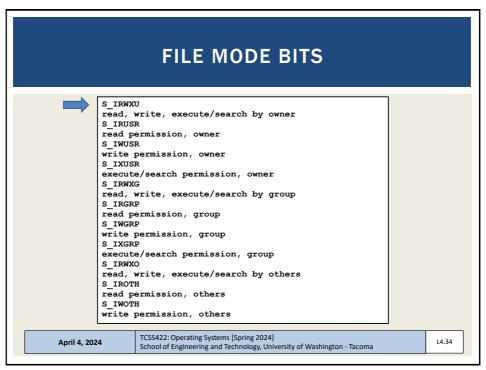
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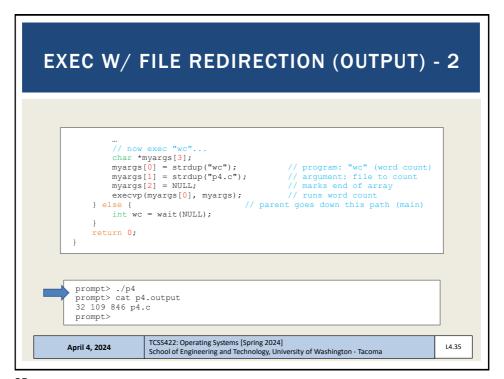
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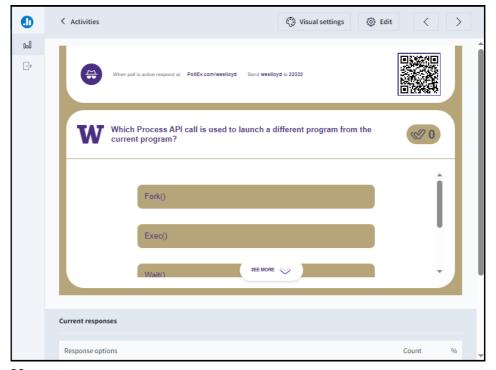


```
EXEC EXAMPLE - 2
          execvp(myargs[0], myargs); // runs word count
          printf("this shouldn't print out");
      } else {
                                    // parent goes down this path (main)
          int wc = wait(NULL);
         printf("hello, I am parent of %d (wc:%d) (pid:%d) \n",
              rc, wc, (int) getpid());
      return 0;
 prompt> ./p3
 hello world (pid:29383)
 hello, I am child (pid:29384)
29 107 1030 p3.c
 hello, I am parent of 29384 (wc:29384) (pid:29383)
 prompt>
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                                                                                 L4.32
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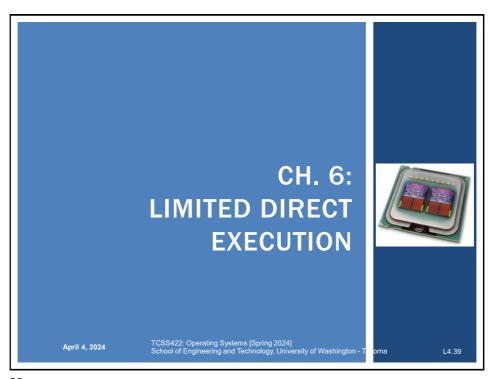


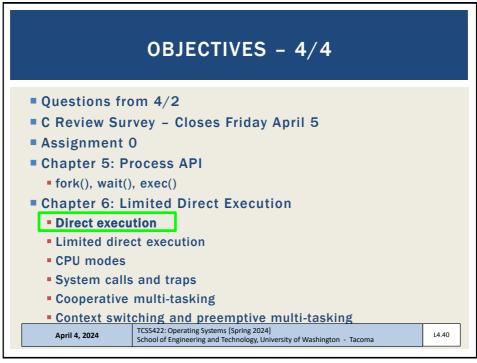


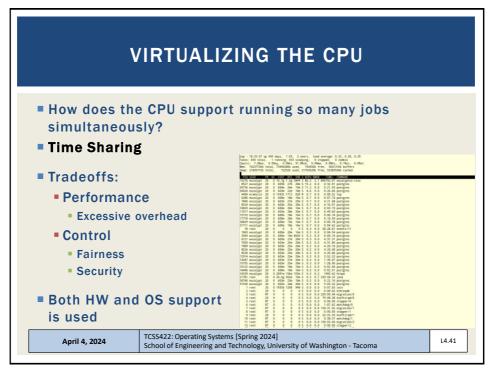
QUESTION: PROCESS API Which Process API call is used to launch a different program from the current program? (a) Fork() (b) Exec() (c) Wait() (d) None of the above (e) All of the above TCSS422: Operating Systems [Spring 2024] School of Engineering and Technology, University of Washington - Tacoma L4.37

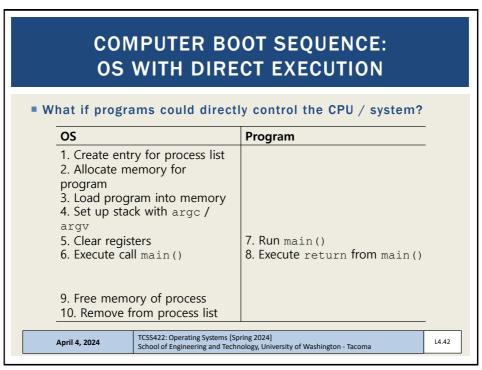
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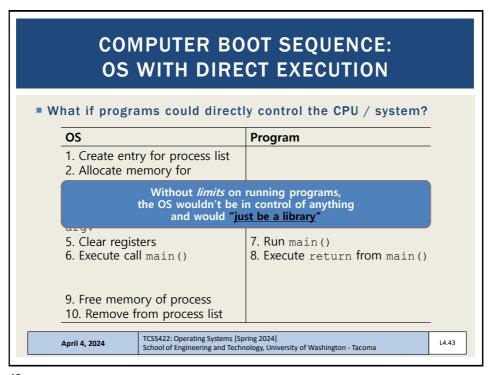
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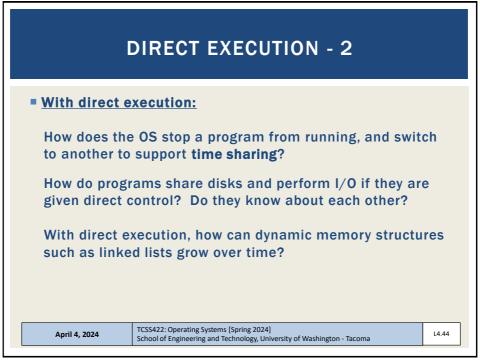


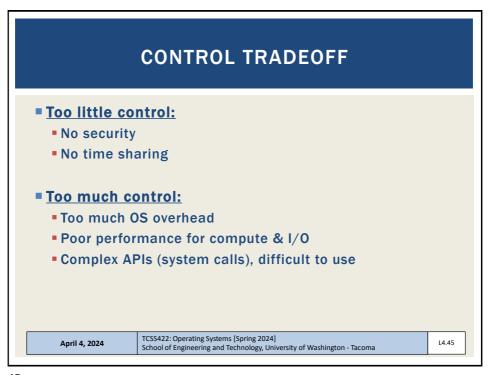


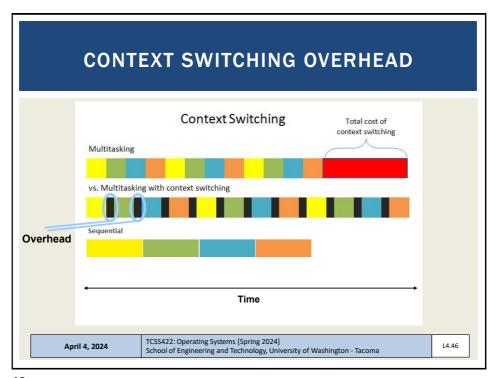




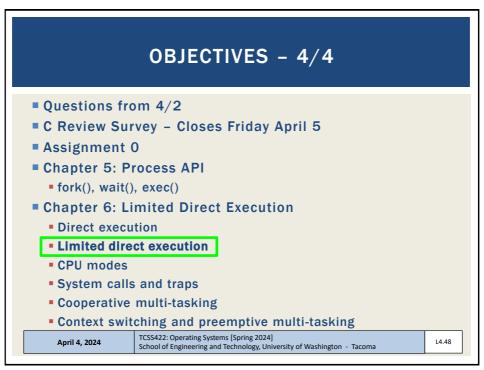












LIMITED DIRECT EXECUTION

- OS implements LDE to support time/resource sharing
- Limited direct execution means "only limited" processes can execute DIRECTLY on the CPU in trusted mode
- TRUSTED means the process is trusted, and it can do anything... (e.g. it is a system / kernel level process)
- Enabled by protected (safe) control transfer
- CPU supported context switch
- Provides data isolation

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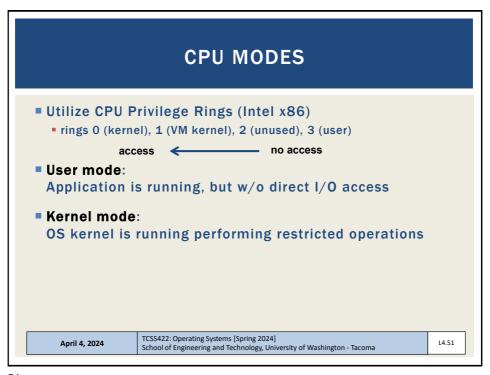
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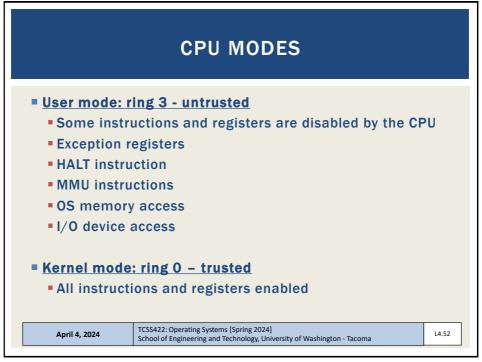
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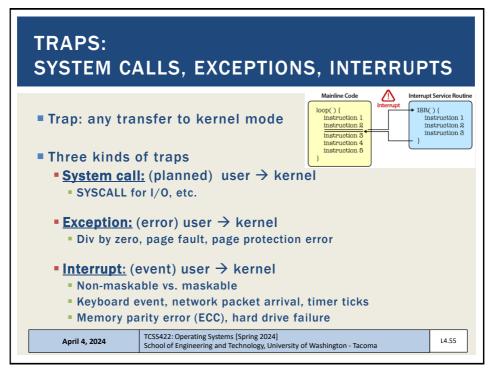


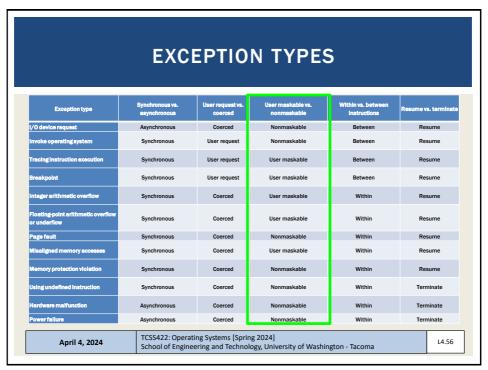


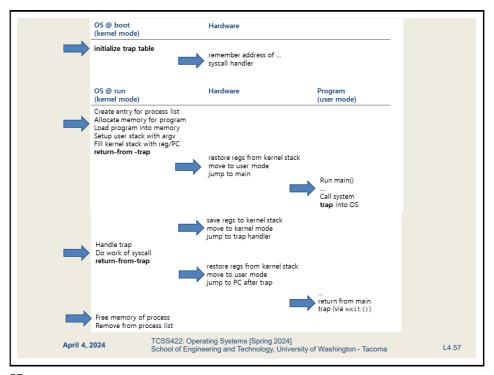
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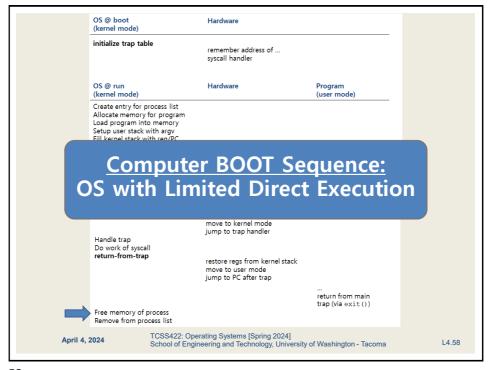
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SYSTEM CALLS Implement restricted "OS" operations Kernel exposes key functions through an API: Device I/O (e.g. file I/O) Task swapping: context switching between processes Memory management/allocation: malloc() Creating/destroying processes









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MULTITASKING

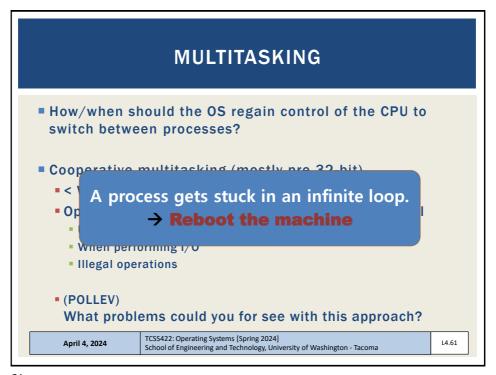
- How/when should the OS regain control of the CPU to switch between processes?
- Cooperative multitasking (mostly pre 32-bit)
 - < Windows 95, Mac OSX</p>
 - Opportunistic: running programs must give up control
 - User programs must call a special yield system call
 - When performing I/O
 - Illegal operations
 - (POLLEV)

What problems could you for see with this approach?

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QUESTION: MULTITASKING

■ What problems exist for regaining the control of the CPU with cooperative multitasking OSes?

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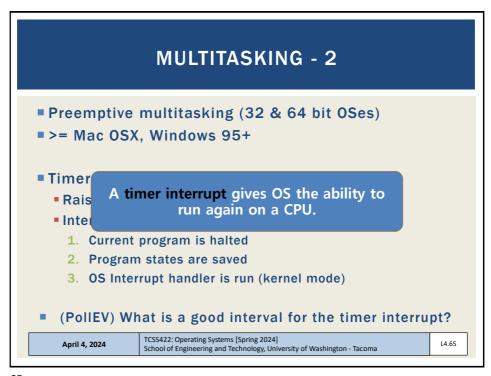
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MULTITASKING - 2

- Preemptive multitasking (32 & 64 bit OSes)
- >= Mac OSX, Windows 95+
- Timer interrupt
 - Raised at some regular interval (in ms)
 - Interrupt handling
 - 1. Current program is halted
 - 2. Program states are saved
 - 3. OS Interrupt handler is run (kernel mode)
- (PollEV) What is a good interval for the timer interrupt?

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QUESTION: TIME SLICE

For an OS that uses a system timer to force arbitrary context switches to share the CPU, what is a good value (in seconds) for the timer interrupt?

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QUESTION: TIME SLICE

- For an OS that uses a system timer to force arbitrary context switches to share the CPU, what is a good value (in seconds) for the timer interrupt?
 - Typical time slice for process execution is 10 to 100 milliseconds
 - Typical context switch overhead is (switch between processes) 0.01 milliseconds
 - 0.1% of the time slice (1/1000th)

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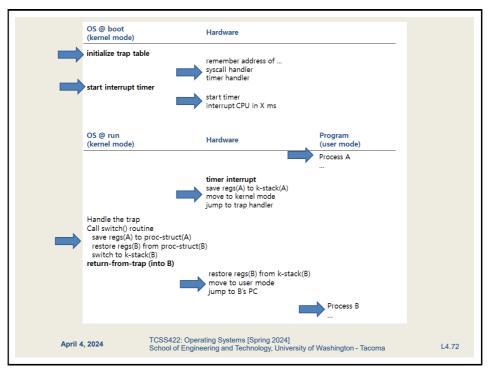
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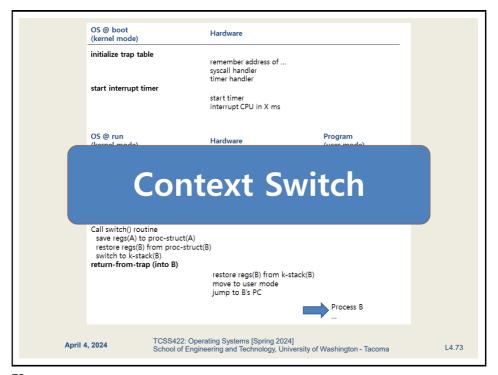
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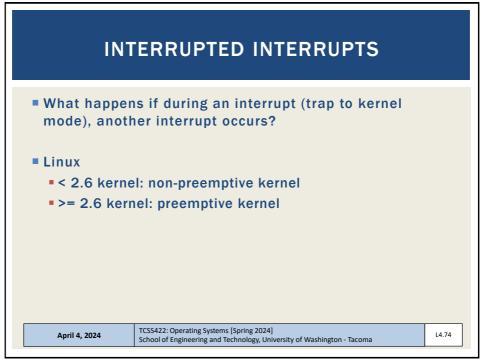
CONTEXT SWITCH Preemptive multitasking initiates "trap" into the OS code to determine: Whether to continue running the current process, or switch to a different one. If the decision is made to switch, the OS performs a context switch swapping out the current process for a new one. TCSS422: Operating Systems [Spring 2024] School of Engineering and Technology, University of Washington - Tacoma

CONTEXT SWITCH - 2 1. Save register values of the current process to its kernel stack General purpose registers PC: program counter (instruction pointer) kernel stack pointer 2. Restore soon-to-be-executing process from its kernel stack 3. Switch to the kernel stack for the soon-to-be-executing process April 4, 2024 TCSS422: Operating Systems (Spring 2024) School of Engineering and Technology, University of Washington - Tacoma

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PREEMPTIVE KERNEL Use "locks" as markers of regions of non-preemptibility (non-maskable interrupt) Preemption counter (preempt_count) begins at zero increments for each lock acquired (not safe to preempt) decrements when locks are released Interrupt can be interrupted when preempt_count=0 It is safe to preempt (maskable interrupt) the interrupt is more important

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