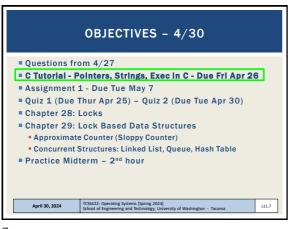
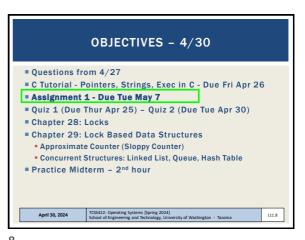
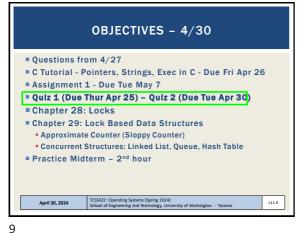


FEEDBACK FROM 4/25 Which APIs are user calls, and which are kernel calls? From Chapter 29: pthread_create() pthread_join() pthread_mutex_lock() pthread_mutex_unlock() pthread_mutex_trylock() pthread_mutex_timelock() pthread_cond_wait() pthread_cond_wait_signal() pthread_cond_wait_broadcast() April 30, 2024 L11.6

Slides by Wes J. Lloyd







QUIZ 2

Canvas Quiz - Practice CPU Scheduling Problems

Posted in Canvas
Unlimited attempts permitted
Provides CPU scheduling practice problems
FIFO, SJF, STCF, RR, MLFQ (Ch. 7 & 8)
Multiple choice and fill-in the blank
Quiz automatically scored by Canvas
Please report any grading problems

Due Tuesday April 30th at 11:59pm

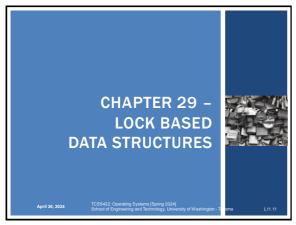
Link:
https://canvas.uw.edu/courses/1728244/quizzes/2030525

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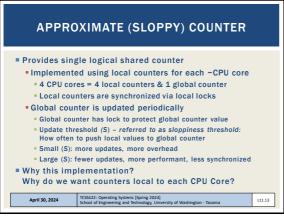
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OBJECTIVES - 4/30

Questions from 4/27
C Tutorial - Pointers, Strings, Exec in C - Due Fri Apr 26
Assignment 1 - Due Tue May 7
Quiz 1 (Due Thur Apr 25) - Quiz 2 (Due Tue Apr 30)
Chapter 28: Locks
Chapter 29: Lock Based Data Structures
Approximate Counter (Sloppy Counter)
Concurrent Structures: Linked List, Queue, Hash Table
Practice Midterm - 2nd hour

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APPROXIMATE COUNTER - MAIN POINTS

Idea of the Approximate Counter is to RELAX the synchronization requirement for counting
Instead of synchronizing global count variable each time: counter=counter+1
Synchronization occurs only every so often: e.g. every 1000 counts

Relaxing the synchronization requirement drastically reduces locking API overhead by trading-off split-second accuracy of the counter

Approximate counter: trade-off accuracy for speed
It's approximate because it's not so accurate (until the end)

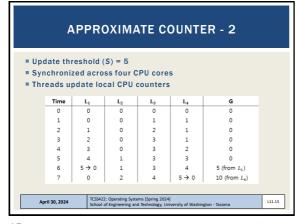
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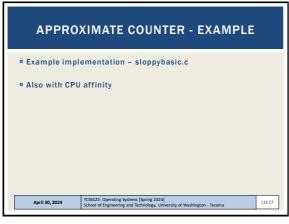
THRESHOLD VALUE S

■ Consider 4 threads increment a counter 1000000 times each
■ Low S → What is the consequence?
■ High S → What is the consequence?

■ Is 15 32 64 128 256 5121024 Approximation Factor (s)

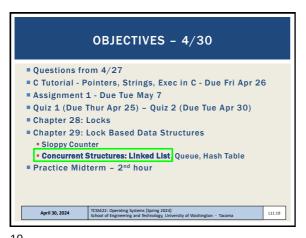
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Which of the following is NOT a problem as a result of having a low S-value for the approximate counter (Sloppy Counter) threshold?

The counter overhead is very high.
The counter implementation performs a very large number of LOCK/UNLOCK API calls.
The global counter value is highly accurate.
The counter performs very few local to global counter plates.
None of the above



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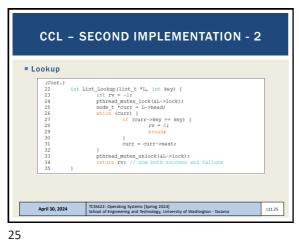
```
CONCURRENT LINKED LIST

First Implementation:
Lock everything inside Insert() and Lookup()
If malloc() fails lock must be released
Research has shown "exception-based control flow" to be error prone
Mow of Linux OS bugs occur in rarely taken code paths
Unlocking in an exception handler is considered a poor coding practice
There is nothing specifically wrong with this example however

Second Implementation ...
```

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CONCURRENT LINKED LIST PERFORMANCE Using a single lock for entire list is not very performant Users must "wait" in line for a single lock to access/modify any item ■ Hand-over-hand-locking (lock coupling) Introduce a lock for each node of a list Traversal involves handing over previous node's lock, acquiring the next node's lock... Improves lock granularity Degrades traversal performance Consider hybrid approach • Fewer locks, but more than 1 Best lock-to-node distribution? April 30, 2024 L11.26

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```
OBJECTIVES - 4/30
■ Questions from 4/27
C Tutorial - Pointers, Strings, Exec in C - Due Fri Apr 26
Assignment 1 - Due Tue May 7
Quiz 1 (Due Thur Apr 25) - Quiz 2 (Due Tue Apr 30)
Chapter 28: Locks
■ Chapter 29: Lock Based Data Structures

    Sloppy Counter

    Concurrent Structures: Linked List, Queue Hash Table

■ Practice Midterm - 2<sup>nd</sup> hour
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    April 30, 2024
                                                                      L11.27
```

MICHAEL AND SCOTT CONCURRENT QUEUES Improvement beyond a single master lock for a queue (FIFO) ■ Two locks: • One for the head of the queue • One for the tall Synchronize enqueue and dequeue operations Add a dummy node • Allocated in the queue initialization routine Supports separation of head and tail operations Items can be added and removed by separate threads at the TCSS422: Operating Systems [Spring 2024] School of Engineering and Technology, University of Washington - Tacoma April 30, 2024 L11.28

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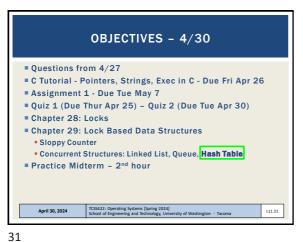
```
CONCURRENT QUEUE
Remove from queue
                                               typedef struct __node_t {
    int value;
    struct __node_t *next;
}
                                             typedef struct __queue_t {
    node_t *head;
    node_t *tail;
    pthread_mutex_t headLock;
    pthread_mutex_t tailLock;
} queue_t;
                           10
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                                              void Queue_Init(queue_t *q) {
    node_t *tmp = malloc(sizeof(node_t));
    tmp-react = WULL;
    q->head = q->tail = tmp;
    pthread_mutex_init(sq->headLock, NULL);
    pthread_mutex_init(sq->taillock, NULL);
                                                   TCSS422: Operating Systems [Spring 2024]
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                                                                                                                                                                                                        L11.29
```

CONCURRENT QUEUE - 2 Add to queue void Queue_Enqueue(queue_t *q, int value) {
 node_t *tmp = malloc(sizeof(node_t));
 assert(tmp != NULL); tmp->value = value; tmp->next = NULL; pthread_mutex_lock(&q->tailLock);
q->tail->next = tmp;
q->tail = tmp;
pthread_mutex_unlock(&q->tailLock); TCSS422: Operating Systems [Spring 2024] School of Engineering and Technology, University of Washington - Tacoma April 30, 2024 L11.30

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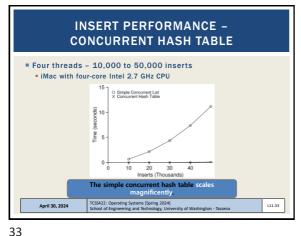
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L11.5



CONCURRENT HASH TABLE Consider a simple hash table Fixed (static) size Hash maps to a bucket Bucket is implemented using a concurrent linked list One lock per hash (bucket) Hash bucket is a linked lists April 30, 2024 L11.32

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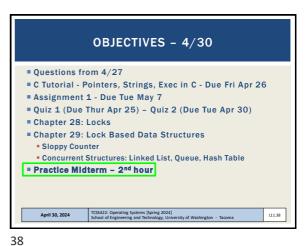
CONCURRENT HASH TABLE #define BUCKETS (101) void Hash_Init(hash_t *H) { int i;
for (i = 0; i < BUCKETS; i++) {
 List_Init(6H->lists[i]); 10 11 12 13 14 15 16 17 18 19 20 21 22 int Hash_Insert(hash_t *H, int key) {
 int bucket = key % BUCKETS;
 return List_Insert(&H->lists[bucket], key); int Hash_Lookup(hash_t *H, int key) {
 int bucket = key % BUCKETS;
 return List_Lookup(sH->lists[bucket], key); April 30, 2024 L11.34

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```
Which is a major advantage of using concurrent data
                 structures in your programs?
  Locks are encapsulated within data
structure code ensuring thread safety
    Lock granularity tradeoff already
    optimized inside data structurev
    Multiple threads can more easily
                       share data
                   All of the above
                None of the above
```

LOCK-FREE DATA STRUCTURES Lock-free data structures in Java Java.util.concurrent.atomic package AtomicBoolean AtomicInteger AtomicIntegerArray AtomicIntegerFieldUpdater AtomicLong AtomicLongArray AtomicLongFieldUpdater AtomicReference See: https://docs.oracle.com/en/java/javase/11/docs/api/ java.base/java/utll/concurrent/atomic/package-summary.html April 30, 2024 TCSS422: Operating Systems [Spring 2024] School of Engineering and Technology, Uni







MIDTERM ■ Thursday May 2nd ■ In Class in BHS 106 (2.0 hrs 3:40 - 5:40p) ■ Test designed to take less than 2 hours ■ Two pages of notes, double-sided, any-size paper permitted No book, other notes, cell phones, or internet ■ Basic calculators OK ■ Individual work Coverage: all content up through Chapter 29 Preparation: Practice quiz: Quiz 2: CPU scheduling (posted) Auto grading w/ multiple attempts allowed as study aid Practice - second hour of lecture Series of problems presented with some time to solve Will then work through solutions April 30, 2024 L11.40

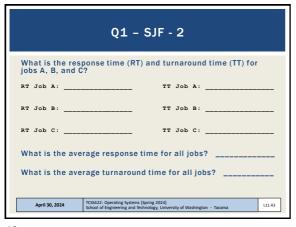
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FIFO EXAMPLE Operation of CPU schedulers can be visualized with timing graphs. ■ The graph below depicts a FIFO scheduler where three jobs arrive in the sequence A, B, C, where job A runs for 10 time slices, job B for 5 time slices, and job C for 10 time slices. FIFO | AAAAAAAAABBBBBCCCCCCCCC 0 10 April 30, 2024 L11.41

Q1- SHORTEST JOB FIRST (SJF) **SCHEDULER** Draw a scheduling graph for the SJF scheduler without preemption for the following jobs. Draw vertical lines for key events and be sure to label the X-axis times as in the example. Job Length **Arrival Time** T=0 25 T=5 С T=10 15 SJF April 30, 2024 L11.42

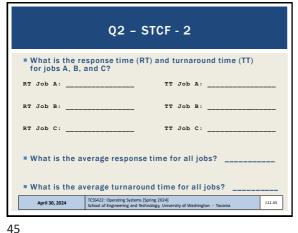
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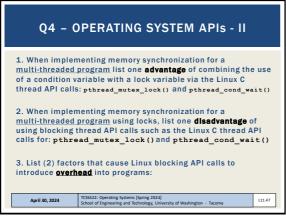
Q2 - SHORTEST TIME TO COMPLETION FIRST (STCF) SCHEDULER Draw a scheduling graph for the STCF scheduler with preemption for the following jobs Draw vertical lines for key events and be sure to label the X-axis times as in the example. Job **Arrival Time** Job Length Α T=025 R T=5 10 С T=10 CPU April 30, 2024 L11.44

43 44



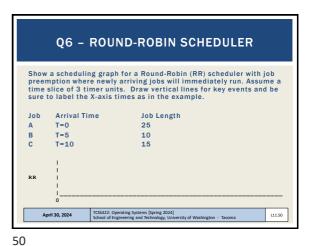
Q3 - OPERATING SYSTEM APIS 1. Provide a definition for what is a blocking API call 2. Provide a definition for a non-blocking API call 3. Provide an example of a blocking API call. Consider APIs used to manage processes and/or threads. 4. Provide an example of a non-blocking API call. Consider APIs used to manage processes and/or threads. TCSS422: Operating Systems [Spring 2024] School of Engineering and Technology, University of Washington - Tacoma April 30, 2024 L11.46

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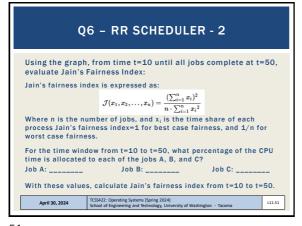


Q5 - PERFECT MULTITASKING **OPERATING SYSTEM** In a perfect-multi-tasking operating system, every process of the same priority will always receive exactly $1/n^{\text{th}}$ of the available CPU time. Important CPU improvements for multi-tasking include: (1) fast context switching to enable jobs to be swapped in-and-out of the CPU very quickly, and (2) the use of a timer interrupt to preempt running jobs without the user voluntarily yielding the CPU. These innovations have enabled major improvements towards achieving a coveted "Perfect Multi-Tasking System". List and describe two challenges that remain complicating the full realization of a Perfect Multi-Tasking Operating System. In other words, what makes it very difficult for all jobs (for example, 10 jobs) of the same priority to receive **EXACTLY** the same runtime on the CPU? Your description must explain why the challenge is a problem for achieving perfect multi-tasking. TCSS422: Operating Systems [Spring 2024] School of Engineering and Technology, University of Washington - Tacoma April 30, 2024 L11.48





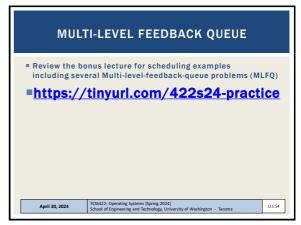
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Q6 - II $\mathcal{J}(x_1,x_2,\ldots,x_n) = rac{(\sum_{i=1}^n x_i)^2}{n \cdot \sum_{i=1}^n x_i^2}$

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Q7 - SLOPPY COUNTER Below is a tradeoff space graph similar to those we've shown in class. Based on the sloppy counter threshold (S), add numbers on the left or right side of the graph for each of the following tradeoffs: 1. High number of Global Updates 2. High Performance 3. High Overhead 4. High Accuracy 5. Low number of Global Updates 6. Low Performance 7. Low Overhead 8. Low Accuracy Low sloppy threshold (S) High sloppy threshold (S) TCSS422: Operating Systems [Spring 2024] School of Engineering and Technology, University of Washington - Tacoma April 30, 2024 L11.53 53



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