

TCSS 372A
Computer Architecture
Fall 2007

Class Times: TTH 4:30-6:45 CP 108
Supporting Laboratory: Cherry Parkes 206D

Website: <http://faculty.washington.edu/lcrum>

Instructor:	<i>Professor Larry A. Crum</i>
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Office Hours:	MW 9:00-10:20, TH 3:30-4:20, and by Appointment <i>Appointment recommended for all times.</i>
Text:	Computer Organization & Architecture - Designing for Performance Seventh Edition (2006) Williams Stallings Pearson Prentice Hall ISBN-10: 013-185644-8, ISBN-13: 978-013-185644-8 Website: http://williamstallings.com/COA/COA7e.html

TCSS 372 Computer Architecture Catalog Description:

TCSS 372A covers the micro architecture level of machine design and advanced architecture features for performance enhancement. Subjects include I/O, bus, memory and CPU design, hardware support for operating systems, CISC/RISC architectures, embedded systems, and parallelism. Extends the understanding of systems programming. (5 credit hours)

Prerequisites: TCSS 371; TCSS 342.

Simulator:

Logic Works™ 5 Interactive Circuit Design Software, Capilano Computing, Pearson Prentice hall, 2004

References:

Introduction to Computing Systems, from bits & gates to C & beyond (2nd Edition), Yale N. Patt & Sanjay J. Patel, McGraw Hill, 2004

Computer Architecture, Fourth Edition: A Quantitative Approach, John L. Hennessy, David A. Patterson, Morgan Kaufmann, 2006 (or Third Edition)

Computer Organization and Design: The Hardware/Software Interface (3rd Edition), John L. Hennessy, David A. Patterson, Morgan Kaufmann, 2004

Digital Design: Principles and Practices Package (4th Edition), John F. Wakerly, Prentice hall, 2005

The Art of Electronics (2nd Edition), Paul Horowitz and Winfield Hill, Cambridge press, 1989

Grading:

Midterms (40%) Final Exam (25%), Homework (15%), Laboratory (15%), Class Participation (5%)

Characteristics of an A student – Work is consistently outstanding in quality and displays particular insight and creativity. It is consistently presented with exceptional clarity and completeness. (S)he shows academic leadership in class discussions and contributes to others understanding and learning.

Characteristics of a B student – Work is consistently complete, the predominance of it is correct, it shows understanding of the material, and it is presented professionally and clearly. (S)he makes regular contributions to class discussions. (S)he is well prepared to use the material in the next course.

Characteristics of a C student – Work is basically complete and correct, and it is presented coherently and completely. (S)he is prepared to use the material in the next course but will likely need additional study in the area. (S)he participates in class discussions.

Plagiarism:

Students are encouraged to collaborate regularly with colleagues to gain a deep understanding of the material, and to gain insight on options for problem solutions. Solutions submitted are to display individual knowledge and accomplishment. Any significant contribution in a submission must be acknowledged and the responsible student or source given due credit. See <http://depts.washington.edu/grading/issue1/honesty.htm>

Laboratory:

We will complete approximately three hands-on projects in our laboratory (CP 206D). You will have access to the lab anytime of your choosing. See **Laboratory Etiquette reference.**

Schedule (Subject to Minor Adjustment):

Week	Topics Covered	Preparations for Class (Chapters)	Homework/Projects (Homework due Tuesday after assignment)
1: Sep 27	Introduction, Buses	Chap 3.3 – 3.7	3.8, 11, 12, 15, 19
2: Oct 2 Oct 4	Cache memory	Chap 4	4.1, 5, 11, 23
3: Oct 9 Oct 11	Internal Memory	Chap 5	5.2, 4, 7, 8, 11, 12 <i>Lab Project 1</i>
4: Oct 16 Oct 18	External memory I/O	Chap 6 Chap 7	6.3, 4
5: Oct 23 Oct 25	Midterm 1 (Chapters 3 thru 6) I/O		7.13, 18
6: Oct 30 Nov 1	Operating System Support	Chap 8	8.8, 15, 17 <i>Lab Project 2</i>
7: Nov 6 Nov 8	Pipelining RISC Processors	Chap 12 Chap 13	12.4a, 12.7, 12.10 13.6, 13.7
8: Nov 13 Nov 15	Superscalar Processors	Chap 14	14.1, 14.5
9: Nov 20 Nov 22	Midterm 2 (Chapters 7 thru 14) <i>Thanksgiving – No Class</i>		
10: Nov 27 Nov 29	IA-64 Processor Control units, Microprogramming	Chap 15 Chap 16, 17	15.7, 15.8, 15.10 16.2 (Load, Add, Jmp if), 16.3, 17.4, 17.8
11: Dec 4 Dec 6	Parallel Processing Review & Wrap up	Chap 18	
12: Dec 11 Dec 13	<i>No Class Scheduled</i> Final Exam 4:30 – 6:45		

Safety Escorts: Safety escorts are available to accompany you to your vehicle 24 hours a day, 7 days a week. Call Campus Safety at **2-4416** from a campus phone, and **253-692-4416** from a non-campus phone.

Reporting Emergencies: From campus phones, report emergencies by dialing **9-911** and state the T-number that is on a sticker on the phone; from non-campus phones dial **911**. Building location numbers are posted on all buildings. For assistance with non-emergencies call Campus Safety at **2-4416** from a campus phone, and **253-692-4416** from a non-campus phone.

Emergency Procedures: In case of emergency, follow your professor's instructions. When an alarm sounds, evacuate the building immediately. MATT, CP, WG, GWP, and BB buildings assemble in the Cragle Parking Lot south of the library. BHS, WCG, and DOU buildings assemble near the transit station next to the Pinkerton Building on Broadway (across from Spaghetti Factory). Pinkerton occupants go to the convention center parking lot north of Pinkerton. For more information about emergency procedures and information, please go to: <http://www.tacoma.washington.edu/safety/>

Disability Support: If you would like to request academic accommodations due to a temporary or permanent disability, contact Lisa Tice, Manager for Disability Support Services (DSS) in the Mattress Factory Bldg, Suite 206. An appointment can be made through the front desk of Student Affairs (692-4400), through Student Development and Success (692-4501), by phoning Lisa directly at 692-4493 (voice) or 692-4413 (TTY), or by e-mail ltice@u.washington.edu. Appropriate accommodations are arranged after you've conferred with the DSS Manager and presented the required documentation of your disability to DSS.